Channel Isolated Analog-Digital Converter Module Channel Isolated Analog-Digital Converter Module (With Signal Conditioning Function)

**MITSUBISHI** 

User's Manual



Mitsubishi Programmable Controller



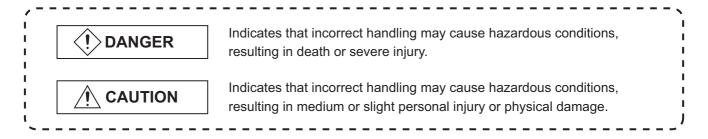
Q68AD-G Q66AD-DG GX Configurator-AD (SW2D5C-QADU-E)



(Always read these instructions before using this equipment.)

Before using this product, please read this manual and the relevant manuals introduced in this manual carefully and pay full attention to safety to handle the product correctly.

The instructions given in this manual are concerned with this product. For the safety instructions of the programmable controller system, please read the user's manual for the CPU module to use. In this manual, the safety instructions are ranked as "DANGER" and "CAUTION".



Note that the <u>A</u>CAUTION level may lead to a serious consequence according to the circumstances. Always follow the instructions of both levels because they are important to personal safety.

Please store this manual in a safe place and make it accessible when required. Always forward it to the end user.

# [DESIGN PRECAUTION]

# <!> DANGER

■ Do not write data into the "system area" of the buffer memory of intelligent function modules. Also, do not use any "prohibited to use" signals as an output signal to an intelligent function module from the programmable controller CPU.

Writing data into the "system area" or outputting a signal for "prohibited to use" may cause a malfunction of the programmable controller system.

# **CAUTION**

■ Do not bunch the control wires or communication cables with the main circuit or power wires, or install them close to each other.

They should be installed 100mm(3.9inch) or more from each other.

Not doing so could result in noise that may cause malfunction.

# [INSTALLATION PRECAUTIONS]

# **CAUTION**

- Use the programmable controller in an environment that meets the general specifications contained in the user's manual of the CPU module to use.
  - Using this programmable controller in an environment outside the range of the general specifications may cause electric shock, fire, malfunction, and damage to or deterioration of the product.
- While pressing the installation lever located at the bottom of module, insert the module fixing tab into the fixing hole in the base unit until it stops. Then, securely mount the module with the fixing hole as a supporting point.
  - For the Q66AD-DG, secure the module with fixing brackets after installation to the base unit.

When using the Q68AD-G in an environment where they are frequent vibrations, screw the module to the base unit after installation.

- Tighten the screws within the range of specified torque.
  - If the screws are loose, it may cause the module to fallout, short circuits, or malfunction.
  - If the screws are tightened too much, it may cause damage to the screw and/or the module, resulting in fallout, short circuits or malfunction.
- Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.
  - Not doing so may cause damage to the module.
  - In the system where a CPU module supporting the online module change is used and on the MELSECNET/H remote I/O stations, modules can be replaced online (during energizing).
  - However, there are some restrictions on replaceable modules and the replacement procedures are predetermined for each module.
  - For details, refer to the chapter of the online module change in this manual.
- Do not directly touch the conductive area or electronic components of the module. Doing so may cause malfunction or failure in the module.

# [WIRING PRECAUTIONS]

# **CAUTION**

- Always ground the FG terminal.
  Not doing so can cause an electric shock or malfunction.
- When turning on the power and operating the module after wiring is completed, always attach the terminal cover that comes with the product.

There is a risk of electric shock if the terminal cover is not attached.

- Tighten the terminal screws within the range of specified torque.
  If the terminal screws are loose, it may result in short circuits or malfunction.
  If the terminal screws are tightened too much, it may cause damage to the screw and/or the module, resulting in short circuits or malfunction.
- Be careful not to let foreign matter such as sawdust or wire chips get inside the module. They may cause fires, failure or malfunction.
- The top surface of the module is covered with protective film to prevent foreign objects such as cable offcuts from entering the module when wiring.

Do not remove this film until the wiring is complete.

Before operating the system, be sure to remove the film to provide adequate ventilation.

# [STARTING AND MAINTENANCE PRECAUTIONS]

# **CAUTION**

Do not disassemble or modify the modules.
 Doing so could cause failure, malfunction injury or fire.

Be sure to shut off all phases of the external power supply used by the system before mounting or removing the module.

Not doing so may cause failure or malfunction of the module.

In the system where a CPU module supporting the online module change is used and on the MELSECNET/H remote I/O stations, modules can be replaced online (during energizing).

However, there are some restrictions on replaceable modules and the replacement procedures are predetermined for each module.

For details, refer to the chapter of the online module change in this manual.

■ Do not install/remove the module to/from the base unit more than 50 times after the first use of the product. (IEC 61131-2 compliant)

Failure to do so may cause malfunction.

- Do not touch the connector while the power is on. Doing so may cause malfunction.
- Be sure to shut off all phases of the external power supply before cleaning or retightening the terminal screws or module fixing screws.

Not doing so may cause failure or malfunction of the module.

If the screws are loose, it may cause the module to fallout, short circuits, or malfunction.

If the screws are tightened too much, it may cause damages to the screws and/or the module, resulting in the module falling out, short circuits or malfunction.

Always make sure to touch the grounded metal to discharge the electricity charged in the body, etc., before touching the module.

Failure to do so may cause a failure or malfunctions of the module.

# [DISPOSAL PRECAUTIONS]



When disposing of this product, treat it as industrial waste.

#### **REVISIONS**

\* The manual number is given on the bottom left of the back cover.

Print Date	*Manual Number	Revision
Oct., 2006	SH (NA)-080647ENG-A	First edition
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Japanese Manual Version SH-080645-F

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#### **INTRODUCTION**

Thank you for purchasing the MELSEC-Q series programmable controller.

Before using the equipment, please read this manual carefully to develop full familiarity with the functions and performance of the Q series programmable controller you have purchased, so as to ensure correct use.

Please forward a copy of this manual to the end user.

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#### **ABOUT MANUALS**

The following manuals are also related to this product.

If necessary, order them by quoting the details in the tables below.

#### Related Manuals

Manual Name	Manual Number (Model Code)
GX Developer Version 8 Operating Manual  Describes the methods of using GX Developer to create a program and print out, monitor, and debug the program.  (Sold separately)	SH-080373E (13JU41)
GX Developer Version 8 Operating Manual (Function Block)  Describes the methods of using GX Developer to create a function block and print out the function block.  (Sold separately)	SH-080376E (13JU44)



If you would like to obtain a manual individually, printed matters are available separately. Order the manual by quoting the manual number on the table above (model code).

#### **COMPLIANCE WITH THE EMC AND LOW VOLTAGE DIRECTIVES**

#### (1) For programmable controller system

To configure a system meeting the requirements of the EMC and Low Voltage Directives when incorporating the Mitsubishi programmable controller (EMC and Low Voltage Directives compliant) into other machinery of equipment, refer to Chapter 9 "EMC AND LOW VOLTAGE DIRECTIVES" of the QCPU User's Manual (Hardware Design, Maintenance and Inspection).

The CE mark, indicating compliance with the EMC and Low Voltage Directives, is printed on the rating plate of the programmable controller.

#### (2) For the product

No additional measures are necessary for the compliance of this product with the EMC and Low Voltage Direcitives.

#### **ABOUT THE GENERIC TERMS AND ABBREVIATIONS**

Unless otherwise specified, this manual uses the following general terms and abbreviations.

Abbreviation/general terms	Description of the abbreviation/general terms		
A/D converter module	Generic term for Q68AD-G and Q66AD-DG		
DOS/V personal computer	IBM PC/AT® or compatible computer with DOS/V.		
	Generic product name for the SWnD5C-GPPW-E, SWnD5C-GPPW-EA, SWnD5C-GPPW-EV and		
GX Developer	SWnD5C-GPPW-EVA. ("n" is 4 or greater.)		
	"-A" and "-V" denote volume license product and upgraded product respectively.		
GX Configurator-AD	Generic term for analog-digital converter module setting and monitor tool GX Configurator-AD (SW2D5C-QADU-E)		
	Generic term for Q00JCPU, Q00CPU, Q01CPU, Q02CPU, Q02HCPU, Q06HCPU, Q12HCPU,		
QCPU (Q mode)	Q25HCPU, Q02PHCPU, Q06PHCPU, Q12PHCPU, Q25PHCPU, Q12PRHCPU, Q25PRHCPU,		
QCFO (Q IIIOde)	Q02UCPU, Q03UDCPU, Q04UDHCPU, Q06UDHCPU, Q13UDHCPU, Q26UDHCPU,		
	Q03UDECPU, Q04UDEHCPU, Q06UDEHCPU, Q13UDEHCPU and Q26UDEHCPU.		
Process CPU	Generic term for Q02PHCPU, Q06PHCPU, Q12PHCPU and Q25PHCPU.		
Personal computer	Generic term for DOS/V personal computer		
Industrial shipment setting	Generic term for analog input ranges 0 to 10V, 0 to 5V, 1 to 5V, -10 to 10V, 0 to 20mA and 4 to 20mA		
FB	Abbreviation of function block.		
	Generic term for the following:		
	Microsoft® Windows Vista® Home Basic Operating System,		
	Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Home Premium Operating System,		
Windows Vista®	Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Business Operating System,		
	Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Ultimate Operating System,		
	Microsoft® Windows Vista® Enterprise Operating System		
	Generic term for the following:		
Windows <sup>®</sup> XP	Microsoft® Windows® XP Professional Operating System,		
	Microsoft® Windows® XP Home Edition Operating System		

#### **PRODUCTS STRUCTURE**

The product structure of this product is given in the table below.

Model code	Model code Product name	
Q68AD-G	Type Q68AD-G Channel Isolated Analog-Digital Converter Module	1
Q66AD-DG	Type Q66AD-DG Channel Isolated Analog-Digital Converter Module (with Signal Conditioning Function)	1
SW2D5C-QADU-E	GX Configurator-AD Version 2 (1-license product)(CD-ROM)	1
SW2D5C-QADU-EA	GX Configurator-AD Version 2 (Multiple-license product)(CD-ROM)	1



### 1 OVERVIEW

This User's Manual describes the specifications, handling and programming methods for the type Q68AD-G channel isolated analog-digital converter module (hereinafter referred to as the Q68AD-G) and type Q66AD-DG channel isolated analog-digital converter module (with signal conditioning function) (hereinafter referred to as the Q66AD-DG), which are used with the MELSEC-Q series CPU modules.

The Q66AD-DG is exclusively used for current input.

In this manual, the Q68AD-G and Q66AD-DG are collectively referred to as the A/D converter modules.

#### 1.1 Features

#### (1) Channel isolated

The channels are isolated.

The Q66AD-DG is also isolated between the external supply power and channels.

#### (2) Multi-channel analog input is available.

- (a) By using a single Q68AD-G, analog voltage or current inputs of 8 points (8 channels) are available.
- (b) A single Q66AD-DG allows connection of 2-wire transmitters of 6 points (6 channels).

It has an input range for analog current, in addition to connections to 2-wire transmitters.

#### (3) Power supply to 2-wire transmitter (Q66AD-DG only)

Supplying power to the 2-wire transmitter, the Q66AD-DG does not require the power supply for the 2-wire transmitter.

Supply power can be switched ON/OFF channel-by-channel by the A/D conversion enable/disable setting.

# (4) Module protection provided by short-circuit protection circuit (Q66AD-DG only)

If an excessive current flows into the module due to a short circuit of the wiring, the short-circuit protection circuit limits the current to within 25 to 35mA, protecting the module.

#### (5) Analog input check by check terminals (Q66AD-DG only)

Measurement of a voltage at the check terminals allows the mA of the 2-wire transmitter output to be checked without the wiring being disconnected.

#### (6) High accuracy

The reference accuracy  $^{*1}$  is as high as  $\pm 0.1\%$  and the temperature coefficient  $^{*2}$  is as high as  $\pm 71.4$ ppm/°C.

- \*1 Accuracy of offset/gain setting at ambient temperature
- \*2 Accuracy per temperature change of 1°C

Example) Accuracy when the temperature varies from 25 to 30°C 0.1% (reference accuracy) + 0.00714 %/°C (temperature coefficient) × 5°C (temperature variation difference) = 0.1357%

#### (7) Changing the input range

The input range<sup>\*3</sup> can easily be set from the GX Developer.

\*3 Input range refers to the type of offset/gain settings. The most frequently used range is set as the default but the user can also set the offset/gain.

#### (8) A/D conversion system

There are the following five A/D conversion systems.

(a) Sampling processing

Analog input values are converted into digital values one by one on a channel basis and the digital output value is output at every conversion.

- (b) Averaging processing
  - 1) Time averaging

A/D conversion is averaged in terms of time on a channel basis and a digital average value is output.

2) Count averaging

A/D conversion is averaged in terms of count on a channel basis and a digital average value is output.

3) Move averaging

The specified number of digital output values measured per sampling time are averaged.

(c) Primary delay filter

A digital output value is smoothed according to the preset time constant.

\*3 Refer to Section 3.1.2 for the details of the A/D conversion system.

#### (9) Input signal error detection function

The voltage/current outside the setting range is detected.

#### (10)Warning output

There are the following two warning outputs.

(a) Process alarm

A warning is output if a digital output value falls outside the setting range.

(b) Rate alarm

A warning is output if the varying rate of a digital output value falls outside the preset varying rate range.



#### (11) Scaling function

A/D conversion values can be converted to percentage values (%) in the preset range and be loaded into the buffer memory. This function can reduce the time required for programming. (Refer to Section 3.2.6.)

#### (12)Online module change

The module can be changed without the system being stopped.

Further, the dedicated instruction (G(P).OGLOAD,G(P).OGSTOR), write to the buffer memory, or turning ON the Y signal enables "inheritance of offset/gain settings to the new A/D converter module replacing the old one changed online" and "transfer of offset/gain settings to the other A/D converter module mounted on the other slot". (These apply to the modules of the same model.)

#### (13)Offset/gain setting

GX Configurator-AD, dedicated instruction (G(P).OFFGAN) or mode switching setting allows a shift to the offset/gain setting mode easily.

#### (14) Easy settings using the utility package

A utility package is sold separately (GX Configurator-AD).

The utility package is not a required item, however, it is useful for on-screen setting of the intelligent function module parameters (initial setting/auto refresh setting).

In addition, FB<sup>\*1</sup> can be generated automatically from the intelligent function module parameters that have been set up and used in a sequence program.

\*1 FB is the function for making a circuit block used in a sequence program repeatedly a part (FB) to use it in the sequence program.

This function can improve the efficiency of program development and minimize program bugs to improve program qualities.

For the details of FB, refer to "GX Developer Version 8 Operating Manual (Function Block)."

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TROUBLESHOOTING

# SYSTEM CONFIGURATION

This chapter explains the system configuration of the A/D converter module.

#### **Applicable Systems** 2.1

This section describes the applicable systems.

#### (1) Applicable modules and base units, and No. of modules

(a) When mounted with a CPU module

The table below shows the CPU modules and base units applicable to the A/D converter module and quantities for each CPU model.

Depending on the combination with other modules or the number of mounted modules, power supply capacity may be insufficient.

Pay attention to the power supply capacity before mounting modules, and if the power supply capacity is insufficient, change the combination of the modules.

Applicable CPU module				Base unit <sup>*2</sup>	
CPU type		CPU model No. of modules *1		Main base unit	Extension base unit
		Q00JCPU	Up to 16		
	Basic model QCPU		No. of modules*1  Main base unit  Du Up to 16  Up to 24  Up to 64  Du Up to 64  CPU CPU CPU CPU CPU CPU CPU CPU CPU CP	0	
		No. of modules   Main base unit			
	High Performance				
	model QCPU		Up to 64	0	0
			Up to 64	0	0
	Process CPU				
Programmable	Redundant CPU		Up to 53	×	0
controller CPU			·		Ŭ
			Up to 36		
				0	0
	Universal model		Up to 64		
	QCPU				
	Safety CPU	QS001CPU	N/A	×	×
		Q06CCPU-V		×	*
C. Controller module		Q06CCPU-V-B	Up to 64	0	0

O: Applicable, x: N/A

2 - 1

<sup>\*1</sup> Limited within the range of I/O points for the CPU module.

<sup>\*2</sup> Can be installed to any I/O slot of a base unit.



#### (b) Mounting to a MELSECNET/H remote I/O station

The table below shows the network modules and base units applicable to the A/D converter module and quantities for each network module model.

Depending on the combination with other modules or the number of mounted modules, power supply capacity may be insufficient.

Pay attention to the power supply capacity before mounting modules, and if the power supply capacity is insufficient, change the combination of the modules.

		Base unit <sup>*2</sup>		
Applicable network module	No. of modules <sup>1</sup>	Main base unit of	Extension base unit	
		remote I/O station	of remote I/O station	
QJ72LP25-25		0		
QJ72LP25G	Up to 64			
QJ72LP25GE	Op 10 04		0	
QJ72BR15				

O: Applicable, x: N/A

<sup>\*2</sup> Can be installed to any I/O slot of a base unit.



The Basic model QCPU or C Controller module cannot create the MELSECNET/ H remote I/O network.

#### (2) Support of the multiple CPU system

The function version of the A/D converter modules has been C from the first release, supporting the multiple CPU system.

When using the A/D converter in a multiple CPU system, refer to the following manual first.

- QCPU User's Manual (Multiple CPU System)
- (a) Intelligent function module parameters
  Write intelligent function module parameters to only the control CPU of the A/D converter module.

#### (3) Compatibility with online module change

To make an online module change, use the A/D converter module of function.

<sup>\*1</sup> Limited within the range of I/O points for the network module.

#### (4) Supported software packages

Relation between the system containing the Q68AD-G and software package is shown in the following table.

GX Developer is necessary when using the Q68AD-G.

		Software Version		
		GX Developer	GX Configurator-AD	
Q00J/Q00/Q01CPU	Single CPU system	Version 7 or later		
Q003/Q00/Q01010	Multiple CPU system	Version 8 or later		
Q02/Q02H/Q06H/	Single CPU system	Version 4 or later		
Q12H/Q25HCPU	Multiple CPU system	Version 6 or later		
Q02PH/Q06PHCPU	Single CPU system	Version 8.68W or later		
QUZI TI/QUUI TIOI U	Multiple CPU system	VCISION 0.00VV OI IAICI		
Q12PH/Q25PHCPU	Single CPU system	Version 7.10L or later		
Q 121 TI/Q251 TIOL 0	Multiple CPU system	version 7. Tot of later		
Q12PRH/	Redundant CPU system	Version 8.45X or later		
Q25PRHCPU	redundant of o system	VCISION 0.40X OF later	Version 2.09K or later	
Q02U/Q03UD/	Single CPU system			
Q04UDH/	Multiple CPU system	Version 8.48A or later		
Q06UDHCPU	Multiple of 0 system			
Q13UDH/	Single CPU system	Version 8.62Q or later		
Q26UDHCPU	Multiple CPU system	Version 0.02Q or later		
Q03UDE/Q04UDEH/	Single CPU system			
Q06UDEH/Q13UDEH/	Multiple CPU system	Version 8.68W or later		
Q26UDEHCPU	Multiple OF O System			
If installed in a MELSEC	NET/H remote I/O station	Version 6 or later		

### **⊠**Point

Depending on the version of GX Configurator-AD, applicable system, CPU module and functions of A/D converter module.

Refer to Appendix 2 for details.



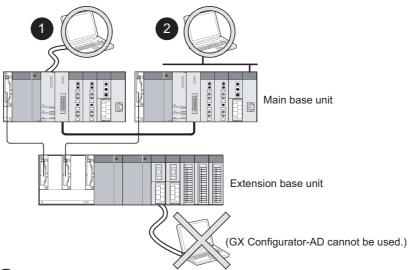
# 2.2 Precautions on System Configuration

#### (1) For Use with Q12PRH/Q25PRHCPU

(a) Dedicated instruction

The dedicated instruction cannnot be used.

(b) GX Configurator-AD connection GX Configurator-AD cannot be used when accessing the Q12PRH/Q25PRHCPU via an intelligent function module on an extension base unit from GX Developer. Connect a personal computer with a communication path indicated below.



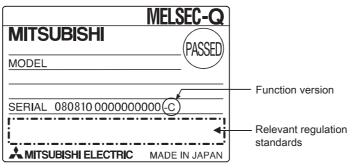
- 1 Direct connection to use the CPU
- Connection through an intelligent function module on the main base unit (Through Ethernet module, MELSECNET/H module, or CC-Link module)

# 2.3 How to Check the Function Version, Serial No., Product Information and Software Version

This section describes how to check the function version, serial No., product information of the A/D converter module and GX Configurator-AD software version.

# (1) Checking the function version, serial No. and product information of the A/D converter module

(a) To check the function version and serial number using the "SERIAL column of the rating plate" located on the side of the module



(b) To check the function version and product information using GX Developer Refer to Section 8.2.6 of this manual.

# **⊠**Point

added.

The serial No. on the rating plate may be different from the serial No. displayed on the product information screen of GX Developer.

- The serial No. on the rating plate indicates the management information of the product.
- The serial No. displayed on the product information screen of GX Developer indicates the function information of the product.
   The function information of the product is updated when a new function is

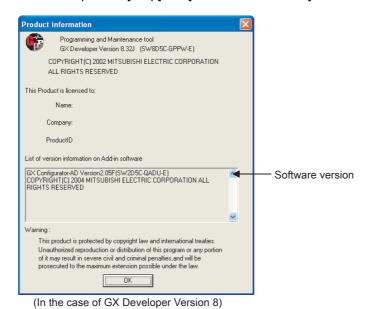


#### (2) Checking the software version of GX Configurator-AD

The software version of GX Configurator-AD can be checked in GX Developer's "Product information" screen.

#### [Operating procedure]

GX Developer  $\rightarrow$  [Help]  $\rightarrow$  [Product Information]



# 3 SPECIFICATIONS

The description of this chapter and later is based on the Q68AD-G.

# 3.1 Performance Specifications

### 3.1.1 Performance specifications list

Table 3.1 shows the performance specifications of the A/D converter modules.

Table3.1 Performance Specifications of Q68AD-G

Ite	em			Specificatio	ns			
Number of analog input		8 points (8 channels)						
points		o points (o channels)						
Analog input Voltage		-10 to 10VDC (Input impedance 1 MΩ or more)						
	Current			DmADC (Input res	,			
Digital output		16-bit signed binary (normal resolution mode: –4096 to 4095, high resolution mode: –12288 to 12287,						
			40.11	-16384 to 163	,			
Using scalin	ng function		16-bit	signed binary (-32	2/68 to 32/6/)			
				Normal reso	lution mode	High resolu	ıtion mode	
		Input	Analog input range	Digital output value	Maximum resolution	Digital output value	Maximum resolution	
			0 to 10V		2.5 mV	0 to 16000	0.625 mV	
			0 to 5V 1 to 5V	0 to 4000	1.25 mV 1.0 mV	0 to 12000	0.416 mV 0.333 mV	
		Voltage	1 to 5V (Expanded mode)	-1000 to 4500	1.0 mV	-3000 to 13500	0.333 mV	
I/O characteristics, maximum resolution			-10 to 10V	-4000 to 4000	2.5 mV	-16000 to 16000	0.625 mV	
			Users range setting	4000 to 4000	0.375 m V	-12000 to 12000	0.333 mV	
		Current	0 to 20mA	0 to 4000	5 μ A	0 to 12000	1.66 μ A	
			4 to 20mA		4 μ A	- 0 10 12000	1.33 μ A	
			4 to 20mA (Expanded mode)	-1000 to 4500	4 $\mu$ A	-3000 to 13500	1.33 $\mu$ A	
			Users range setting	-4000 to 4000	1.37 $\mu$ A	-12000 to 12000	1.33 $\mu$ A	
		<u> </u>		±0.1%				
Accuracy	Reference		Norr	mal resolution mod	de: ±4digit <sup>*2</sup>			
(Accuracy	accuracy *1	High resolution mode (0 to 10V, -10 to 10V) : ±16digit <sup>*2</sup>						
relative to			High resolution mode (Other than the above ranges): ±12digit* <sup>2</sup>					
digital output	Temperature	Thigh resolution mode (Other than the above ranges) . ±12uigit =						
value)	coefficient *3		±	71.4ppm/°C (0.00	714%/°C)			
Common mode		Common mode voltage, Input-Common ground (input voltage 0V): 500VAC						
characteristic		Common mode voltage rejection ratio (VCM < 500V): 60Hz 107dB, 50Hz 106dB						
Sampling cyc	le <sup>*4</sup>			10ms/ chanr	nel			
Response tim	ne <sup>*5</sup>			20ms				
Absolute maximum input		Voltage: ± 15V Current: ± 30mA*6						



#### Table3.1 Performance Specifications of Q68AD-G

Item	Specifications				
	Specific isolated area	Isolation method	Dielectric withstand voltage	Insulation resistance	
Isolation	Between input terminal and programmable controller power supply	Transformer isolation	500VAC rms, 1min.	500VDC, 10MΩ or more	
	Between analog input channels		1000VAC rms, 1min.	more	
Maximum number of writes to flash memory	Up to 50,000 times				
Number of I/O occupied points	16 points (I/O assignment: Intelligent 16 points)				
External wiring connection system		40-pin connector			
Applicable wire size	Within 0.3mm <sup>2</sup> (AWG#22)				
External device connection connector	A6CON4 (sold separately)				
Internal current consumption (5VDC)	0.46A				
Weight	0.16kg				

<sup>\*1</sup> Accuracy of offset/gain setting at ambient temperature

Example) Accuracy when temperature changes from 25 to 30°C

0.1% (reference accuracy) + 0.00714 %/°C (temperature coefficient)  $\times$  5°C (temperature change difference) = 0.1357%

<sup>\*2 &</sup>quot;digit" indicates a digital value.

<sup>\*3</sup> Accuracy per temperature change of 1°C

<sup>\*4</sup> The cycle in which A/D conversion values are updated.

<sup>\*5</sup> The time required for an input signal to reach the A/D converter inside the Q68AD-G.

<sup>\*6</sup> Current value indicates value of instant input current that does not break module inner electrical resistance.

#### Table3.2 Performance Specifications of Q66AD-DG

	Item		Spe	ecifications				
Number of ana	alog input points (2-wire							
transmitters)	<b>0</b> 1 1 \	6 points (6 channels)						
	With 2-wire	4 to 20mADC (Input resistance 250Ω)						
nput	transmitter							
pecification	Without 2-wire transmitter	0 to 20mADC±1 (Input resistance 250 $\Omega$ )						
	Supply voltage	26±2VDC						
	Maximum supply			24mADC				
Supply power specification	current							
	Short-circuit			Available	_			
	protection Chack terminals			rent: 25 to 35	mA			
	Check terminals	16-bit signed binary			to 4005 high reso	olution mode:		
Digital output		10-bit signed billar	,	38 to 12287)	to 4095, mgm resc	nution mode.		
Using	scaling function		16-bit signed b	,	to 32767)			
	<u> </u>		Normal reso	• ,	<u> </u>	ution mode		
		Analog input range	Digital out-	Maximum	Digital out-	Maximum		
		<u> </u>	put value	resolution	put value	resolution		
10 -h -		0 to 20mA		5 μ A	0 to 12000	1.66 $\mu$ A		
O characteris	stics, maximum resolution	4 to 20mA	0 to 4000	4 μ A	0 to 12000	1.33 μ A		
		4 to 20mA	-1000 to 4500	4 μ A	-3000 to	1.33 μ A		
		(Expanded mode)	1000 to 4000	4 // //	13500	1.55 # A		
		Users range setting*4	0 to 4000	1.37 $\mu$ A	0 to 12000	1.33 $\mu$ A		
Accuracy		±0.1% (Normal resolution mode: ±4digit *2						
Accuracy	Reference accuracy *1							
elative to		High resolution mode: ±12digit *2)						
digital	Temperature coefficient *3	±71.4ppm/°C (0.00714 %/°C)						
output value) Sampling cycle		10ms/ channel						
Response time		20ms						
Absolute maxi		±30mA* <sup>7</sup>						
ADSOIULE IIIAXI	mum output		:					
		Specific isolated are	a Isolation m	ethod Diel	ectric withstand	Insulation resistance		
		Between I/O terminal a	and l		voltage	resistance		
		programmable control		500	VAC rms, 1min.			
solation				1 - 30	-,	500VDC		
isolation		power supply	Tec	mor		300 V D C		
oolation		Between analog inpu	Transform		OVAC rms 1min	10MΩ or		
ooldiioii		Between analog inpu	isolatio		OVAC rms, 1min.			
		Between analog inpuchannels Between external supp	isolatio	n 1000	OVAC rms, 1min.	10MΩ or		
		Between analog inpu	isolatio	n 1000		10MΩ or		
Maximum num	nber of writes to flash	Between analog inpuchannels Between external supp	isolatio	n 1000	VAC rms, 1min.	10MΩ or		
Maximum num nemory		Between analog inpuchannels  Between external suppower and analog inp	isolation isolat	50,000 times	VAC rms, 1min.	10MΩ or		
Maximum num nemory Number of I/O	occupied points	Between analog inpuchannels  Between external suppower and analog inp	oly ut  Up to 5 points (I/O assig	500 50,000 times	VAC rms, 1min.	10MΩ or		
Maximum num nemory Number of I/O External wiring	occupied points g connection system	Between analog inpuchannels  Between external suppower and analog inp	Up to	50,000 times nment: Intelligion connector	oVAC rms, 1min.	10MΩ or		
Maximum num nemory Number of I/O External wiring Applicable wire	occupied points g connection system	Between analog inpuchannels  Between external suppower and analog inp	Up to spoints (I/O assig 40-p Within 0.	500 50,000 times	pent 16 points)	10MΩ or		
Maximum num nemory Number of I/O External wiring Applicable wire	occupied points g connection system e size	Between analog inpuchannels  Between external suppower and analog inp	Up to S points (I/O assig 40-p Within 0.	50,000 times nment: Intelliging connector 3mm² (AWG#	gent 16 points)	10MΩ or		
Maximum num memory Number of I/O External wiring Applicable wird External devic	occupied points g connection system e size e connection connector	Between analog inpuchannels  Between external suppower and analog inp	Up to S points (I/O assig 40-p Within 0. A6CON4	50,000 times  nment: Intelligion connector  3mm² (AWG# (sold separat	gent 16 points)	10MΩ or		
Maximum num memory Number of I/O External wiring Applicable wire External devic	occupied points g connection system e size e connection connector	Between analog inpuchannels  Between external suppower and analog inp	Up to S points (I/O assig 40-p Within 0. A6CON4 24VD Ripple, spil	50,000 times  nment: Intelliging connector  3mm² (AWG# (sold separat) C +20%, -15% ke within 500n ht: 5.0A, within	gent 16 points)  #22) ely) onV <sub>P-P</sub>	10MΩ or		
Maximum num memory Number of I/O External wiring Applicable wire External devic	occupied points g connection system e size e connection connector	Between analog inpuchannels  Between external suppower and analog inp	Up to S points (I/O assig 40-p Within 0. A6CON4 24VD Ripple, spil	50,000 times  nment: Intelligion connector  3mm² (AWG# (sold separat C +20%, -15% ke within 500r	gent 16 points)  #22) ely) onV <sub>P-P</sub>	10MΩ or		



- \*1 Accuracy of offset/gain setting at ambient temperature Q66AD-DG needs to be powered on 30 minutes prior to operation for compliance to the specification (accuracy).
- \*2 "digit" indicates a digital value.
- \*3 Accuracy per temperature change of 1°C

Example) Accuracy when temperature changes from 25 to  $30^{\circ}$ C 
0.1% (reference accuracy) + 0.00714 %/°C (temperature coefficient) × 5°C 
(temperature change difference) = 0.1357%

- \*4 User range setting is 2 to 24mA
- \*5 The cycle in which A/D conversion values are updated.
- \*6 The time required for an input signal to reach the A/D converter inside the Q66AD-DG.
- \*7 Current value indicates value of instant input current that does not break module inner electrical resistance.



See the user's manual for the CPU module being used for general specifications of the A/D converter modules.

#### 3.1.2 I/O conversion characteristic

The I/O conversion characteristic represents the angle formed by a straight line connecting the "offset value" and "gain value" when the analog signals (voltage or current input) from outside the programmable controller are converted to digital values.

#### Offset value

The offset value denotes the analog input value (voltage or current) that makes the digital output value 0.

#### Gain value

The gain value denotes the analog input value (voltage or current) that makes the digital output value:

4000 (in normal resolution mode) 16000/12000 (in high resolution mode) OVERVIEW



#### (1) Input characteristics of Q68AD-G

(a) Voltage input characteristic Fig. 3.1 shows a graph of the voltage input characteristic.

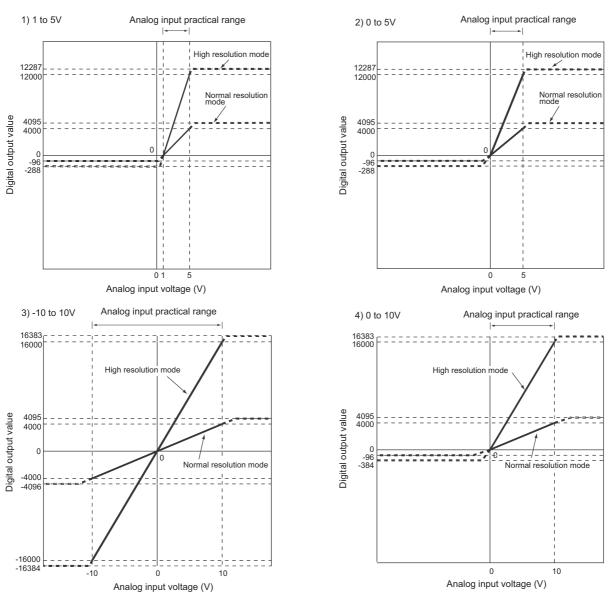
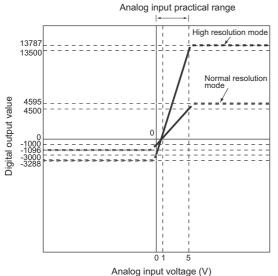


Fig.3.1 Voltage input characteristic of Q68AD-G (1/2)





6) User range setting (Offset value: -5V, Gain value: 0V)

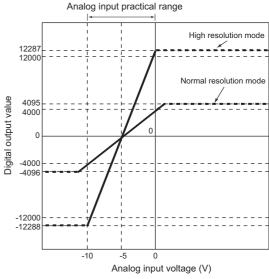


Fig.3.1 Voltage input characteristic of Q68AD-G (2/2)

# **⊠**Point

- (1) Set within the analog input range and digital output range for each input range. If these ranges are exceeded, the maximum resolution and accuracy may not fall within the performance specifications. (Avoid use shown by the dotted lines in Fig.3.1.)
- (2) Do not input an analog input voltage of ±15 V or more. The input elements may be damaged.
- (3) Set the offset/gain values for the User range setting marked within a range which satisfies the following conditions.
  - (a) Offset value, gain value setting range: -10V to 10V
  - (b) { (gain value) (offset value) } > A
  - <Value of A>

Normal resolution mode	High resolution mode
1.5 V	4.0 V

(4) When an analog value that exceeds the range for the digital output value is entered, the digital output value will be fixed at the maximum or minimum value.

Analog input	Normal reso	lution mode	High resolution mode		
range setting	Minimum	Maximum	Minimum	Maximum	
1 to 5V	-96		-288	12287	
0 to 5V	-90	4095	-200	12207	
-10 to 10V	10 to 10V -4096		-16384	16383	
0 to 10V	-96		-384	10303	
1 to 5V	-1096	4595	-3288	13787	
(Expanded mode)	-1090	4393	-3200	13707	
User range setting	-4096	4095	-12288	12287	

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# (b) Current input characteristic Fig. 3.2 shows a graph of the current input characteristic

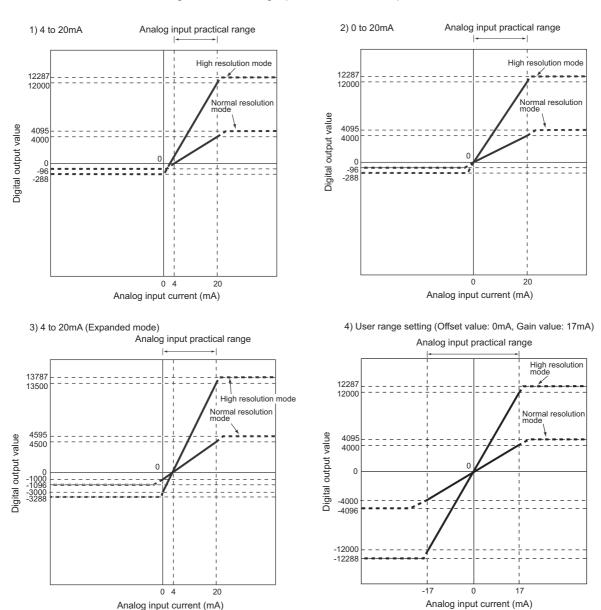


Fig.3.2 Current input characteristic of Q68AD-G

# **⊠**Point

(1) Set within the analog input range and digital output range for each input range.

If these ranges are exceeded, the maximum resolution and accuracy may not fall within the performance specifications. (Avoid use shown by the dotted lines in Fig.3.2.)

- (2) Do not input an analog input current of ±30 mA or more. The input elements may be damaged.
- (3) Set the offset/gain values for the User range setting marked within a range in which satisfies the following conditions.
  - (a) Gain value  $\leq$  20mA, offset value  $\geq$  0mA
  - (b) { (gain value) (offset value) } > A

<Value of A>

Normal resolution mode	High resolution mode
5.5mA	16.0mA

(4) When an analog value that exceeds the range of the digital output value is entered, the digital output value will be fixed at the maximum or minimum value.

Analog input range setting	Digital output value (Normal resolution mode)		Digital output value (High resolution mode)	
range setting	Minimum	Maximum	Minimum	Maximum
4 to 20mA	to 20mA -96		-288	12287
0 to 20mA	-90	4095	-200	12201
4 to 20mA	-1096	4595	-3288	13787
(Expanded mode)	-1090	4393	-3200	13707
User range setting	-4096	4095	-12288	12287



#### (2) Input characteristic of Q66AD-DG

Fig. 3.3 shows a graph of the Q66AD-DG input characteristic.

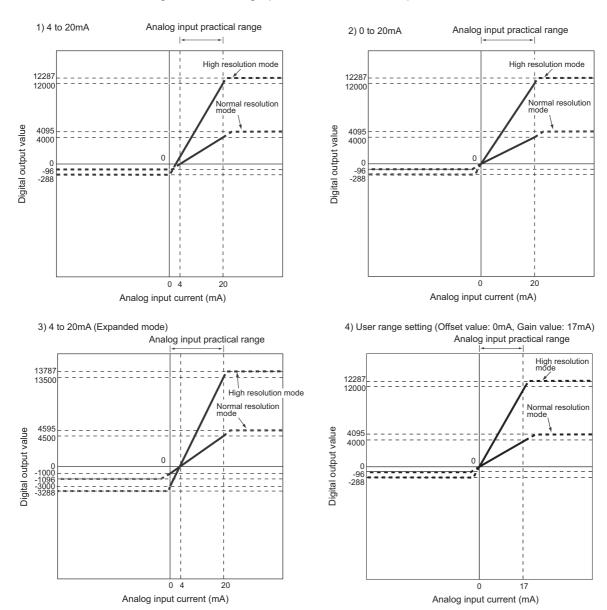


Fig.3.3 Input characteristic of Q66AD-DG

3

# **⊠**Point

- (1) Set within the analog input range and digital output range for each input range.
  - If these ranges are exceeded, the maximum resolution and accuracy may not fall within the performance specifications. (Avoid use shown by the dotted lines in Fig.3.3.)
- (2) Do not input an analog input current of  $\pm 30$  mA or more. The input elements may be damaged.
- (3) Set the offset/gain values for the User range setting marked within a range which satisfies the following conditions.
  - (a) Gain value  $\leq$  24mA, offset value  $\geq$  0mA
  - (b) { (gain value) (offset value) } > A

<Value of A>

Normal resolution mode	High resolution mode
5.5 V	16.0 V

(4) When an analog value that exceeds the range of the digital output value is entered, the digital output value will be fixed at the maximum or minimum value.

Analog input range setting	Digital output value (Normal resolution mode)		Digital output value (High resolution mode)	
range setting	Minimum	Maximum	Minimum	Maximum
0 to 20mA	-96	4095	-288	12287
4 to 20mA	-90	4093	-200	
4 to 20mA	-1096	4595	-3288	13787
(Expanded mode)	expanded mode)		-3200	13767
User range setting	-96	4095	-288	12287



#### 3.1.3 Accuracy

The reference accuracy is the accuracy at the ambient temperature for offset/gain setting. The temperature coefficient is the accuracy per temperature variation of  $1^{\circ}$ C.

The reference accuracy is the accuracy relative to the maximum digital output value. Even if you change the offset/gain setting or input range to change the input characteristic, the reference accuracy and temperature coefficient do not change and are kept within the ranges given in the performance specifications.

[Example] Accuracy when the temperature changed from  $25^{\circ}$ C to  $30^{\circ}$ C 0.1% (reference accuracy) + 0.00714%/°C (temperature coefficient)  $5^{\circ}$ C (temperature variation difference) = 0.1357%

#### **Function List** 3.2

Table 3.3 lists the functions of the A/D converter modules.

#### **Table3.3 Function list**

Item	Function	Reference section
A/D conversion enable/	(1) Specifies whether to enable or disable the A/D conversion for each channel.	
disable setting	(2) Since the conversion time is 10ms per channel, disabling A/D conversion of	Section 3.4.2
disable setting	unused channels can reduce the entire conversion time.	
	(1) Sampling processing	
	The A/D conversion for analog input values is performed successively for each	
	channel, and the digital output value is output upon each conversion.	
	(2) Averaging processing	
	(a) Time averaging	
	A/D conversion is averaged in terms of time on a channel basis and a digital	
	average value is output.	
A/D conversion method	(b) Count averaging	Section 3.2.1
	A/D conversion is averaged in terms of count on a channel basis and a	
	digital average value is output.	
	(c) Move averaging	
	The specified number of digital output values measured per sampling time	
	are averaged.	
	(3) Primary delay filter	
Maximum and minimum	A digital output value is smoothed according to the preset time constant.	
values hold function	(1) The maximum and minimum values of the digital output values are retained in the module.	Section 3.2.2
Input signal error	module.	
detection function	(1) The voltage/current outside the setting range is detected.	Section 3.2.3
	(1) Process alarm	
	A warning is output if a digital output value falls outside the setting range.	
Warning output function	(2) Rate alarm	Section 3.2.4
	A warning is output if the varying rate of a digital output value falls outside the	
	preset varying rate range.	
Conversion starting time	(1) Setting the A/D conversion starting time allows A/D conversion to be started at	
setting function	the point when the output of the 2-wire transmitter stabilizes.	Section 3.2.5
(Q66AD-DG only)		
	(1) The power supply to the 2-wire transmitter can be switched ON/OFF channel by	
Supply power ON/OFF	channel.	
function	(2) Power is supplied to channels that have input range settings of "4 to 20mA	Section 3.4.2
(Q66AD-DG only)	(2-wire transmitter input): 0H", "4 to 20mA (Expanded mode) (2-wire transmitter	
	input): Ah" or "User range setting (2-wire transmitter input): Fh", and that are set to "Enabled" in A/D conversion enable/disable setting (Un\G0).	
	(1) Conversion of A/D conversion values to preset percentage values and loading	_
Scaling function	into the buffer memory is available.	Section 3.2.6
g	Programming steps for the scaling can be eliminated.	
	3 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Chapter 7

#### 3.2.1 A/D conversion methods

#### (1) Sampling processing

A/D conversion is performed successively for analog input values, and the converted digital output values are stored in the buffer memory.

The sampling processing time varies depending on the number of channels used (number of channels set as A/D conversion enable).

Sampling processing time = No. of channels used  $\times$  10 (ms)

Example) When setting 6 for the number of channels used

$$6 \times 10 = 60 \text{ (ms)}$$

#### (2) Averaging processing

#### (a) Time averaging

A/D conversion is made for the preset period of time, the sum of values other than the maximum and minimum values is averaged, and the result is stored into the buffer memory.

The processing count within the set time varies depending on the number of channels used (number of channels set as A/D conversion enable).

Processing count = Set time/(No. of channels used  $\times$  10) (times)

Example) When setting 6 for the number of channels used, and 500ms for the set time

 $500/(6 \times 10) = 8.333$  (times) ··· Drop the fractional part.

#### (b) Count averaging

A/D conversion is made the preset number of times, the sum of values other than the maximum and minimum values is averaged, and the result is stored into the buffer memory.

The time required for the count-based average value to be stored into the buffer memory varies depending on the number of channels used (number of channels set as A/D conversion enable).

Processing time = Set count  $\times$  (No. of channels used  $\times$  10) (ms)

Example) When setting 5 (times) for the average processing count

$$5 \times 6 \times 10 = 50 \text{ (ms)}$$

#### (c) Moving average

The specified count of digital output values imported per sampling period are averaged to find a value, which is then stored into the buffer memory. Since average processing is performed with data shifted per sampling, the most recent digital output value is obtainable.

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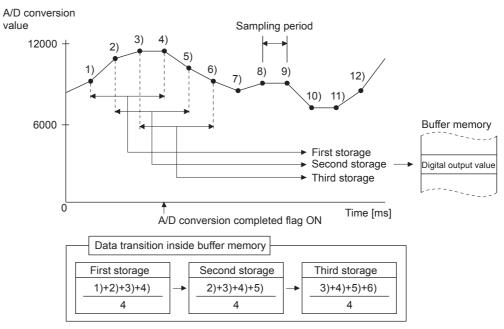
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### (3) Primary delay filter

A digital value whose transient noise has been smoothed is output according to the preset time constant.

The degree of smoothing varies with the time constant setting.

The relational expression of the time constant and digital output value is indicated below.

$$[If n = 1]$$

$$Yn = 0$$

$$[If n = 2]$$

$$Yn = yn-1 + \frac{\triangle t}{\triangle t + TA} (yn - yn-1)$$

[If 
$$n \ge 3$$
]

$$Yn = Yn-1 + \frac{\Delta t}{\Delta t + TA} (yn - Yn-1)$$

Yn : Current digital output value Pre-smoothing digital output value yn

Yn-1 Immediately preceding digital output Yn-1 Immediately preceding

value

pre-smoothing digital output value

Sampling count A/D conversion time (0.01  $\times$ Δt

number of conversion enabled chan-

nels)(s)

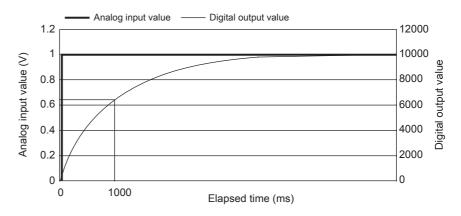
TΑ Time constant (s)

\*The A/D conversion completed flag turns ON when  $n \ge 2$ .

[Example 1] Digital output value when the analog input value varied from 0 to 1V

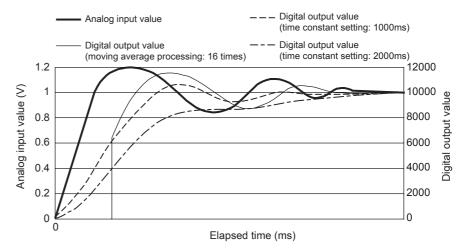
The variation of the digital output value at the time constant setting of 1000ms (1s) is as shown below.

1000ms (1s) after the analog input value has reached 1V, the digital output value reaches 63.2% of the value attained when the sampling processing is selected.



[Example 2] Digital output value when the variation of the analog input value has a ringing waveform

The variations of the digital output values at the time constant setting of 2000ms (2s), at the time constant setting of 1000ms (1s), and at the moving average processing of 16 times are as shown below.

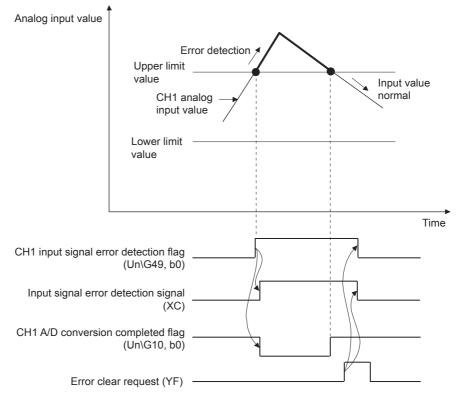


- (1) The maximum and minimum values are held in the buffer memory channel by channel.
- (2) The maximum and minimum values are cleared to 0 when the maximum value/ minimum value reset request (YD) or operating condition setting request (Y9) is turned ON, and new maximum and minimum values are stored when conversion is started.
- (3) Since the area for storing the maximum and minimum values can be rewritten with the sequence program, the maximum and minimum values within a specific period of time can be checked.
- (4) When the scaling function is enabled, values after scaling conversion are stored as the maximum and minimum values. For the scaling function, refer to Section 3.2.6.

## 3.2.3 Input signal error detection function

- (1) If the input voltage/current rose to or above the input signal error detection upper limit value or fell to or below the lower limit value, the input signal error detection flag (Un\G49) and input signal error detection signal (XC) turn ON and the ALM LED flickers to indicate the error.
- (2) When the input signal error detection flag (Un\G49) turns ON for a channel, a digital output value immediately before the error detection is held for the channel, and the A/D conversion completed flag (Un\G10) of the corresponding channel turns OFF.
- (3) By bringing the analog input value within the setting range and then turning ON the error clear request (YF), the input signal error detection flag (Un\G49) and input signal error detection signal (XC) turn OFF.

(4) When the analog input value returns to within the setting range, A/D conversion is resumed independently of whether the input signal error detection flag (Un\G49) and input signal error detection signal (XC) are reset or not, the A/D conversion completed flag (Un\G10) of the corresponding channel turns ON again after the first updating. (The ERR. LED remains flickering.)



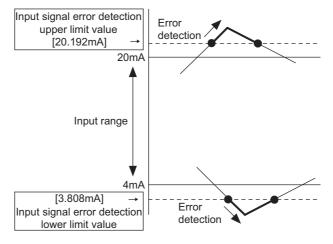
- (5) This function is executed at every sampling processing.
- (6) Perform the following procedure to use this function.
  - 1) Set the input signal error detection setting value for the corresponding channel.
  - 2) Enable the A/D conversion of the corresponding channel.
  - 3) Enable the input signal error detection of the corresponding channel.
  - 4) Turn ON the operating condition setting request (Y9).

(7) The input signal error detection extended setting, input signal error detection setting, and warning output setting (Un\G47) enables the input signal error detection to be performed by setting the same value of upper limit and lower limit or the different value of upper limit and lower limit.

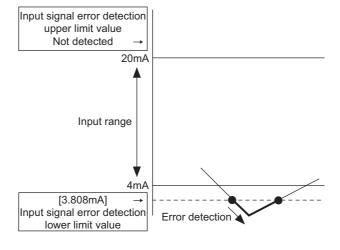
The settings make it possible to perform the input signal error detection not only by setting the same value of upper limit and lower limit, but also setting the upper limit only, the lower limit only, or the different value.

Example) The following settings can be performed for the channels set the input range from 4 to 20mA (expanded mode).

- 1) If the following settings are performed, the value of upper limit and lower limit can be set to be same.
  - Input signal error detection extended setting: 0 (Same upper limit value / lower limit value)
  - Input signal error detection setting value: 12 (1.2%)

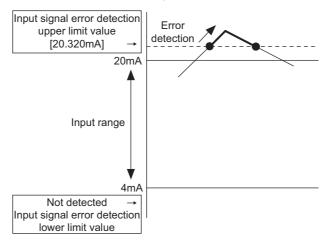


- 2) If the following settings are performed, only the value of lower limit can be set.
  - Input signal error detection extended setting : 1 (Different upper limit value/lower limit value)
  - Input signal error detection upper limit setting value : 251 (input signal error detection invalid)
  - Input signal error detection lower limit setting value : 12 (1.2%)

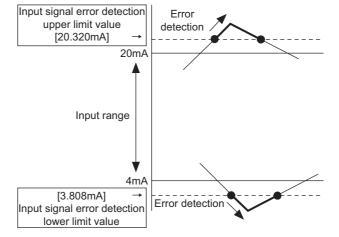




- 3) If the following settings are performed, only the value of upper limit can be set.
  - Input signal error detection extended setting: 1 (Different upper limit value / lower limit value)
  - Input signal error detection upper limit setting value : 12 (1.2%)
  - Input signal error detection lower limit setting value : 251 (input signal error detection invalid)



- 4) If the following settings are performed, the value of upper limit and lower limit can be set to be different.
  - Input signal error detection extended setting : 1 (Different upper limit value / lower limit value)
  - Input signal error detection upper limit setting value : 20 (2.0%)
  - Input signal error detection lower limit setting value : 12 (1.2%)



## 3.2.4 Warning output function

#### (1) Process alarm

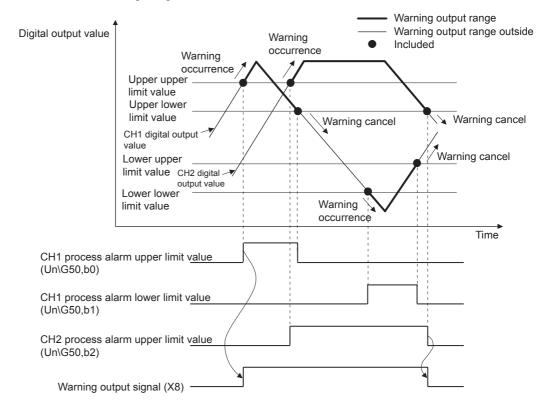
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(a) If the detected digital output value rose to or above the process alarm upper upper limit value or fell to or below the process alarm lower lower limit value and entered the warning output range zone, the warning output flag (process alarm)(Un\G50) and warning output signal (X8) turn ON and the ALM LED is lit to indicate the warning.

A warning will be output according to the following digital output values.

Item		Digital value causing warning output
Value set in scaling enable/	0: Disable	CH□ digital output value (Un\G11 to Un\G18)
disable setting (Un\G53)	1: Enable	CH□ scaling value (Un\G54 to Un\G61)

The warning output signal (X8) turns OFF only when all channels return to within the setting range.



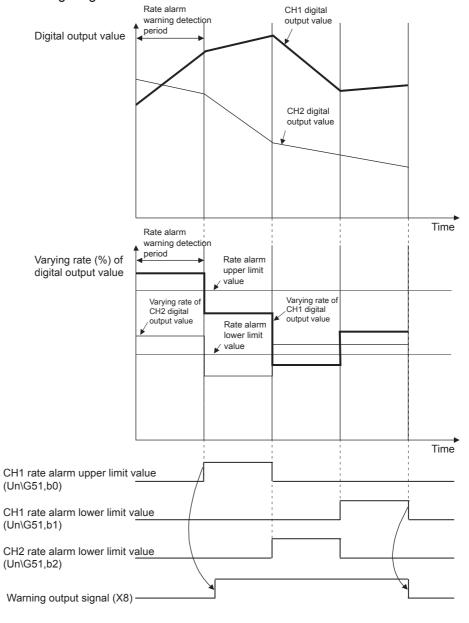
- (b) When time or count averaging is specified, this function is executed at intervals of the preset averaging time or averaging count. When any other A/D conversion system (sampling processing, moving average, primary delay filter) is specified, this function is executed at intervals of the sampling time.
- (c) To use the scaling function, be sure to consider the scaling conversion before setting the CH□ process alarm upper/lower limit value.



#### (2) Rate alarm

- (a) If the range of change in the digital output value sampled at intervals of the rate alarm warning detection period is equal to or greater than the rate alarm upper limit value or is equal to or less than the rate alarm lower limit value, the warning output flag (rate alarm) (Un\G51) and warning output signal (X8) turn ON and the ALM LED is lit to indicate the warning of the rate alarm.
- (b) If, after the output of the warning, the rate fell below the rate alarm upper limit value or rose above the rate alarm lower limit value and returned to within the setting range, "0" is stored into the bit position corresponding to the channel number of the warning output flag (rate alarm) (Un\G51).

The warning output signal (X8) turns OFF only when all channels return to within the setting range.



- (c) Set the rate alarm upper limit value/lower limit value in 0.1%/s increments relative to the maximum value (16000/12000/4000) of the digital output value. The setting range is -32768 to 32767 (-3276.8% to 3276.7%).
- (d) The setting range of the rate alarm warning detection period is 10 to 5000ms. When the period is set to 5000ms, the digital values are compared at intervals of 5 seconds to detect the varying rate.
- (e) The rate alarm is judged by converting the rate alarm upper/lower limit value into the digit value per rate alarm warning detection period. The expression for the value used to make judgment per rate alarm warning detection period is as follows.

Value used to make judgment per rate alarm warning detection period [digit] =rate alarm upper limit value or lower limit value × 0.001 × maximum value of the digital output value × rate alarm warning detection period ÷ 1000

#### Example

In channel 1, when the following:

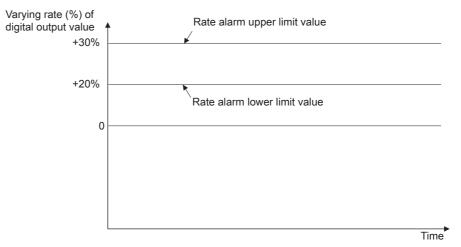
- Upper limit value of change rate: 30%/s (300 is stored in buffer memory)
- Maximum digital output value: 16000
- Rate alarm warning detection period: 10ms

are set, a value [digit] used at every rate alarm warning detection period can be calculated as follows:

 $300 \times 0.001 \times 16000 \times 10 \div 1000 = 48 (digit)$ 

Therefore, the current value is compared with the previous value every 10ms in channel 1, and whether a difference of 48 (digit) or more is identified between them or not is determined.

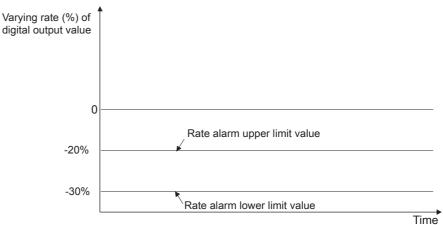
- (f) The rate alarm is useful to watch the varying rate of the digital output value in a limited range.
  - Example of setting the rate alarm upper limit value/lower limit value when it is desired to watch that the digital output value increases within the specified range



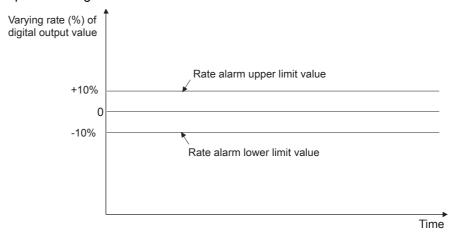
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 Example of setting the rate alarm upper limit value/lower limit value when it is desired to watch that the digital output value decreases within the specified range



3) Example of setting the rate alarm upper limit value/lower limit value when it is desired to watch that the digital output value increases/decreases within the specified range



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#### 3.2.5 Conversion starting time setting function (Q66AD-DG only)

- (1) As the A/D conversion starting time, set the "time necessary from when the used 2-wire transmitter powers on until its output stabilizes". This setting allows A/D conversion processing to be started as soon as the output of the 2-wire transmitter stabilizes.
- (2) Set the time to the CH□ Conversion starting time setting (for 2-wire transmitter) (Un\G78 to Un\G83).
- (3) The following indicates the time until the A/D conversion completed flag (Un\G10) turns ON when the A/D conversion starting time has been set.

(Conversion starting time) + (A/D conversion pre-processing: Approx. 150 to 165ms) + (A/D conversion processing: number of conversion enabled channels × 10ms)

## **⊠**Point

Set the A/D conversion starting time in consideration of the time necessary from when the 2-wire transmitter powers on until its output stabilizes and the warm-up time of the 2-wire transmitter.

[Example] When the time necessary from when the 2-wire transmitter powers on until its output stabilizes is 500ms

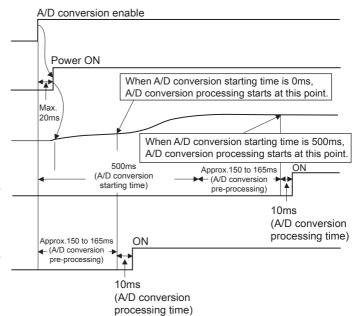
A/D conversion enable/disable setting (Power supply to 2-wire transmitter ON/OFF) (Un\G0)

26V power supply to 2-wire transmitter

Analog output of 2-wire transmitter

A/D conversion completed flag when A/D conversion starting time is set to 500ms (Un\G10)

A/D conversion completed flag when A/D conversion starting time is set to 0ms (Un\G10)

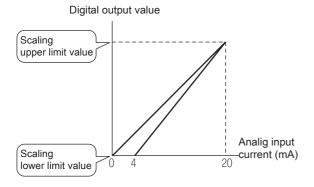




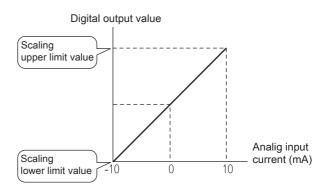
## 3.2.6 Scaling Function

- (1) With this function, A/D conversion values are converted to rate values and loaded into the buffer memory.
- (2) A digital value stored in CH□ digital output value (Un\G11 to Un\G18) is converted to a value in the range set by CH□ scaling upper/lower limit value (Un\G62 to Un\G77). The converted value is stored in CH□ scaling value storage area (Un\G54 to Un\G61).
- (3) The scaling function is used for processed values when using the averaging processing or primary delay filter.
- (4) The setting of CH□ scaling upper/lower limit value (Un\G62 to Un\G77) varies depending on whether to use the factory default range setting or the user range setting for the input range.
  - (a) When using the factory default setting for the input range Set a value that should correspond to the upper limit of digital output as the scaling upper limit value, and a value that should correspond to the lower limit as the scaling lower limit value.

Example 1) Input range setting: 0 to 20mA, or 4 to 20mA (Q68AD-G, Q66AD-DG)

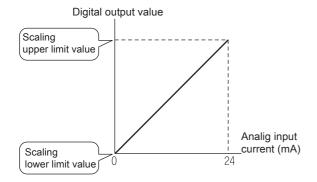


Example 2) Input range setting: -10 to 10V (Q68AD-G)

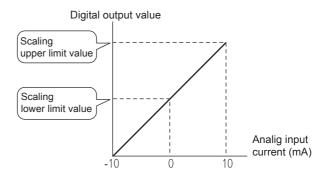


(b) When using the user range setting for the input range Set a value corresponding to the gain or offset value to the scaling upper or lower limit value respectively.

Example 1) Gain value: 0mA, Offset value: 24mA (Q66AD-DG)



Example 2) Gain value: 0V, Offset value: 10V (Q68AD-G)



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> > TROUBLESHOOTING

- (5) How to calculate a scaling value is explained below.
  - (a) When using the factory default setting for the input range
    - 1) Input range: 0 to10V, 0 to 5V, 1 to 5V, 0 to 20mA, 4 to 20mA, 1 to 5V (Expanded mode), or 4 to 20mA (Expanded mode)

Scaling value = 
$$\frac{Dx \times (SH-SL)}{D_{max}} + SL$$

2) Input range: -10 to 10V

Scaling value = 
$$\frac{Dx \times (SH-SL)}{D_{max} - DMin} + \frac{SH+SL}{2}$$

D<sub>x</sub> : Digital input value

D<sub>Max</sub> : The maximum digital output value in the input range being used
 D<sub>Min</sub> : The minimum digital output value in the input range being used

S<sub>H</sub> : Scaling upper limit valueS<sub>L</sub> : Scaling lower limit value

Example) On the Q68AD-G, using the scaling function in High resolution mode

and in the output range of -10 to 10V

If the setting is

Scaling upper limit value,  $S_H$ : 14000 Scaling lower limit value,  $S_L$ : 2000 and the digital input value  $D_X$  is 7500,

Scaling value = 
$$\frac{7500 \times (14000-2000)}{16000-(-16000)} + \frac{(14000+2000)}{2}$$

= 10812.5....

= 10812

It omits digits below the decimal point.



(b) When using the user range setting for the input range

Scaling value = 
$$\frac{Dx \times (SH-SL)}{D_{max}} + SL$$

D<sub>x</sub> : Digital input value

The maximum digital input value in the output range being used

(A/D conversion value corresponding to the gain value)

S<sub>H</sub> : Scaling upper limit value S<sub>L</sub> : Scaling lower limit value

Example) On the Q66AD-G, using the scaling function in High resolution mode

and in the user range setting

If the setting is

Scaling upper limit value, SH: 10000 Scaling lower limit value, SL: 2000 and the digital input value 4250 is input,

Scaling value = 
$$\frac{4250 \times (10000 - 2000)}{12000} + 2000$$
= 4833.33....
= 4833

It omits digits below the decimal point.

# **⊠**Point

The setting range allowed for the scaling upper and lower values is -32000 to 32000. Note that the resolution will not change even if a scaling upper/lower limit value is set to change more than the resolution.

# 3.3 I/O Signals for the Programmable Controller CPU

### 3.3.1 List of I/O signals

Table 3.4 lists the I/O signals of the Q68AD-G.

Table 3.5 lists the I/O signals of the Q66AD-DG.

Note that I/O numbers (X/Y) shown in this chapter and thereafter are the values when the start I/O number for the A/D converter module is set to 0.

Table3.4 List of I/O signal (Q68AD-G)

Signal dire	ection CPU Module ← Q68AD-G	Signal dire	ection CPU Module → Q68AD-G
Device No. (Input)	Signal name	Device No. (Output)	Signal name
X0	Module ready	Y0	
X1		Y1	
X2		Y2	
X3	11	Y3	
X4	Use prohibited*1	Y4	Use prohibited <sup>*1</sup>
X5		Y5	
X6		Y6	
X7	High resolution mode status flag	Y7	
X8	Warning output signal	Y8	
X9	Operating condition setting completed flag	Y9	Operating condition setting request
XA	Offset/gain setting mode flag	YA	User range writing request
XB	Channel change completed flag	YB	Channel change request
XC	Input signal error detection signal	YC	Use prohibited*1
XD	Maximum value/minimum value reset completed flag	YD	Maximum value/minimum value reset request
XE	A/D conversion completed flag	YE	Use prohibited*1
XF	Error flag	YF	Error clear request

# **⊠**Point

\*1 These signals cannot be used by the user since they are for system use only.

If these are turned ON/OFF by the sequence program, the performance of the A/D converter module cannot be guaranteed.



#### Table3.5 List of I/O signal (Q66AD-DG)

Signal dire	ction CPU Module ← Q66AD-DG	Signal dire	ction CPU Module → Q66AD-DG
Device No. (Input)	Signal name	Device No. (Output)	Signal name
X0	Module ready	Y0	
X1		Y1	
X2		Y2	
X3		Y3	
X4	Use prohibited*1	Y4	Use prohibited <sup>*1</sup>
X5		Y5	
X6		Y6	
X7	High resolution mode status flag	Y7	
X8	Warning output signal	Y8	
X9	Operating condition setting completed flag	Y9	Operating condition setting request
XA	Offset/gain setting mode flag	YA	User range writing request
XB	Channel change completed flag	YB	Channel change request
XC	Input signal error detection signal	YC	Offset/gain change request
XC	Offset/gain change completed flag	10	Onsergani change request
XD	Maximum value/minimum value reset	YD	Maximum value/minimum value reset
ΛD	completed flag	1 D	request
XE	A/D conversion completed flag	YE	Use prohibited*1
XF	Error flag	YF	Error clear request

# **⊠** Point

\*1 These signals cannot be used by the user since they are for system use only.

If these are turned ON/OFF by the sequence program, the performance of the A/D converter module cannot be guaranteed.

# 3.3.2 Details of I/O signals

I/O signals for the A/D converter modules are explained in detail below.

#### (1) Input signals

Device No	. Signal Name	Description
Х0	Module ready	<ul> <li>(1) When the programmable controller CPU is powered on or reset, this signal turns on once the preparation for A/D conversion has been completed, and A/D conversion processing is then performed.</li> <li>(2) In either of the following states, the Module ready (X0) turns OFF.</li> <li>During offset/gain setting mode (A/D conversion processing is performed.)</li> <li>When the A/D converter module has a watchdog timer error*1</li> <li>(A/D conversion processing is not performed.)</li> </ul>
X7	High resolution mode status flag	(1) This turns ON when in high resolution mode.
X8	Warning output signal	(a) Process alarm  1) This signal turns ON when the digital output value falls outside the setting range set to the process alarm upper/lower limit values (Un\G86 to Un\G117) on any of the channels enabled for A/D conversion after the process alarm function has been made valid.  2) As soon as the digital output values return to within the setting ranges on all channels enabled for A/D conversion, this signal turns OFF automatically and the ALM LED is also extinguished.  (b) Rate alarm  1) This signal turns ON when the varying rate of the digital output value falls outside the varying rate range set to the rate alarm upper/lower limit values (Un\G122 to Un\G137) on any of the channels enabled for A/D conversion after the rate alarm function has been made valid.  2) As soon as the varying rates of the digital output values return to within the preset varying ranges on all channels enabled for A/D conversion, this signal turns OFF automatically and the ALM LED is also extinguished. > Performed by the A/D converter module  Performed by the sequence program  Warning output flag (Un\G50, Un\G51)  Warning output signal (X8)

<sup>\*1</sup> A watchdog timer error occurs when the program calculations are not completed within the scheduled time due to malfunctions of A/D converter module hardware.

When a watchdog timer error occurs, the RUN LED for the A/D converter module turns off.

Device No.	Signal Name	Description
		(1) This signal is used as an interlock condition to turn ON/OFF the Operating condition setting request
		(Y9) when any of the following settings has been changed.
		A/D conversion enable/disable setting (Un\G0)
		- CH□Average time/Average number of times/Moving average/Time constant settings (Un\G1 to Un\G8)
		<ul> <li>Averaging process specification (Un\G24, Un\G25)</li> </ul>
		<ul> <li>Input signal error detection extended/input signal error detection setting (Un\G47)</li> </ul>
		Warning output settings (Un\G48)
		<ul> <li>Scaling enable/disable setting(Un\G53)</li> </ul>
		- CH□ scaling upper/lower limit value (Un\G62 to Un\G77)
		- CH□ conversion starting time setting (for 2-wire transmitter) *1 (Un\G78 to Un\G83)
		- CH□ process alarm upper/lower limit value (Un\G86 to Un\G117)
		- CH□ rate alarm warning detection period (Un\G118 to Un\G125)
		- CH□ rate alarm upper/lower limit value (Un\G126 to Un\G141)
		<ul> <li>CH□ input signal error detection setting value/CH□ input signal error detection lower limit setting value (Un\G142 to Un\G149)</li> </ul>
		CH□ input signal error detection upper limit setting value (Un\G150 to Un\G157)
	Operating	(2) When the operating condition setting completed flag (X9) is OFF, A/D conversion processing is not
X9	condition setting	performed. Under the following conditions, the operating condition setting completed flag (X9)
	completed flag	turns OFF.
		<ul> <li>When operating condition setting request (Y9) is ON</li> </ul>
		→ Performed by the A/D converter module  → Performed by the sequence program
		Module ready (X0)
		Operating condition setting completed flag (X9)
		Operating condition change request (Y9)
		A/D conversion completed flag (XE)
		(3) Digital outputs are cleared immediately after this Operating condition change request (Y9) turns
		ON. Therefore, turn ON the A/D conversion completed flag before reading digital outputs.

<sup>\*1:</sup> For the Q66AD-DG only

Device No.	Signal Name	Description	
		In offset/gain setting mode]	
		1) This signal is used as an interlock condition to turn ON/OFF the User range wr	iting request (Y
		when the value at completion of offset/gain setting adjustment is registered.	
		2) Refer to Section 4.6 regarding the offset/gain settings.	
		→ Performed by the A/D converter m	odule
		Performed by the sequence progra	
		Module ready (X0)	
		Offset/gain setting mode flag (XA)	<b>▼</b>
XA	Offset/gain	User range writing request (YA)	<i>;</i>
XA	setting mode flag	In normal mode]	
		1) This signal is used as an interlock condition to turn ON/OFF the User range wr	iting request (Y
		when the user range is restored.	
		2) Refer to Chapter 7 for the user range restoration.	
		→ Performed by the A/D converter n	nodule
		→ Performed by the sequence progr	ram
		Module ready (X0) ON	
		Offset/gain setting mode flag (XA)	)
		User range writing request (YA)	
		This is used as an interlock condition for setting the channel change request (	YB) to ON/OFF
		when changing the channel for which the offset/gain settings are to be perforn	*
		2) Refer to Section 4.6 regarding the offset/gain settings.	
		→ Performed by the A/D converter mode	ule
		→ Performed by the sequence program	
ХВ	Channel change completed flag	Offset/gain setting mode Offset/gain specifications (Un\G22, Un\G23)	<b>&gt;</b>
		Channel change completed flag (XB)	
		Channel change request (YB)	

De les Ne	O'r and Norman	1	
Device No.	Signal Name	(1)	Description  This signal turns ON when the angles input value falls outside the patting range set to the language.
		(1)	This signal turns ON when the analog input value falls outside the setting range set to the Input signal error detection setting value value/input signal error detection lower limit setting value
			(Un\G142 to Un\G149), Input signal error detection upper limit setting value (Un\G150 to
			Un\G157) on any of the channels enabled for A/D conversion after the Input signal error detec-
			tion is made valid.
		(2)	When the Input signal error detection signal turns ON
		(2)	The A/D conversion completed flag (Un\G10) of the corresponding channel turns OFF.
			2) The digital output value is held as at the time of error detection.
			3) The ALM LED flickers.
		(3)	By bringing the analog input value within the setting range and then turning ON the Error clear
		,	request (YF), the Input signal error detection signal (XC) turns OFF and the ALM LED is
			extinguished.
		(4)	When the analog input value returns to within the setting range, A/D conversion is resumed
	Input signal error		independently of whether the Input signal error detection signal (XC) is reset or not, and after
	detection signal		the first updating, the A/D conversion completed flag (Un\G10) of the corresponding channel
	actorion oigna.		turns ON again.
			The processing, such as averaging processing or primary delay filter, starts from the first time
			after resumption of A/D conversion.
			→ Performed by the A/D converter module
XC			→ Performed by the sequence program
			Input signal error detection flag  O Input signal error 0
			(Un\G49)
			<u>//////</u>
			Input signal error detection signal
			(XC)
			Error clear request (YF)
		(1)	This signal is used as an interlock condition to turn ON/OFF the offset/gain change request (YC)
			when the offset/gain value is changed.
		(2)	Refer to Section 4.6 for the offset/gain setting.
	Offset/gain		→ Performed by the A/D converter module  → Performed by the sequence program
	change		F renomined by the sequence program
	completed flag*1		Offset/gain change completed flag
	completed lidg		(XC)
			Offset/gain change request (YC)

OVERVIEW

Device No.	Signal Name	Description
		(1) This signal turns ON when the maximum value/minimum value stored at any of the buffer memory addresses 30 to 45 (Un\G30 to Un\G45) is reset by turning ON the Maximum value/minimum value reset request (YD).   Performed by the A/D converter module  Performed by the sequence program
XD	Maximum value/ minimum value reset completed flag	Maximum and minimum values storage area (Un\G30 to Un\G45)
		Maximum value/minimum value reset request (YD)
		Maximum value/minimum value reset completed flag (XD)
		(1) This turns ON when conversion for all of the channels that are conversion enabled has been completed.
XE	A/D conversion completed flag	(2) *1When the external supply power to the Q66AD-DG switches OFF, the A/D conversion completed flag turns OFF, and A/D conversions stop with the previous digital output values being held. When the external supply power switches ON, A/D conversions resume, and as soon as all conversion-enabled channels have completed conversions, the A/D conversion completed flag turns ON. The processing, such as averaging processing or primary delay filter, starts from the first time after resumption of A/D conversion.
XF	Error flag	(1) The error flag turns ON when a write error occurs.  (2) To clear the error code, set the error clear request (YF) to ON.  Performed by the A/D converter module  Performed by the sequence program  Error code(Un\G19)  Error occurs
		Error flag (XF)  Error clear request (YF)

\*1: Q66AD-DG only

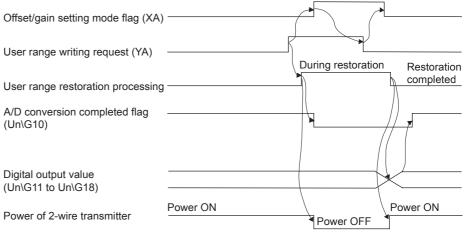


# (2) Output signals

Device No.	Signal Name	Description
		(1) Turn this signal ON when making any of the following settings valid.
		- A/D conversion enable/disable setting (Un\G0)
		CH□ Average time/Average number of times/Moving average/Time constant settings
		(Un\G1 to Un\G8)
		Averaging process specification (Un\G24, Un\G25)
		<ul> <li>Input signal error detection extended/input signal error detection setting (Un\G47)</li> </ul>
		Warning output settings (Un\G48)
	Operating	Scaling enable/disable setting(Un\G53)
Y9	condition setting	CH□ scaling upper/lower limit value(Un\G62 to Un\G72)
	request	• CH□ conversion starting time setting (for 2-wire transmitter) (Un\G78 to Un\G83)
		CH□ process alarm upper/lower limit value (Un\G86 to Un\G117)
		CH□ rate alarm warning detection period (Un\G118 to Un\G125)
		CH□ rate alarm upper/lower limit value (Un\G126 to Un\G141)
		CH□ input signal error detection setting value/CH input signal error detection lower limit setting
		value (Un\G142 to Un\G149)
		CH□ input signal error detection upper limit setting value (Un\G150 to Un\G157)
		(2) Refer to the X9 column for ON/OFF timing.
		[In offset/gain setting mode]
	User range writing request	(1) This turns ON when the value for the adjusted offset/gain settings are registered in the A/D
		converter module.
		(2) Refer to the XA column for ON/OFF timing.
YA		Refer to Section 4.6 for offset/gain settings.
		[In normal mode]
		(1) This signal turns ON when the user range is restored.
		(2) Refer to the field of XA for the ON/OFF timing.
		Refer to Chapter 7 for user range restoration.
	Channel change	(1) This turns ON when changing the channel for which offset/gain settings are to be performed.
YB	request	(2) Refer to the XB column for ON/OFF timing.
	•	Refer to Section 4.6 for offset/gain settings.
VC	Offset/gain	(1) Turn this signal ON when changing the offset/gain value.
YC	change request*1	(2) Refer to the field of XC for the ON/OFF timing.
YD	Maximum value/	Refer to Section 4.6 for the offset/gain setting.  (1) Turning ON the Maximum value/minimum value reset request (YD) clears the maximum value/
	minimum value	minimum value stored at any of the buffer memory addresses 30 to 45 (Un\G30 to Un\G45).
. 5	reset request	(2) Refer to the XD column for ON/OFF timing.
	Error clear	(1) Turn this signal ON when clearing a write error or input signal error.
YF r		1 . ,

<sup>\*1:</sup> Q66AD-DG only

When the User range writing request (YA) is turned ON in the normal mode with A/D conversion enabled, the A/D converter module restores the user range.



During user range restoration: A/D conversion stop, A/D conversion

> completed flag (Un\G10) OFF, previous digital output value held, power of 2-wire transmitter

OFF (Q66AD-DG only)

After user range restoration A/D conversion resumed (when user range

setting is used, A/D conversion is resumed at

the restored offset/gain setting value.)



# 3.4 Buffer Memory

# 3.4.1 Buffer memory assignment

This section describes the buffer memory assignments of the A/D converter modules.

# **⊠**Point

Do not write data from system area or sequence program to the buffer memory area where writing is disabled.

Doing so may cause malfunction.

Table3.6 Buffer memory assignment of Q68AD-G (1/6)

	Table3.6 Buffer memory assignment of Q68AD-G (1/6)					
Addres Hexadecimal	ss Decimal	Description	Default	R/W*1		
0н	0	A/D conversion enable/disable setting	0000н	R/W <sup>*2</sup>		
1н	1	CH1 Average time/Average number of times/	0	R/W <sup>*2</sup>		
IH	'	Moving average/Time constant settings	0	R/VV -		
2н	2	CH2 Average time/Average number of times/	0	R/W*2		
ZΠ	2	Moving average/Time constant settings		R/VV -		
3н	3	CH3 Average time/Average number of times/	0	R/W <sup>*2</sup>		
Jn	JH J	Moving average/Time constant settings		R/VV -		
4н	4	CH4 Average time/Average number of times/	0	R/W <sup>*2</sup>		
70	7	Moving average/Time constant settings		FX/VV		
5н	5	CH5 Average time/Average number of times/	0	R/W <sup>*2</sup>		
Jn .	3	Moving average/Time constant settings		FK/VV -		
6н	6	CH6 Average time/Average number of times/	0	R/W <sup>*2</sup>		
ОН	O	Moving average/Time constant settings	0	FK/VV -		
7н	7	CH7 Average time/Average number of times/	0	R/W <sup>*2</sup>		
/ H	,	Moving average/Time constant settings	0	R/W -		
8н	8	CH8 Average time/Average number of times/	0	R/W*2		
ОН	0	Moving average/Time constant settings				
9н	9	System area	_	_		
Ан	10	A/D conversion completed flag	0	R		
Вн	11	CH1 Digital output value	0	R		
Сн	12	CH2 Digital output value	0	R		
Dн	13	CH3 Digital output value	0	R		
Ен	14	CH4 Digital output value	0	R		
Fн	15	CH5 Digital output value	0	R		
10н	16	CH6 Digital output value	0	R		
11н	17	CH7 Digital output value	0	R		
12н	18	CH8 Digital output value	0	R		
13н	19	Error code	0	R		
14н	20	Setting range(CH1 to CH4)	0	R		
15н	21	Setting range(CH5 to CH8)	0	R		
16н	22	Offset/gain setting mode offset specification	0	R/W		
17н	23	Offset/gain setting mode gain specification	0	R/W		
18н	24	Averaging process specification (CH1 to CH4)	0	R/W*2		
19н	25	Averaging process specification (CH5 to CH8)	0	R/W <sup>*2</sup>		

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



R : Read enabled

W : Write enabled



Table3.6 Buffer memory assignment of Q68AD-G (2/6)

Addre	ss	Pour de Cour	D. C. 16	*1
Hexadecimal	Decimal	Description	Default	R/W*1
1Ан	26			
to	to	System area	_	_
1Dн	29			
1Ен	30	CH1 Maximum value	0	R
<b>1</b> Fн	31	CH1 Minimum value	0	R
20н	32	CH2 Maximum value	0	R
21н	33	CH2 Minimum value	0	R
22н	34	CH3 Maximum value	0	R
23н	35	CH3 Minimum value	0	R
24н	36	CH4 Maximum value	0	R
25н	37	CH4 Minimum value	0	R
26н	38	CH5 Maximum value	0	R
27н	39	CH5 Minimum value	0	R
28н	40	CH6 Maximum value	0	R
29н	41	CH6 Minimum value	0	R
2Ан	42	CH7 Maximum value	0	R
2Вн	43	CH7 Minimum value	0	R
2Сн	44	CH8 Maximum value	0	R
2Dн	45	CH8 Minimum value	0	R
2Ен	46	System area	_	_
2Fн	47	Input signal error detection extended/input signal error	00FFн	R/W*2
21 11	77	detection setting	00111	FK/VV
30н	48	Warning output setting	FFFFH	R/W <sup>*2</sup>
31н	49	Input signal error detection flag	0	R
32н	50	Warning output flag (Process alarm)	0	R
33н	51	Warning output flag (Rate alarm)	0	R
34н	52	System area	_	_
35н	53	Scaling enable/disable setting	00FFн	R/W*2
36н	54	CH1 Scaling value	0	R
37н	55	CH2 Scaling value	0	R
38н	56	CH3 Scaling value	0	R
39н	57	CH4 Scaling value	0	R
ЗАн	58	CH5 Scaling value	0	R
3Вн	59	CH6 Scaling value	0	R
3Сн	60	CH7 Scaling value	0	R
3Dн	61	CH8 Scaling value	0	R
3Ен	62	CH1 Scaling lower limit value	0	R/W*2
3Fн	63	CH1 Scaling upper limit value	0	R/W*2
40н	64	CH2 Scaling lower limit value	0	R/W <sup>*2</sup>
41н	65	CH2 Scaling upper limit value	0	R/W <sup>*2</sup>
42н	66	CH3 Scaling lower limit value	0	R/W*2
43н	67	CH3 Scaling upper limit value	0	R/W <sup>*2</sup>

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



#### Table3.6 Buffer memory assignment of Q68AD-G (3/6)

Addres		Description	Default	R/W*1
Hexadecimal	Decimal	Beschption	Doladit	
44н	68	CH4 Scaling lower limit value	0	R/W*2
45н	69	CH4 Scaling upper limit value	0	R/W*2
46н	70	CH5 Scaling lower limit value	0	R/W*2
47н	71	CH5 Scaling upper limit value	0	R/W*2
48н	72	CH6 Scaling lower limit value	0	R/W*2
49н	73	CH6 Scaling upper limit value	0	R/W <sup>*2</sup>
4Ан	74	CH7 Scaling lower limit value	0	R/W <sup>*2</sup>
4Вн	75	CH7 Scaling upper limit value	0	R/W*2
4Сн	76	CH8 Scaling lower limit value	0	R/W <sup>*2</sup>
4Dн	77	CH8 Scaling upper limit value	0	R/W <sup>*2</sup>
<b>4</b> Ен	78			
to	to	System area	_	_
55н	85			
56н	86	CH1 Process alarm lower lower limit value	0	R/W <sup>*2</sup>
57н	87	CH1 Process alarm lower upper limit value	0	R/W <sup>*2</sup>
58н	88	CH1 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
59н	89	CH1 Process alarm upper upper limit value	0	R/W <sup>*2</sup>
5Ан	90	CH2 Process alarm lower lower limit value	0	R/W <sup>*2</sup>
5Вн	91	CH2 Process alarm lower upper limit value	0	R/W*2
5Сн	92	CH2 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
5Dн	93	CH2 Process alarm upper upper limit value	0	R/W*2
5Ен	94	CH3 Process alarm lower lower limit value	0	R/W*2
5Fн	95	CH3 Process alarm lower upper limit value	0	R/W*2
60н	96	CH3 Process alarm upper lower limit value	0	R/W*2
61н	97	CH3 Process alarm upper upper limit value	0	R/W*2
62н	98	CH4 Process alarm lower lower limit value	0	R/W*2
63н	99	CH4 Process alarm lower upper limit value	0	R/W <sup>*2</sup>
64н	100	CH4 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
65н	101	CH4 Process alarm upper upper limit value	0	R/W*2
66н	102	CH5 Process alarm lower lower limit value	0	R/W <sup>*2</sup>
67н	103	CH5 Process alarm lower upper limit value	0	R/W <sup>*2</sup>
68н	104	CH5 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
69н	105	CH5 Process alarm upper upper limit value	0	R/W*2
6Ан	106	CH6 Process alarm lower lower limit value	0	R/W*2
6Вн	107	CH6 Process alarm lower upper limit value	0	R/W <sup>*2</sup>
6Сн	108	CH6 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
6Dн	109	CH6 Process alarm upper upper limit value	0	R/W*2
6Ен	110	CH7 Process alarm lower lower limit value	0	R/W*2
6Fн	111	CH7 Process alarm lower upper limit value	0	R/W <sup>*2</sup>
70н	112	CH7 Process alarm upper lower limit value	0	R/W <sup>*2</sup>
71н	113	CH7 Process alarm upper upper limit value	0	R/W <sup>*2</sup>

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

R : Read enabled

W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.

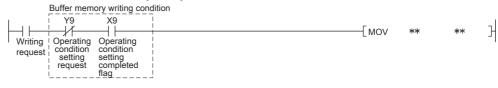




Table3.6 Buffer memory assignment of Q68AD-G (4/6)

Addres	ss	Description	Default	- au*1	
Hexadecimal	Decimal	Description	Detault	R/W*1	
72н	114	CH8 Process alarm lower lower limit value	0	R/W*2	
73н	115	CH8 Process alarm lower upper limit value	0	R/W*2	
74н	116	CH8 Process alarm upper lower limit value	0	R/W*2	
75н	117	CH8 Process alarm upper upper limit value	0	R/W*2	
76н	118	CH1 Rate alarm warning detection period	0	R/W*2	
77н	119	CH2 Rate alarm warning detection period	0	R/W*2	
78н	120	CH3 Rate alarm warning detection period	0	R/W*2	
79н	121	CH4 Rate alarm warning detection period	0	R/W*2	
7Ан	122	CH5 Rate alarm warning detection period	0	R/W <sup>*2</sup>	
7Вн	123	CH6 Rate alarm warning detection period	0	R/W*2	
7Сн	124	CH7 Rate alarm warning detection period	0	R/W*2	
7Dн	125	CH8 Rate alarm warning detection period	0	R/W*2	
7Ен	126	CH1 Rate alarm upper limit value	0	R/W*2	
7Fн	127	CH1 Rate alarm lower limit value	0	R/W*2	
80н	128	CH2 Rate alarm upper limit value	0	R/W*2	
81н	129	CH2 Rate alarm lower limit value	0	R/W*2	
82н	130	CH3 Rate alarm upper limit value	0	R/W*2	
83н	131	CH3 Rate alarm lower limit value	0	R/W*2	
84н	132	CH4 Rate alarm upper limit value	0	R/W*2	
85н	133	CH4 Rate alarm lower limit value	0	R/W*2	
86н	134	CH5 Rate alarm upper limit value	0	R/W*2	
87н	135	CH5 Rate alarm lower limit value	0	R/W*2	
88н	136	CH6 Rate alarm upper limit value	0	R/W <sup>*2</sup>	
89н	137	CH6 Rate alarm lower limit value	0	R/W*2	
8Ан	138	CH7 Rate alarm upper limit value	0	R/W <sup>*2</sup>	
8Вн	139	CH7 Rate alarm lower limit value	0	R/W <sup>*2</sup>	
8Сн	140	CH8 Rate alarm upper limit value	0	R/W*2	
8Dн	141	CH8 Rate alarm lower limit value	0	R/W*2	
8Ен	142	CH1 Input signal error detection setting value/CH1 Input	50	R/W*2	
OLH	142	signal error detection lower limit setting value	30	R/VV -	
8Fн	143	CH2 Input signal error detection setting value/CH2 Input	50	R/W*2	
OI H	140	signal error detection lower limit setting value	30	FX/VV	
90н	144	CH3 Input signal error detection setting value/CH3 Input	50	R/W <sup>*2</sup>	
ЗОП	144	si	signal error detection lower limit setting value	30	K/W <sup>2</sup>
91н	145	145	CH4 Input signal error detection setting value/CH4 Input	50	R/W*2
O III		signal error detection lower limit setting value	00	IV/VV	
92н	146	CH5 Input signal error detection setting value/CH5 Input	50	R/W*2	
JEII	1 10	signal error detection lower limit setting value		17/44	
93н	147	CH6 Input signal error detection setting value/CH6 Input	50	R/W*2	
93н		signal error detection lower limit setting value		17/1/	

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W: Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



<sup>\*3</sup> Areas used to restore the User range settings offset/gain values when the module is replaced online. Refer to Chapter 7 for details of online module change.

Desire	Address				
94H			Description	Default	R/W*1
95H   149   signal error detection lower limit setting value   50   R/W     96H   150   CH1 Input signal error detection upper limit setting value   50   R/W     97H   151   CH2 Input signal error detection upper limit setting value   50   R/W     98H   152   CH3 Input signal error detection upper limit setting value   50   R/W     99H   153   CH4 Input signal error detection upper limit setting value   50   R/W     99H   154   CH5 Input signal error detection upper limit setting value   50   R/W     99H   155   CH6 Input signal error detection upper limit setting value   50   R/W     99CH   156   CH7 Input signal error detection upper limit setting value   50   R/W     99CH   156   CH7 Input signal error detection upper limit setting value   50   R/W     99CH   157   CH8 Input signal error detection upper limit setting value   50   R/W     99FH   158   Mode switching setting   0   R/W     99FH   159   Mode switching setting   0   R/W     90H   160   System area		148	, , , , , , , , , , , , , , , , , , , ,	50	R/W*2
97H         151         CH2 Input signal error detection upper limit setting value         50         R/W²²           98H         152         CH3 Input signal error detection upper limit setting value         50         R/W²²           99H         153         CH4 Input signal error detection upper limit setting value         50         R/W²²           9AH         154         CH5 Input signal error detection upper limit setting value         50         R/W²²           9BH         155         CH6 Input signal error detection upper limit setting value         50         R/W²²           9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W²²           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W²²           9EH         158         Mode switching setting         0         R/W²²           9EH         159         Mode switching setting         0         R/W           9FH         159         Mode switching setting         0         R/W           0BH         159         Mode switching setting         0         R/W           0BH         159         A0h         160         A0h         A0h         A0h         A0h         A0h         A0h	95н	149		50	R/W <sup>*2</sup>
98H         152         CH3 Input signal error detection upper limit setting value         50         R/W*²           99H         153         CH4 Input signal error detection upper limit setting value         50         R/W*²           9AH         154         CH5 Input signal error detection upper limit setting value         50         R/W*²           9BH         155         CH6 Input signal error detection upper limit setting value         50         R/W*²           9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W*²           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W*²           9EH         158         Mode switching setting         0         R/W*²           9EH         158         Mode switching setting         0         R/W           0         159         A0h         160         0         R/W           0         159         A0h         160         0         R/W           0         159         A0h         160         0         R/W           0         200         Save data classification setting**         0         R/W           0         201         System area	96н	150	CH1 Input signal error detection upper limit setting value	50	
99H         153         CH4 Input signal error detection upper limit setting value         50         R/W²           9AH         154         CH5 Input signal error detection upper limit setting value         50         R/W²           9BH         155         CH6 Input signal error detection upper limit setting value         50         R/W²           9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W²           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W²           9EH         158         Mode switching setting         0         R/W²           9EH         159         Mode switching setting         0         R/W           40h         160         to         to         to         co         R/W           AOH         160         to         to         co         R/W           C7H         199         D         System area         —         —         —           C7H         199         201         System area         —         —         —         —         —         —         —         —         —         —         —         —         —         —         — <t< td=""><td>97н</td><td>151</td><td>CH2 Input signal error detection upper limit setting value</td><td>50</td><td>R/W<sup>*2</sup></td></t<>	97н	151	CH2 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9AH         154         CH5 Input signal error detection upper limit setting value         50         R/W²           9BH         155         CH6 Input signal error detection upper limit setting value         50         R/W²²           9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W²²           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W²²           9EH         158         Mode switching setting         0         R/W           40H         160         No         No         No         No           40H         160         No	98н	152	CH3 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9BH         155         CH6 Input signal error detection upper limit setting value         50         R/W*2           9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W*2           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W*2           9EH         158         Mode switching setting         0         R/W           9FH         159         Mode switching setting         0         R/W           A0H         160         to         to         to         review and to	99н	153	CH4 Input signal error detection upper limit setting value	50	R/W*2
9CH         156         CH7 Input signal error detection upper limit setting value         50         R/W*2           9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W*2           9EH         158         Mode switching setting         0         R/W           9FH         159         Mode switching setting         0         R/W           A0H         160         to         to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to to         column and to	9Ан	154	CH5 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9DH         157         CH8 Input signal error detection upper limit setting value         50         R/W*2           9EH         158         Mode switching setting         0         R/W           40H         159         Mode switching setting         0         R/W           A0H         160         Lot         Lot <td>9Вн</td> <td>155</td> <td>CH6 Input signal error detection upper limit setting value</td> <td>50</td> <td>R/W<sup>*2</sup></td>	9Вн	155	CH6 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9EH         158         Mode switching setting         0         R/W           9FH         159         Mode switching setting         0         R/W           A0H         160         to         to         to         System area         —         —           C7H         199         O         Save data classification setting*3         0         R/W           C8H         200         Save data classification setting*3         0         R/W           C9H         201         System area         —         —         —           CAH         202         CH1 Factory default offset value*3         0         R/W           CBH         203         CH1 Factory default offset value*3         0         R/W           CCH         204         CH2 Factory default gain value*3         0         R/W           CDH         205         CH2 Factory default offset value*3         0         R/W           CFH         206         CH3 Factory default offset value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default offset value*3         0         R/W           D3H	9Сн	156	CH7 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9EH         158         Mode switching setting         0         R/W           9FH         159         Mode switching setting         0         R/W           A0H         160         to         to         to         System area         —         —           C7H         199         O         Save data classification setting*3         0         R/W           C8H         200         Save data classification setting*3         0         R/W           C9H         201         System area         —         —         —           CAH         202         CH1 Factory default offset value*3         0         R/W           CBH         203         CH1 Factory default offset value*3         0         R/W           CCH         204         CH2 Factory default gain value*3         0         R/W           CDH         205         CH2 Factory default offset value*3         0         R/W           CFH         206         CH3 Factory default offset value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default offset value*3         0         R/W           D3H	9Dн	157	CH8 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
9FH         159           A0H         160           to         to           C7H         199           C8H         200           Save data classification setting*3         0           C9H         201           System area         —           CAH         202           CH1 Factory default offset value*3         0           CBH         203           CH1 Factory default gain value*3         0           CCH         204         CH2 Factory default offset value*3           CDH         205         CH2 Factory default offset value*3           CEH         206         CH3 Factory default offset value*3           CFH         207         CH3 Factory default gain value*3           D0H         208         CH4 Factory default offset value*3           D1H         209         CH4 Factory default offset value*3           D2H         210         CH5 Factory default offset value*3           D3H         211         CH5 Factory default offset value*3           D4H         212         CH6 Factory default offset value*3           D5H         213         CH6 Factory default offset value*3           D6H         214         CH7 Factory default offset value	9Ен	158	Mode switching cotting	0	
to         to         System area         —         —           C7H         199         —	9Fн	159	inode switching setting	0	R/VV
C7H         199           C8H         200         Save data classification setting*3         0         R/W           C9H         201         System area         —         —           CAH         202         CH1 Factory default offset value*3         0         R/W           CBH         203         CH1 Factory default gain value*3         0         R/W           CCH         204         CH2 Factory default offset value*3         0         R/W           CDH         205         CH2 Factory default gain value*3         0         R/W           CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default gain value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D5H         213         CH6 Factory default offset value*3         0         R/W           D6H	А0н	160		_	_
C8H         200         Save data classification setting "3         0         R/W           C9H         201         System area         —			System area		
C9H         201         System area         —					
CAH         202         CH1 Factory default offset value*3         0         R/W           CBH         203         CH1 Factory default gain value*3         0         R/W           CCH         204         CH2 Factory default offset value*3         0         R/W           CDH         205         CH2 Factory default gain value*3         0         R/W           CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default offset value*3         0         R/W           D4H         212         CH6 Factory default gain value*3         0         R/W           D5H         213         CH6 Factory default offset value*3         0         R/W           D6H         214         CH7 Factory default gain value*3         0         R/W           D7H         215         CH3				0	R/W
CBH         203         CH1 Factory default gain value*3         0         R/W           CCH         204         CH2 Factory default offset value*3         0         R/W           CDH         205         CH2 Factory default gain value*3         0         R/W           CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default offset value*3         0         R/W           D4H         212         CH6 Factory default gain value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default offset value*3         0         R/W           D8H         216         CH8				_	
CCH         204         CH2 Factory default offset value*3         0         R/W           CDH         205         CH2 Factory default gain value*3         0         R/W           CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default offset value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default offset value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default offset value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W					R/W
CDH         205         CH2 Factory default gain value*3         0         R/W           CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W		203			R/W
CEH         206         CH3 Factory default offset value*3         0         R/W           CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	ССн	204		0	R/W
CFH         207         CH3 Factory default gain value*3         0         R/W           D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	СДн	205	CH2 Factory default gain value*3	0	R/W
D0H         208         CH4 Factory default offset value*3         0         R/W           D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	СЕн	206	CH3 Factory default offset value*3	0	R/W
D1H         209         CH4 Factory default gain value*3         0         R/W           D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	СГн	207	CH3 Factory default gain value*3	0	R/W
D2H         210         CH5 Factory default offset value*3         0         R/W           D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	D0н	208	CH4 Factory default offset value*3	0	R/W
D3H         211         CH5 Factory default gain value*3         0         R/W           D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	<b>D</b> 1н	209	CH4 Factory default gain value*3	0	R/W
D4H         212         CH6 Factory default offset value*3         0         R/W           D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	<b>D</b> 2н	210	CH5 Factory default offset value*3	0	R/W
D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	<b>D</b> 3н	211	CH5 Factory default gain value*3	0	R/W
D5H         213         CH6 Factory default gain value*3         0         R/W           D6H         214         CH7 Factory default offset value*3         0         R/W           D7H         215         CH7 Factory default gain value*3         0         R/W           D8H         216         CH8 Factory default offset value*3         0         R/W	<b>D4</b> н	212	CH6 Factory default offset value*3	0	R/W
D6H 214 CH7 Factory default offset value*3 0 R/W D7H 215 CH7 Factory default gain value*3 0 R/W D8H 216 CH8 Factory default offset value*3 0 R/W	<b>D</b> 5н	213	-	0	R/W
D7H 215 CH7 Factory default gain value*3 0 R/W D8H 216 CH8 Factory default offset value*3 0 R/W	D6н	214		0	R/W
D8H 216 CH8 Factory default offset value*3 0 R/W	<b>D7</b> н	215	-	0	R/W
,	<b>D</b> 8н	216	-	0	R/W
	<b>D</b> 9н	217	CH8 Factory default gain value*3	0	R/W

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



<sup>\*3</sup> Areas used to restore the User range settings offset/gain values when the module is replaced online.

Refer to Chapter 7 for details of online module change.



Table3.6 Buffer memory assignment of Q68AD-G (6/6)

Addre	Default	<b>-</b> *1		
Hexadecimal	Decimal	Description	Default	R/W*1
DAн	218	CH1 User range settings offset value*3	0	R/W
DВн	219	CH1 User range settings gain value*3	0	R/W
DCн	220	CH2 User range settings offset value*3	0	R/W
DDн	221	CH2 User range settings gain value*3	0	R/W
DЕн	222	CH3 User range settings offset value*3	0	R/W
DFн	223	CH3 User range settings gain value*3	0	R/W
Е0н	224	CH4 User range settings offset value*3	0	R/W
Е1н	225	CH4 User range settings gain value*3	0	R/W
Е2н	226	CH5 User range settings offset value*3	0	R/W
Е3н	227	CH5 User range settings gain value*3	0	R/W
Е4н	228	CH6 User range settings offset value*3	0	R/W
Е5н	229	CH6 User range settings gain value*3	0	R/W
Е6н	230	CH7 User range settings offset value*3	0	R/W
Е7н	231	CH7 User range settings gain value*3	0	R/W
Е8н	232	CH8 User range settings offset value*3	0	R/W
Е9н	233	CH8 User range settings gain value*3	0	R/W

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W: Write enabled

\*2 When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



<sup>\*3</sup> Areas used to restore the User range settings offset/gain values when the module is replaced online. Refer to Chapter 7 for details of online module change.

#### (2) Buffer memory assignment of Q66AD-DG

# **⊠**Point

Do not write data from system area or sequence program to the buffer memory area where writing is disabled.

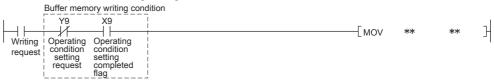
Doing so may cause malfunction.

Table3.7 Buffer memory assignment of Q66AD-DG (1/5)

Tables.7 Butter memory assignment of Q66AD-DG (175)				
Addres Hexadecimal	ss Decimal	Description	Default	R/W*1
Он	0	A/D conversion enable/disable setting	003Fн	R/W <sup>*2</sup>
1н	1	CH1 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
2н	2	CH2 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
3н	3	CH3 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
4н	4	CH4 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
5н	5	CH5 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
6н	6	CH6 Average time/Average number of times/ Moving average/Time constant settings	0	R/W*2
7н	7			_
to	to	System area	_	
9н	9			
Ан	10	A/D conversion completed flag	0	R
Вн	11	CH1 Digital output value	0	R
Сн	12	CH2 Digital output value	0	R
Dн	13	CH3 Digital output value	0	R
Ен	14	CH4 Digital output value	0	R
Fн	15	CH5 Digital output value	0	R
10н	16	CH6 Digital output value	0	R
11н	17	System area		
12н	18			
13н	19	Error code	0	R
14н	20	Setting range(CH1 to CH4)	0	R
15н	21	Setting range(CH5,CH6)	0	R
16н	22	Offset/gain setting mode offset specification	0	R/W
17н	23	Offset/gain setting mode gain specification	0	R/W
18н	24	Averaging process specification (CH1 to CH4)	0	R/W <sup>*2</sup>
19н	25	Averaging process specification (CH5 to CH6)	0	R/W <sup>*2</sup>

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



R: Read enabled

W: Write enabled



Table3.7 Buffer memory assignment of Q66AD-DG (2/5)

Address		Description	Default	R/W*1
Hexadecimal	Decimal	Description	Delauit	R/W ·
1Ан	26			
to	to	System area	_	_
1Dн	29			
1Ен	30	CH1 Maximum value	0	R
1F <sub>H</sub>	31	CH1 Minimum value	0	R
20н	32	CH2 Maximum value	0	R
21н	33	CH2 Minimum value	0	R
22н	34	CH3 Maximum value	0	R
23н	35	CH3 Minimum value	0	R
24н	36	CH4 Maximum value	0	R
25н	37	CH4 Minimum value	0	R
26н	38	CH5 Maximum value	0	R
27н	39	CH5 Minimum value	0	R
28н	40	CH6 Maximum value	0	R
29н	41	CH6 Minimum value	0	R
2Ан	42			
to	to	System area	_	_
2Ен	46			
2Fн	47	Input signal error detection extended/input signal error detection setting	003Fн	R/W*2
30н	48	Warning output setting	3F3Fн	R/W*2
31н	49	Input signal error detection flag	0	R
32н	50	Warning output flag (Process alarm)	0	R
33н	51	Warning output flag (Rate alarm)	0	R
34н	52	System area	_	_
35н	53	Scaling enable/disable setting	003Fн	R/W*2
36н	54	CH1 Scaling value	0	R
37н	55	CH2 Scaling value	0	R
38н	56	CH3 Scaling value	0	R
39н	57	CH4 Scaling value	0	R
3Ан	58	CH5 Scaling value	0	R
3Вн	59	CH6 Scaling value	0	R
3Сн	60	System area		
3Dн	61	- Gyotein area		
3Ен	62	CH1 Scaling lower limit value	0	R/W <sup>*2</sup>
3Fн	63	CH1 Scaling upper limit value	0	R/W*2
40н	64	CH2 Scaling lower limit value	0	R/W*2
41н	65	CH2 Scaling upper limit value	0	R/W*2
42н	66	CH3 Scaling lower limit value	0	R/W*2
43н	67	CH3 Scaling upper limit value	0	R/W*2
44н	68	CH4 Scaling lower limit value	0	R/W*2
45н	69	CH4 Scaling upper limit value	0	R/W <sup>*2</sup>

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



3

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

Table3.7 Buffer memory assignment of Q66AD-DG (4/5)

Addre		Description	Default	R/W*1
Hexadecimal 76 <sub>H</sub>	Decimal 118	CH1 Rate alarm warning detection period	0	R/W*2
77H	119		0	R/W *2
		CH2 Rate alarm warning detection period		
78н	120	CH3 Rate alarm warning detection period	0	R/W <sup>*2</sup>
79н	121	CH4 Rate alarm warning detection period	0	R/W <sup>*2</sup>
7Ан	122	CH5 Rate alarm warning detection period	0	R/W <sup>*2</sup>
7Вн	123	CH6 Rate alarm warning detection period	0	R/W <sup>*2</sup>
7Сн	124	System area	_	_
7Dн	125	·		*2
7Ен	126	CH1 Rate alarm upper limit value	0	R/W <sup>*2</sup>
7Fн	127	CH1 Rate alarm lower limit value	0	R/W <sup>*2</sup>
80н	128	CH2 Rate alarm upper limit value	0	R/W <sup>*2</sup>
81н	129	CH2 Rate alarm lower limit value	0	R/W*2
82н	130	CH3 Rate alarm upper limit value	0	R/W <sup>*2</sup>
83н	131	CH3 Rate alarm lower limit value	0	R/W*2
84н	132	CH4 Rate alarm upper limit value	0	R/W*2
85н	133	CH4 Rate alarm lower limit value	0	R/W*2
86н	134	CH5 Rate alarm upper limit value	0	R/W*2
87н	135	CH5 Rate alarm lower limit value	0	R/W*2
88н	136	CH6 Rate alarm upper limit value	0	R/W <sup>*2</sup>
89н	137	CH6 Rate alarm lower limit value	0	R/W <sup>*2</sup>
8Ан	138			1011
to	to	System area	_	_
8Dн	141			
8Ен	142	CH1 Input signal error detection setting value/CH1 Input	50	R/W*2
OLH	OLH 142	signal error detection lower limit setting value	30	IT/VV
8Fн	143	CH2 Input signal error detection setting value/CH2 Input	50	R/W*2
		signal error detection lower limit setting value		
90н	144	CH3 Input signal error detection setting value/CH3 Input signal error detection lower limit setting value	50	R/W*2
		CH4 Input signal error detection setting value/CH4 Input	<del>                                     </del>	
91н	145	signal error detection lower limit setting value	50	R/W*2
		CH5 Input signal error detection setting value/CH5 Input	50	**
92н	146	signal error detection lower limit setting value	50	R/W*2
93н	147	CH6 Input signal error detection setting value/CH6 Input	50	R/W*2
		signal error detection lower limit setting value	30	FK/ VV =
94н	148	System area		_
95н	149			**
96н	150	CH1 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
97н	151	CH2 Input signal error detection upper limit setting value	50	R/W <sup>*2</sup>
98н	152	CH3 Input signal error detection upper limit setting value	50	R/W*2

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

W: Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals



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Addre	ss	Description	Default	D 04*1
Hexadecimal	Decimal	Description	Derault	R/W <sup>*1</sup>
99н	153	CH4 Input signal error detection upper limit setting value	50	R/W*2
9Ан	154	CH5 Input signal error detection upper limit setting value	50	R/W*2
9Вн	155	CH6 Input signal error detection upper limit setting value	50	R/W*2
9Сн	156	System area		
9Dн	157	System area		
9Ен	158	Mode switching setting	0	R/W
9F <sub>H</sub>	159	mass and mas		
А0н	160	0		
to	to	System area	_	_
С9н	201	*3		
САн	202	CH1 Factory default offset value*3	0	R/W
СВн	203	CH1 Factory default gain value <sup>*3</sup>	0	R/W
ССн	204	CH2 Factory default offset value*3	0	R/W
СДн	205	CH2 Factory default gain value*3	0	R/W
СЕн	206	CH3 Factory default offset value*3	0	R/W
СГн	207	CH3 Factory default gain value*3	0	R/W
D0н	208	CH4 Factory default offset value*3	0	R/W
<b>D</b> 1н	209	CH4 Factory default gain value*3	0	R/W
<b>D2</b> н	210	CH5 Factory default offset value*3	0	R/W
D3 <sub>н</sub>	211	CH5 Factory default gain value*3	0	R/W
<b>D4</b> н	212	CH6 Factory default offset value <sup>*3</sup>	0	R/W
<b>D</b> 5н	213	CH6 Factory default gain value*3	0	R/W
D6н	214			
to	to	System area	_	_
<b>D</b> 9н	217			
DAн	218	CH1 User range settings offset value*3	0	R/W
DВн	219	CH1 User range settings gain value*3	0	R/W
DCн	220	CH2 User range settings offset value*3	0	R/W
DDн	221	CH2 User range settings gain value*3	0	R/W
DЕн	222	CH3 User range settings offset value*3	0	R/W
DFн	223	CH3 User range settings gain value*3	0	R/W
Е0н	224	CH4 User range settings offset value*3	0	R/W
Е1н	225	CH4 User range settings gain value*3	0	R/W
Е2н	226	CH5 User range settings offset value*3	0	R/W
Е3н	227	CH5 User range settings gain value*3	0	R/W
Е4н	228	CH6 User range settings offset value*3	0	R/W
Е5н	229	CH6 User range settings gain value*3	0	R/W

<sup>\*1</sup> Indicates whether reading from and writing to a sequence program are enabled.

R : Read enabled

W : Write enabled

<sup>\*2</sup> When writing data to the buffer memory, always use the interlock condition (buffer memory write condition) of the following I/O signals.



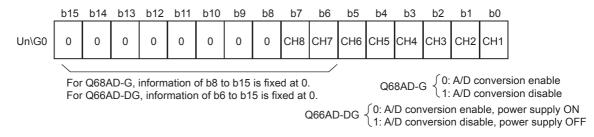
<sup>\*3</sup> Areas used to restore the User range settings offset/gain values when the module is replaced online.

Refer to Chapter 7 for details of online module change.



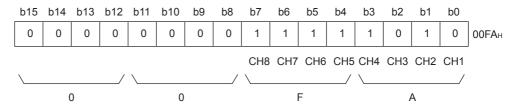
## 3.4.2 A/D conversion enable/disable setting (Un\G0)

- (1) Set whether to ebable or disable A/D conversion for each channel. For the Q66AD-DG, the A/D conversion enable/disable setting also acts as the ON/ OFF setting of the power supply to the 2-wire transmitter. Note that power is supplied to only the 2-wire transmitters whose channels use input ranges of "4 to 20mA (2-wire transmitter input): 0H", "4 to 20mA (Expanded mode) (2-wire transmitter input): AH" or "User range setting (2-wire transmitter input): FH". No power is supplied when any other input range is used.
- (2) It is necessary to set the operating condition setting request (Y9) to ON/OFF in order to validate the A/D conversion enable/disable setting. (Refer to Section 3.3.2.)
- (3) The Q68AD-G is preset to enable A/D conversion on all channels.
- (4) The Q66AD-DG is preset to disable A/D conversion on all channels.



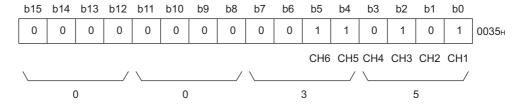
#### [Setting example of Q68AD-G]

When channels for A/D conversion are 1 and 3, 00FAH is stored into the Un\G0.



#### [Setting example of Q66AD-DG]

When channels for A/D conversion are 2 and 4, 0035H is stored into the Un\G0.



- (1) Set the average time, average count, moving average count or primary delay filter time constant for each channel for which averaging processing is specified.
- (2) To validate the setting, the operating condition setting reguest (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) 0 is set as the default.
- (4) The setting ranges are as follows.

Processing method	Setting value				
Time averaging	40 to 5000 (ms)*1				
Count averaging	4 to 500 (times)				
Moving average	2 to 60 (times)				
Primary delay filter	10 to 5000 (ms)*2				

## **⊠**Point

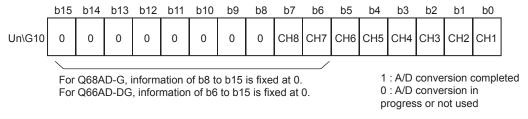
- (1) For the time averaging marked\*1, set a value equal to or greater than (4 times x 10ms x number of channels used). If any insufficient value is set, an error occurs and 0 is stored as a digital output value.
- (2) For the primary delay filter marked\*2, set a value equal to or greater than (10ms x number of channels used). If any insufficient value is set, an error occurs and 0 is stored as a digital output value.
- (3) Writing a value outside the range to a channel will cause an error, storing an error code in Error code (Un\G19) and turning ON the Error flag (XF). If this occurs, A/D conversion is performed based on the setting before the error detection.
- (4) Since the default setting is 0, change it for the selected processing method.
- (5) If a value is set to a sampling-processing channel, the value is ignored.

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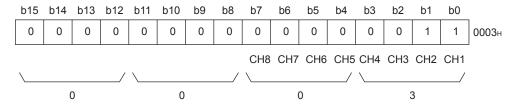
## 3.4.4 A/D conversion completed flag (Un\G10)

- (1) When A/D conversion of a conversion-enabled channel is complete, the A/D conversion completed flag is set to 1.
  - The A/D conversion completed flag (XE) turns ON when conversion for all A/D-conversion-enabled channels is complete.
- (2) When the operating condition setting request (Y9) is set to ON, the flag returns to the default setting of 0, and changes to 1 when A/D conversion is complete.



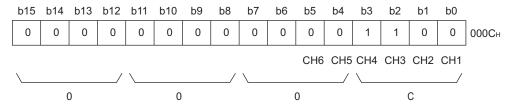
### [Setting example of Q68AD-G]

When all A/D conversions of conversion-enabled channels 1 and 2 are completed, 0003H is stored into the buffer memory address 10 (Un\G10).



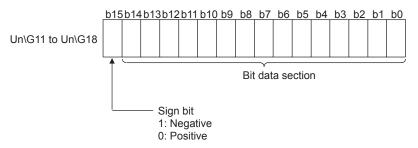
### [Setting example of Q66AD-DG]

When all A/D conversions of conversion-enabled channels 3 and 4 are completed, 000CH is stored into the buffer memory address 10 (Un\G10).



## 3.4.5 CH ☐ digital output value (Un\G11 to Un\G18)

- (1) Digital values converted from analog values are stored for respective channels.
- (2) Digital values are stored in 16-bit signed binary format.



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# 3.4.6 Write data error code (Un\G19)

- (1) An error code generated by the A/D converter module is stored here.
- (2) Refer to Section 8.1 for details of the error codes.

# 3.4.7 Setting range (Un\G20, Un\G21)

(1) These areas are used to confirm the input ranges of respective channels. A value set in the input range setting is stored in the corresponding channel area as shown below.

Un\G20 (Setting range CH1 to CH4)
Un\G21(Setting range CH5 to CH8)

	b15	to	b12	b11 to	b8	b7 to	b4	b3 to	o b0
4)		CH4		CH	3	CH	12	CI	H1
8)		CH8		CH	7	C⊦	16	CI	H5

For Q66AD-DG, information of b8 to b15 is fixed at 0.

### Setting ranges of Q68AD-G

Input range	Setting value
4 to 20 (mA)	0н
0 to 20 (mA)	1н
1 to 5 (V)	2н
0 to 5 (V)	3н
-10 to 10(V)	4н
0 to 10 (V)	5н
4 to 20 (mA) (Expanded mode)	Ан
1 to 5 (V) (Expanded mode)	Вн
User range setting	Fн

### Setting ranges of Q66AD-DG

Input range	Setting value
4 to 20 (mA)	Он
(For 2-wire transmitter input)	OH
4 to 20 (mA) (For current input)	6н
0 to 20 (mA) (For current input)	7н
4 to 20 (mA) (Expanded mode)	Ан
(For 2-wire transmitter input)	АΠ
4 to 20 (mA) (Expanded mode)	Сн
(For current input)	Оп
User range setting	Ен
(For current input)	
User range setting	FH
(For 2-wire transmitter input)	1.11



# 3.4.8 Offset/gain setting mode offset/gain specification (Un\G22, Un\G23)

- (1) Specify the channel to be adjusted for the offset/gain settings.
- (2) Specify the channel to be adjusted with an offset value in Un\G22 and the channel to be adjusted with a gain value in Un\G23.
- (3) Set the offset and gain separately (Set either Un\G22 or Un\G23 to 0). If both are set at the same time, an offset/gain setting mode error (error code 500) occurs.
- (4) Refer to Section 4.6 for the details of the offset/gain settings.

	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
Un\G22 (Offset specification)	0	0	0	0	0	0	0	0	CH8	CH7	СН6	CH5	CH4	СНЗ	CH2	CH1
Un\G23 (Gain specification)	0	0	0	0	0	0	0	0	CH8	CH7	СН6	CH5	CH4	СНЗ	CH2	CH1

For Q68AD-G, information of b8 to b15 is fixed at 0. For Q66AD-DG, information of b6 to b15 is fixed at 0.

1 : Channel set 0 : Invalid

#### 3.4.9 Averaging process specification (Un\G24, Un\G25))

- (1) Specify whether to perform sampling processing or averaging processing (time averaging, count averaging, moving average, or primary delay filter) for each channel.
- (2) To validare the setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) By default, sampling processing(0H) is set for all channels.

Un\G24 (Setting range CH1 to CH4) Un\G25 (Setting range CH5 to CH8)

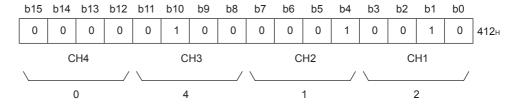
	b15	to	b12	b11 to	b8	b7 to	b4	b3 to	b0
)		CH4		CH	3	CH2		CH1	
		CH8		CH7	7	CH6		CH5	5

For Q66AD-DG, information of b8 to b15 is fixed at 0.

Processing method	Setting value				
Sampling processing	0н				
Time averaging	1н				
Count averaging	2н				
Moving average	3н				
Primary delay filter	4н				

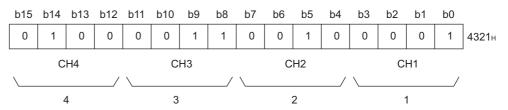
### [Setting example of Q68AD-G]

When setting channel 1 to count averaging, channel 2 to time averaging, channel 3 to primary delay filter, and channel 4 to sampling processing, store 412H into Un\G24.



### [Setting example of Q66AD-DG]

When setting channel 1 to time averaging, channel 2 to count averaging, channel 3 to moving average, and channel 4 to primary delay filter, store 4321H into Un\G24.



# **⊠**Point

When a value outside the above setting range has been written to a channel, sampling processing is applied to the channel.

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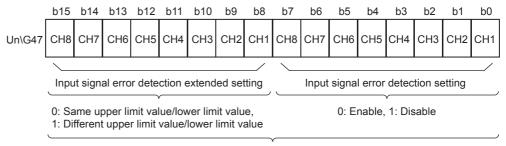


# 3.4.10 CH ☐ maximum value/minimum value storage area (Un\G30 to Un\G45)

- (1) The maximum value and minimum value of converted digital values are stored in 16-bit signed binary format for each channel.
- (2) The stored values for all channels will be cleared to 0 when the operating condition setting request (Y9) is set to ON and the setting is changed or when the maximum value/minimum value reset request (YD) is set to ON.
- (3) The maximum and minimum values are stored at intervals of the sample processing time, even if averaging processing is specified for the channel.
- (4) When the scaling function is enabled, maximum/minimum values after scaling conversion are stored.

# 3.4.11 Input signal error detection extended/input signal error detection setting(Un\G47)

- (1) This area is used to set whether the input signal error detection, process alarm, or rate alarm will be enabled or disabled for each channel. If the warning of input signal error detection is enabled, the input signal error detection can be performed by setting the same value of upper and lower limit or different value of upper and lower limit.
- (2) To validate the input signal error detection extended/input signal error detection setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) All channels for the input signal error detection and all channels for the input signal error detection extended setting are set to the same value of upper limit and lower limit as the default setting.

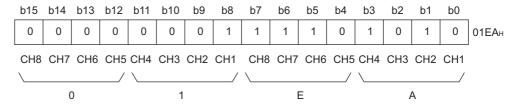


For Q66AD-DG, information of b6, b7, b14, b15 is fixed at 0.

### [Setting example of Q68AD-G]

If the following setting is performed, store 01EAH into Un\G47.

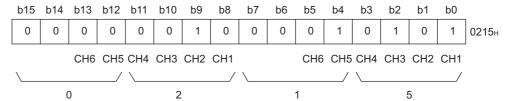
- The channel 1, 3 and 5 specified for input signal error detection are set to 0 (enabled).
- The channel 1 specified for input signal error detection extended setting is set to 1 (different value of lower and upper limit).



### [Setting example of Q66AD-DG]

If the following setting is performed, store 0215H into Un\G47.

- The channel 2, 4 and 6 specified for input signal error detection are set to 0 (enabled).
- The channel 2 specified for input signal error detection extended setting is set to 1 (different value of lower and upper limit).

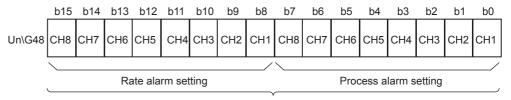


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## 3.4.12 Warning output settings (Un\G48)

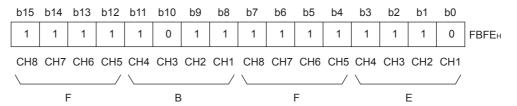
- (1) This area is used to set whether the process alarm/rate alarm warning is to be output or stopped on a channel basis.
- (2) To validate the warning output setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) By default, all channels are set to disable.



0: Enable, 1: Disable For Q66AD-DG, information of b6, b7, b14, b15 is fixed at 0.

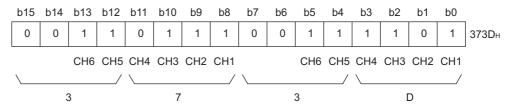
### [Setting example of Q68AD-G]

When process alarm warning output is enabled for channel 1 and rate alarm warning output is enabled for channel 3, FBFEH is stored into Un\G48.



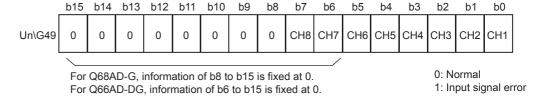
### [Setting example of Q66AD-DG]

When process alarm warning output is enabled for channel 2 and rate alarm warning output is enabled for channel 4, 373DH is stored into Un\G48.



### 3.4.13 Input signal error detection flag (Un\G49)

- (1) If the analog input value detected falls outside the setting range sets to the CH□ input signal error detection setting value/CH□ Input signal error detection lower limit setting value (Un\G142 to Un\G149), or CH□ input signal error detection upper limit setting value (Un\G150 to Un\G157), the Input signal error detection flag for the corresponding channel turns to 1.
- (2) By bringing the analog input value within the setting range and turning ON the Error clear request (YF), the Input signal error detection flag turns OFF.
- (3) If an error is detected on any one of the channels for which input signal error detection is enabled, the Input signal error detection signal (XC) also turns ON.
- (4) When the operating condition setting request (Y9) is turned ON, the Input signal error detection flag is cleared.





### 3.4.14 Warning output flag (Un\G50,Un\51)

- (1) If the digital output value or its varying rate falls outside the setting range set to the CH□ process alarm upper/lower limit value (Un\G86 to Un\G117) or CH□ rate alarm upper/lower limit value (Un\G126 to Un\G141), the warning output flag for the corresponding channel turns to 1.
- (2) For both the process alarm and rate alarm, whether the warning is for the upper or lower limit value can be checked on a channel basis.
- (3) When the digital output value or its varying rate returns to within the setting range, the warning output flag is automatically reset.
- (4) If a warning is detected on any one of the channels for which A/D conversion and process alarm or rate alarm warning output are enabled, the Warning output signal (X8) also turns ON.
- (5) When the operating condition setting request (Y9) is turned ON, the warning output flag is cleared.

Un\G50(Process alarm)

b15	b14	b13	b12	b11	b10	b9	b8	b/	b6	b5	b4	b3	b2	b1	bU
CH8	CH8	CH7	CH7	CH6	CH6	CH5	CH5	CH4	CH4	CH3	СНЗ	CH2	CH2	CH1	CH1
Upper limit value	limit	Lower limit value	limit	Lower limit value	Upper limit value	Lower limit value	Upper limit value	Lower limit value	Upper limit value	Lower limit value	Upper limit value	Lower limit value		Lower limit value	Upper limit value

For Q66AD-DG, information of b12 to b15 is fixed at 0.

b15 b14 b13 b12 b11 b10 b9

0: Normal, 1: Alarm ON

h1

hΩ

h3

h2

Un\G51(Rate alarm)

0.0	~	2.0	٠.ـ	~	0.0	50	50	υ,	20	50	~ .	50	~-	٠.	20
CH8	CH8	CH7	CH7	CH6	CH6	CH5	CH5	CH4	CH4	СНЗ	СНЗ	CH2	CH2	CH1	CH1
Upper limit value		Lower limit value		Lower limit value	Upper limit value										

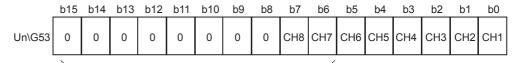
h8

For Q66AD-DG, information of b12 to b15 is fixed at 0.

0: Normal, 1: Alarm ON

# 3.4.15 Scaling enable/disable setting (Un\G53)

- (1) Whether to enable or disable the scaling function for each channel is set in this area.
- (2) To validate the scaling function, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) All channels are defaulted to "Disable".

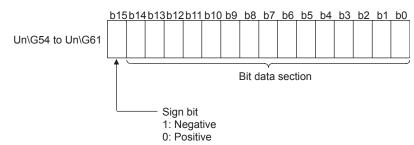


For Q68AD-G, information of b8 to b15 is fixed at 0. For Q66AD-DG, information of b6 to b15 is fixed at 0.

# **⊠**Point

When the Scaling enable/disable setting (Un\G53) is set to "Disable", 0s are stored in the CH□ scaling value storage area (Un\G54 to Un\G61).





## 3.4.17 CH ☐ scaling upper / lower limit value (Un\G62 to Un\G77)

- (1) Set a scaling conversion range for each channel.
- (2) To validate the setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) The setting range is -32000 to 32000.
- (4) Refer to Section 3.2.6 for details of the scaling function.

# **⊠**Point

- (1) Setting a value outside the above setting range or a value that does not meet the inequality "Upper limit > Lower limit" will cause an error. If this occurs, an error code is stored in Error code (Un\G19) followed by ON of the Error flag (XF), and the module will operate under the setting before the error.
- (2) Since the default setting is 0, change the setting.
- (3) When the Scaling enable/disable setting (Un\G53) is set to "Disable", scaling upper/lower limit values are ignored.

# 3.4.18 CH ☐ conversion starting time setting (for 2-wire transmitter) (Un\G78 to Un\G83) (Q66AD-DG only)

- (1) This area is used to set the "time necessary from when the used 2-wire transmitter powers on until its output stabilizes" on a channel basis. This setting is valid only for the channels that use input ranges of "4 to 20mA (2-wire transmitter): 0<sub>H</sub>" or "User range setting (2-wire transmitter): F<sub>H</sub>", and that are set as conversion-enabled in the A/D conversion enable/disable setting (Un\G0). It is ignored in the case of any other setting.
- (2) To validate the setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)

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(3) The setting range is 0 to 3276.7 seconds (0 to 54 minutes and 36.7 seconds) [0 to 32767].

Set the time in 100ms increments.

Example) When setting the A/D conversion starting time to 5 seconds, store 50 into the buffer memory.

- (4) The default is set to 3 seconds [30].
- (5) Refer to Section 3.2.5 for details of the conversion starting time setting function.

## **⊠**Point

If a value outside the above setting range is written to a channel, an error occurs, and an error code is stored into the Error code (Un\G19). In this case, the operation is performed based on the setting before the error detection.

# 3.4.19 CH ☐ process alarm upper/lower limit value (Un\G86 to Un\G117)

- (1) Set the range of the digital output value on a channel basis.
- (2) To validate the setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) The setting range is -32768 to 32767.
- (4) Make four kinds of settings: process alarm upper upper limit value, upper lower limit value, lower upper limit value and lower lower limit value.
- (5) Refer to Section 3.2.4 for details of the process alarm.

### ⊠ Point

- (1) If a value outside the above setting range is set or if a value that does not satisfy the condition of "lower lower limit value ≤ lower upper limit value ≤ upper lower limit value ≤ upper upper limit value" is set, it results in an error. An error code is stored into the Error code (Un\G19), the Error flag (XF) turns ON, and operation is performed based on the setting before the error detection.
- (2) Since the default setting is 0, change the setting.
- (3) When "Enable" is set in the Scaling enable/disable setting (Un\G53), always take into account the scaling conversion before setting values.

#### 3.4.20 CH ☐ rate alarm warning detection period (Un\G118 to Un\G125)

- (1) Set a period, with which the varying rate of the digital output value will be checked, on a channel basis.
- (2) To validate the setting, the operating condition setting reguest (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) The setting range is 10 to 5000ms. The value can be set in 1ms increments, but the first digit is rounded down and the value is processed in 10ms increments.
- (4) When time averaging or count averaging has been specified for averaging process specification, set the rate alarm warning detection period as a multiple of the time averaging or count averaging conversion period.
  - Example) When the number of channels is 5, and if the count value set for the count averaging is 10, the conversion cycle for count averaging is: 10 (times)  $\times$  5 (CH)  $\times$  10 (ms) = 500 (ms) Therefore, set a multiple of 500, such as 1500 or 3000, to the rate alarm warning detection period.
- (5) The default setting is 0ms.
- (6) Refer to Section 3.2.4 for details of the rate alarm.

# **⊠**Point

- (1) If a value outside the above setting range is written to a channel, an error occurs, and an error code is stored into the Error code (Un\G19). The Error flag (XF) turns ON, and the time or count averaging or rate alarm processing is performed based on the setting before the error detection.
- (2) Since the default setting is 0, change the setting.
- (3) If the upper limit value and lower limit value settings of the rate alarm are small, the warning output may turn ON due to overreaction to disturbance or like. This overreaction can be avoided by increasing the setting of the rate alarm warning detection period.

# 3 SPECIFICATIONS



## 3.4.21 CH ☐ rate alarm upper/lower limit value (Un\G126 to Un\G141)

- (1) Set the varying rate range of the digital output value on a channel basis.
- (2) To validate the setting, the operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) The setting range is -32768 to 32767 (-3276.8 to 3276.7%). Set the value in 0.1%/s increments.
  - Example) When setting the rate alarm upper limit value to 30%/s, store 300 into the buffer memory.
- (4) Refer to Section 3.2.4 for details of the rate alarm.

3.4.22	CH ☐ input signal error detection setting value/CH ☐ Input signal error detection lower limit setting value (Un\G142 to Un\G149)
	CH ☐ Input signal error detection upper limit setting value (Un\G150 to Un\G157)

- (1) Set the value (upper limit setting value and lower limit setting value), by which an error of the input analog value will be detected, on a channel basis. The setting value (upper limit value and lower limit value) depends on the input signal error detection extended setting.
  - (a) When Same upper limit value/lower limit value is selected.
    - Set the value of the input signal error detection setting for CH□ input signal error detection setting (Un\G142 to Un\G149).
  - (b) When Different upper limit value/lower limit value is selected.
    - Set the value of the input signal error detection lower limit for CH
       Input signal error detection lower limit setting value (Un\G142 to Un\G149).
    - Set the value of the input signal error detection upper limit for CH□ Input signal error detection upper limit setting value (Un\G150 to Un\G157).
- (2) To validate the setting, the Operating condition setting request (Y9) must be turned ON/OFF. (Refer to Section 3.3.2.)
- (3) The setting range is 0 to 250 (0 to 25.0%). Set the value in 0.1% increments. Example) When setting the input signal error detection setting value to 15%, store 150 into the buffer memory.

If 251 is entered in the CH $\square$  Input signal error detection upper limit setting value and CH $\square$  Input signal error detection lower limit setting value boxes after Different upper limit value/lower limit value is selected for the input signal error detection extended setting, the detection function for the upper and lower limits can be disabled.



- (4) Based on this input signal error detection setting value, the input signal upper and lower limit values are calculated as shown below.
  - The calculated values vary depending on the input range.
  - (a) Input signal error detection upper limit value
    - = gain value of corresponding range + (gain value of corresponding range offset value of corresponding range) x {input signal error detection setting value (Input signal error detection upper limit setting value) /1000}
  - (b) Input signal error detection lower limit value
    - = lower limit value of corresponding range\*1 + (gain value of corresponding range offset value of corresponding range) × {input signal error detection setting value (Input signal error detection lower limit setting value) /1000}
    - \*1 The lower limit value of corresponding range in using the setting range from 4 to 20mA (expanded mode), 1 to 5V (expanded mode) is equivalent to the one of corresponding range from 4 to 20mA and 1 to 5V.

When using the user range setting, the lower limit value is as follows:

	analog value when the digital value is the following.						
Q68AD-G	• -4000 (Normal resolution mode)						
	• -12000 (High resolution mode)						
Q66AD-DG	Offset value						

[Example When same upper limit value/lower limit value is selected for the input signal error detection extended setting, setting 15% (150) to the input signal error detection setting value in the Q68AD-G]

Resolution mode: High resolution mode

Used range : User range setting (Offset value: 5mA, Gain value: 18mA) In this setting, because the lower limit value is an analog value at the time the digital value is -12000, it is -8mA.

Therefore, the input signal error detection upper and lower limit values are as follows:

Input signal error detection upper limit value =  $18 + (18-5) \times 150/1000 = 19.95$ mA Input signal error detection lower limit value =  $-8 - (18-5) \times 150/1000 = -9.95$ mA

(5) Refer to Section 3.2.3 for details of the input signal error detection function.

# **⊠**Point

- (1) Set the input signal error detection upper limit value to less than 25mA. If the setting is 25mA or more, the error may not be detected.
- (2) If a value outside the setting range is set, an error occurs and an error code is stored in the Error code (Un\G19). In this case, the operation is performed based on the setting before the error detection.

- (1) Set values for the mode desired to be switched to.
- (2) After setting the values, turning the operating condition setting request (Y9) from OFF to ON switches to that mode.
- (3) When mode switching is performed, this area is cleared to zero and the operating condition setting completed flag (X9) turns OFF.
- (4) After confirming that the operating condition setting completed flag (X9) has turned OFF, turn OFF the operating condition setting request (Y9).

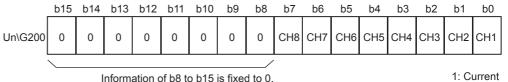
Mode to be switched to	Set value							
Mode to be switched to	Buffer memory address 158	Buffer memory address 159						
Normal mode	0964н	4144н						
Offset/gain setting mode	4144н	0964н						

## **⊠**Point

If any value other than the above is written, mode switching is not performed and only the operating condition is changed.

#### 3.4.24 Save data classification setting (Un\G200) (Q68AD-G only)

- (1) This area is used to restore the User range settings offset/gain values when the module is replaced online.
  - Refer to Chapter 7 for details of online module change.
- (2) Specify whether the offset/gain values to be saved/restored are voltages or currents when saving/restoring the offset/gain values of the user range setting.



Information of b8 to b15 is fixed to 0.

0: Voltage

# **⊠**Point

Refer to Section 4.6 for the offset/gain value setting method.

**3** - 67



# 3.4.25 Factory default and User range settings offset/gain value (Un\G202 to Un\G233)

(1) The areas are used to restore the User range settings offset/gain values when the module is replaced online.

Refer to Chapter 7 for details of online module change.

(2) When the offset/gain values of the user range setting are restored, the used data are stored.

The data are stored (saved) when:

- · Initial setting is written by the utility;
- The operating condition is set (Y9 turns from OFF to ON \* 1); or
- The offset/gain values are written in the offset/gain setting mode (YA turns from OFF to ON).
- \*1: The data are not saved when values have been written to the mode switching setting area (Un\G158, Un\G159).
- (3) When restoring the offset/gain values of the user range setting, set the data saved here into the corresponding area of the module where the data will be restored.
- (4) Buffer memory saving recording procedure for online module change
  - 1) Set the save data classification setting\* (Un\G200).
  - 2) Turn the operating condition setting request (Y9) from OFF to ON.
  - 3) Compare the offset/gain values of the Factory default and User range settings (Un\G202 to Un\G233) with the range reference values. Refer to Section 7.4 for the range reference values.
  - 4) If the values are proper, record the values of the save data classification setting\*, Factory default and User range settings offset/gain value.
  - \*: The Q66AD-DG does not require the setting and recording of the save data classification setting.



Refer to Section 4.6 for the offset/gain value setting method.

SETUP AND PROCEDURES BEFORE OPERATION

# 4.1 Handling Precautions

- (1) Do not drop the module or subject it to heavy impact.
- (2) Do not remove the PCB of the module from its case. Doing so may cause the module to fail.
- (3) Be careful not to let foreign particles such as swarf or wire chips enter the module. They may cause a fire, mechanical failure or malfunction.
- (4) The top surface of the module is covered with a protective film to prevent foreign objects such as wire burrs from entering the module during wiring. Do not remove this film until the wiring is complete. Before operating the system, be sure to remove the film to provide adequate ventilation.
- (5) Tighten the screws such as module fixing screws within the following ranges. Loose screws may cause short circuits, failures, or malfunctions.

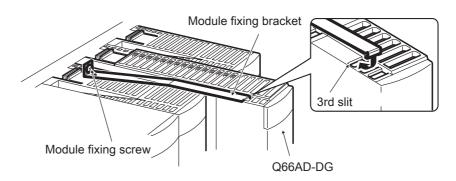
Screw location	Tightening torque range
Module fixing screw (M3 screw)	0.36 to 0.48 N·m
Connector screw (M2.6 screw)	0.20 to 0.29 N·m
FG terminal screw (M3 screw)	0.42 to 0.58 N·m

- (6) To mount the module on the base unit, fully insert the module fixing latch into the fixing hole in the base unit and press the module using the hole as a fulcrum. Improper installation may result in a module malfunction, or may cause the module to fall off.
- (7) Hold the Q66AD-DG with module fixing bracket after the Q66AD-DG is mounted to the base unit.

# **⊠**Point

Make sure that the module fixing bracket is hooked on the 3rd slit viewed from the front of Q66AD-DG.

And tighten the module fixing screw at the specified torque.

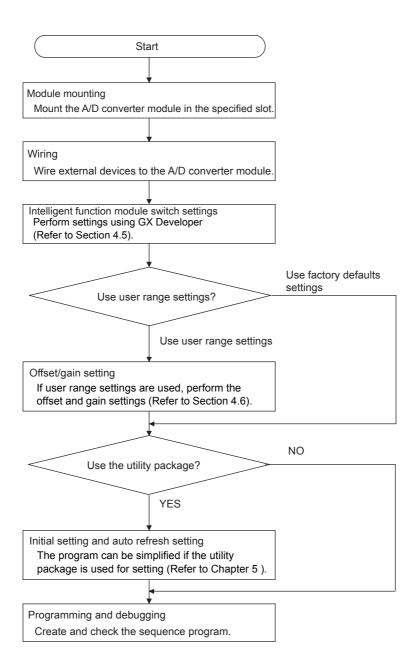


V OVERVIEW

4



# 4.2 Setup and Procedures before Operation



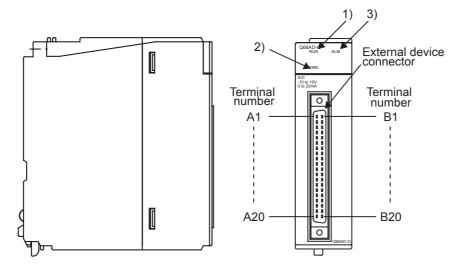
# SETUP AND PROCEDURES BEFORE OPERATION



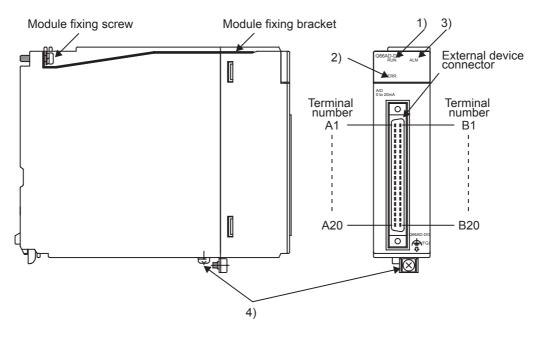
### 4.3 Part Names

The name of each part of the A/D converter module is shown below.

### (1) Q68AD-G



### (2) Q66AD-DG



# SETUP AND PROCEDURES BEFORE OPERATION



Number	Name and appearance	Description	
		Displays the operating status of the A/D converter module.	
	RUN LED	On : Normal operation	
1)		Flashing: During offset/gain setting mode	
		Off : 5V power supply interrupted, watchdog timer error occurred, or	
		online module change enabled.	
	Displays the error status of the A/D converter module.		
		On : Error	
2)	ERR. LED	Flashing: Error in switch settings. Switch No. 5 of the intelligent function	
		module has been set to a value other than zero.	
		Off : Normal operation	
		Displays the alarm status of the A/D converter module.	
3)	ALM LED	On : An alarm (process alarm, rate alarm) occurred.	
3)	ALIVICED	Flashing: An input signal error occurred.	
		Off : Normal operation	
	FG terminal L-Shaped	Metal fitting to wire for FG of the Q66AD-DG.	
4)	metal fitting (Q66AD-DG		
	only)		

<sup>\*</sup> Check the error code for details.

# ⊠Point

When two or more errors have occurred, the latest error found by the A/D converter module is indicated with the LED.

A1 A2 A3 A4 A5

A6 Α7

A8 A9 A10 A11

A12

A14

A15 A16 A17 A18

A19

A20

B2 B3 B4 0 0

В7

B8 B9 B10

B11

B12

B14

B15 B16 B17

B18

B19

0 0 B5

0 0

0 0

Seen from the front

of the module

(1) Q68AD-G

Terminal	Signal name	Terminal	Signal name	
number	Signal name	number		
A1	CH1 V +	B1	CH1 V -/I -	
A2	CH1 I +	B2	-	
A3	-	В3	CH2 V +	
A4	CH2 V -/I -	B4	CH2 I +	
A5	-	B5	-	
A6	CH3 V +	B6	CH3 V -/I -	
A7	CH3 I +	B7	-	
A8	-	B8	CH4 V +	
A9	CH4 V -/I -	B9	CH4 I +	
A10	-	B10	-	
A11	CH5 V +	B11	CH5 V -/I -	
A12	CH5 I +	B12	-	
A13	-	B13	CH6 V +	
A14	CH6 V -/I -	B14	CH6 I +	
A15	-	B15	-	
A16	CH7 V +	B16	CH7 V -/I -	
A17	CH7 I +	B17	-	
A18	-	B18	CH8 V +	
A19	CH8V -/I -	B19	CH8 I +	
A20	-	B20	-	

(2) Q66AD-DG

Terminal	Signal name	Terminal	Signal name	
number	Olgilai Haille	number		
A1	CH1 P	B1	CH1 I +/CHK +	
A2	-	B2	CH1 I -/CHK -	
A3	-	В3	-	
A4	CH2 P	B4	CH2 I +/CHK +	
A5	-	B5	CH2 I -/CHK -	
A6	-	B6	-	
A7	CH3 P	B7	CH3 I +/CHK +	
A8	-	B8	CH3 I -/CHK -	
A9	-	B9	-	
A10	CH4 P	B10	CH4 I +/CHK +	
A11	-	B11	CH4 I -/CHK -	
A12	-	B12	-	
A13	CH5 P	B13	CH5 I +/CHK +	
A14	-	B14	CH5 I -/CHK -	
A15	-	B15	-	
A16	CH6 P	B16	CH6 I +/CHK +	
A17	-	B17	CH6 I -/CHK -	
A18	-	B18	-	
A19	DC24V	B19	DC24V	
A20	DC24G	B20	DC24G	

: Power supply for 2-wire transmitter

I+/CHK+ : 2-wire transmitter current input,

Current(+) input / check (+) terminal

I-/CHK-: Current(-) input / check (-) terminal

4.3 Part Names



## 4.4 Wiring

The wiring precautions and examples of module connection are provided below.

### 4.4.1 Wiring precautions

In order to optimize the functions of the A/D converter module and ensure system reliability, external wiring that is protected from noise is required. Please observe the following precautions for external wiring:

- (1) Use separate cables for the AC control circuit and the external input signals of the Q68AD-G to prevent influences of AC surge or induction.
- (2) Use separate cables for the AC control circuit, the external input signals and external supply power of the Q66AD-DG to avoid influences of AC side surge or induction.
- (3) Keep a distance among the main circuit line, a high-voltage cable and a load cable from other than the programmable controller. Failure to do so may increase the effects of noise, surges and induction.
- (4) The shield wire or the shield of the shielded cable must be grounded at one end.
- (5) When wiring to the module placed on the right side of the Q66AD-DG is difficult, remove the Q66AD-DG before wiring.

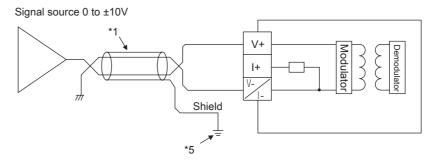
MELSEG Q series

# SETUP AND PROCEDURES BEFORE OPERATION

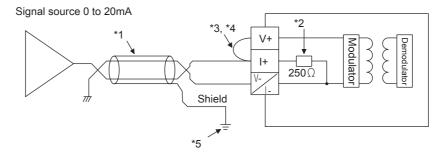


### (1) Q68AD-G

### (a) For voltage input



### (b) For current input



- Use a 2-core twisted shielded wire for the power wire.
- \*2: Shows input resistance.
- For current input, be sure to connect (V+) and (I+) terminals. \*3:
- Connect (V+) terminal to (I+) terminal in the external device connection connector (A6CON4) to reduce resistance of the connection conductor.
- \*5: Always ground the shield of the wire of each channel.

# Remark •

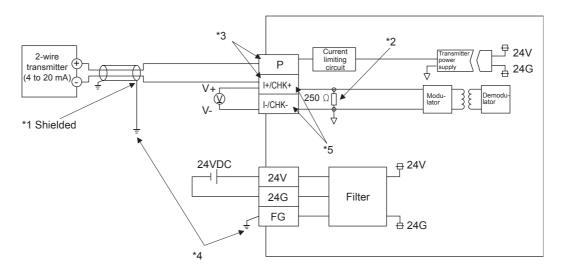
If the external wiring is disconnected during use of voltage input on the Q68AD-G, depending on the internal circuit characteristics, a certain time is required until the digital output reaches a value equivalent to 0V.

To avoid the phenomenon, connect a resistor (approximately  $1M\Omega$ ) across (V+) and (V-) terminals.

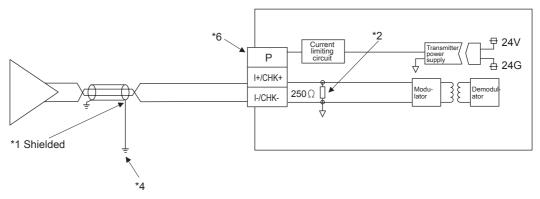


### (2) Q66AD-DG

### (a) For 2-wire transmitter input



### (b) For current input



- \*1 Use a 2-core twisted shielded wire for the power wire.
- \*2 Shows input resistance.
- \*3 To connect with the 2-wire transmitter, be sure to connect to P and I+/CHK+.
- \*4 Always ground the shield of the wire of each channel.
- \*5 The check terminals (CHK+, CHK-) are used to check the amount of input in mA in relation to the 2-wire transmitter output.

This can be checked since analog inputs of 4 to 20mA are converted to analog outputs of 1 to 5V. The relationship of this conversion can be expressed by the following formula:

Analog output(V) = 
$$\frac{\text{Analog input(mA)}}{1000} \times 250 \Omega$$

\*6 Do not connect any devices to the P terminal because the transmitter power supply is always on.



The Q66AD-DG needs to powered on 30 minutes prior to operation for compliance to the specification (accuracy).

Therefore, power on 30 minutes prior to offset/gain setting or after online module change.

4-8

#### **Intelligent Function Module Switch Setting** 4.5

SETUP AND PROCEDURES BEFORE OPERATION

The intelligent function module switches are set using the I/O assignment settings of GX Developer.

### (1) Setting item

The intelligent function module switches consist of switches 1 to 5 and are set using 16-bit data. When the intelligent function module switches are not set, the default value for switches 1 to 5 is 0.

Table4.1 Switch setting item

	Tab	le4.1 Switch setting item	
Switch No.		Setting item	
Q68AD-G			
		Analog input range	Input range setting value
		4 to 20mA	0H
	Input range setting	0 to 20mA	1н
Switch 1	(CH1 to CH4)	1 to 5V	2н
	CH4 CH3 CH2 CH1	0 to 5V	3н
	Cn4 Cn3 Cn2 Cn1	-10 to 10V	4н
		0 to 10V	5н
		4 to 20mA (Expanded mode)	Ан
		1 to 5V (Expanded mode)	Вн
		User range setting	FH
		Q66AD-DG	
		Analog input range	Input range setting value
		4 to 20mA (For 2-wire transmitter input)	ОН
	Input range setting	4 to 20mA (For current input)	6н
	(CH5 to CH8)	0 to 20mA (For current input)	<b>7</b> H
Switch 2	L L L H CH8 CH7 CH6 CH5 Fixed at 00H for Q66AD-DG	4 to 20mA (Expanded mode) (For 2-wire transmitter input)	Ан
		4 to 20mA (Expanded mode) (For current input)	Сн
		User range setting (For current input)	Ен
		User range setting (For 2-wire transmitter input)	Fн
Switch 3		Empty	
		1.9	
Switch 4	Он: N 1н tc	Fixed Normal resolution mode o Fн (value other than 0н)*: High resolution Normal mode (A/D conversion processing) o Fн (value other than 0н)*: Offset/gain setti	
Switch 5		0н : Fixed	

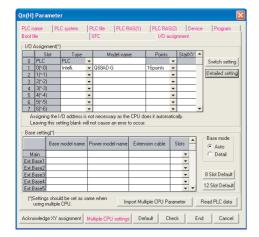
Setting any value within the setting range will provide the same operation. When the setting range is 1H to FH, set 1H for example.

# SETUP AND PROCEDURES BEFORE OPERATION



### (2) Operating procedure

Start the settings with GX Developer I/O assignment setting screen.



(a) I/O assignment setting screen Set the following for the slot in which the A/D converter module is mounted.

The type setting is required; set other items as needed.

Type : Select "intelli."

Model name : Enter the module model name.

Points : Select 16 points.

Start : Enter the start I/O number for

the A/D converter module.

Detailed : Specify the control PLC for the

setting A/D converter module.

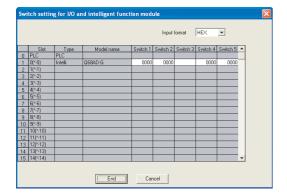
It is unnecessary to set the "Error time output mode" or "H/W error time PLC operation mode" since these settings are invalid for the A/D converter

module.

(b) Switch setting for intelligent function module screen

Click on [Switch setting] on the I/O assignment setting screen to display the screen shown at left, then set switches 1 to 5.

The switches can easily be set if values are entered in hexadecimal. Change the entry format to hexadecimal and then enter the values.



# SETUP AND PROCEDURES BEFORE OPERATION

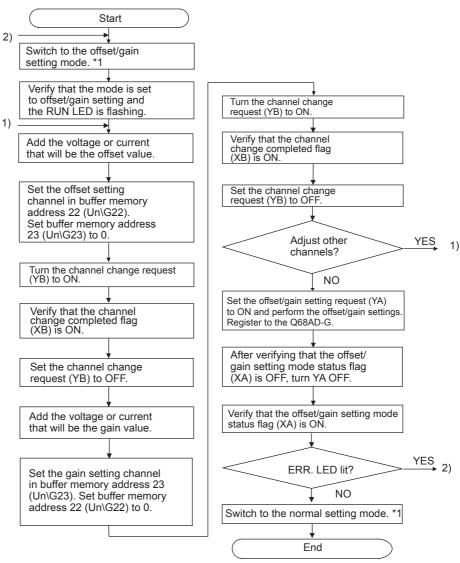


When using the user range setting, make the offset/gain setting according to the operation indicated in Section 4.6.1 or Section 4.6.2.

When the Factory default is used, offset/gain setting is not necessary. If the utility package is installed, perform the offset/gain settings according to the procedure described in Section 5.6.2.

## 4.6.1 Offset/gain settings (Q68AD-G)

### (1) Offset/gain setting procedure



- \*1 The mode switching (normal mode to offset/gain setting mode to normal mode) method is given below.
  - Dedicated instruction (G(P).OFFGAN) ......Refer to Section 4.6.1 (2) (a)

  - Intelligent function module switch setting ....... Refer to Section 4.5, Section 4.6.1 (2) (c)
     (After intelligent function module switch setting, reset the programmable controller CPU or
     switch power OFF, then
     ON.)

## SETUP AND PROCEDURES BEFORE OPERATION



# **⊠**Point

- (1) Perform the offset/gain settings in the range that satisfies the conditions specified in POINT of Section 3.1.2 (2).
  - When the setting exceeds this range, the maximum resolution or total accuracy may not be within the range indicated in the performance specification.
- (2) Though the offset/gain settings can be performed on multiple channels at the same time, set the offset and gain separately (0 at either of the buffer memory addresses 22, 23).
  - If channels are set at Un\G22 and Un\G23 at the same time, an error will occur and the ERR. LED will be lit.
- (3) After the offset/gain settings are completed, verify that the offset and gain values have been set correctly under actual usage conditions.
- (4) The offset and gain values are stored into the Flash memory and are not erased at power-off.
- (5) At the time of offset/gain setting, turn ON the user range write request (YA) to write the values to the Flash memory.
  - Data can be written to the Flash memory up to 100 thousand times. To prevent accidental write to the Flash memory, an error will occur and the error code Un\G19 will be stored if write is performed 26 consecutive times.
- (6) If an error (error code: 40 □<sup>\*1</sup>) occurs during offset/gain setting, re-set the correct offset/gain value.
  - The offset/gain value of the channel where the error has occurred is not written to the module.(\*1: ☐ indicates the corresponding channel number.)
- (7) Module ready (X0) turns from OFF to ON when the offset/gain setting mode switches to the normal mode by the dedicated instruction (G(P).OFFGAN) or the setting of the mode switching setting (Un\G158, Un\G159). Note that initial setting processing will be executed if there is a sequence program that makes initial setting when module ready (X0) turns ON.
- (8) Un\G200, Un\G202 to Un\G233 are the areas used to restore the User-set offset/gain values when online module change is made. Refer to Chapter 7 for details of online module change.

#### (2) Program examples

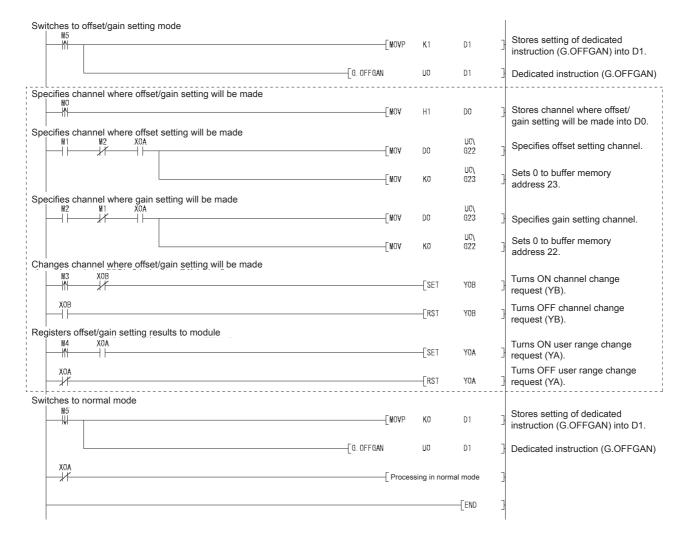
The program in the dotted area of (a) is common to (a), (b) and (c). Is this example, the I/O signals for the Q68AD-G are X/Y0 to X/YF

•	Channel selection	M0
•	Offset setting	M1
•	Gain setting	M2
•	Channel change command	М3
•	Offset/gain setting value write command to the module $\ldots\ldots$	M4
•	Mode switching	M5
•	Channel designation storage device	D0
•	Dedicated instruction (G(P).OFFGAN) setting storage device	D1

# SETUP AND PROCEDURES BEFORE OPERATION



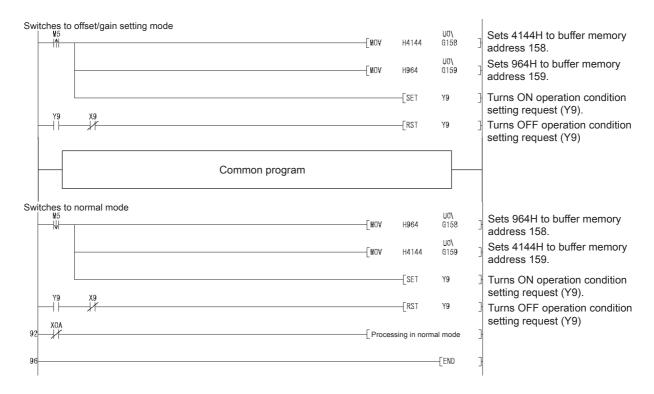
(a) When switching the mode using the dedicated instruction (G(P).OFFGAN) The following program switches to the offset/gain setting mode with the dedicated instruction (G(P).OFFGAN), changes the channel where offset/gain setting will be made, writes the offset/gain values to the Q68AD-G, and then switches to the normal mode.



# SETUP AND PROCEDURES BEFORE OPERATION



(b) When switching the mode using the setting of the mode switching setting (Un\G158, Un\G159) and operation condition setting request (Y9)

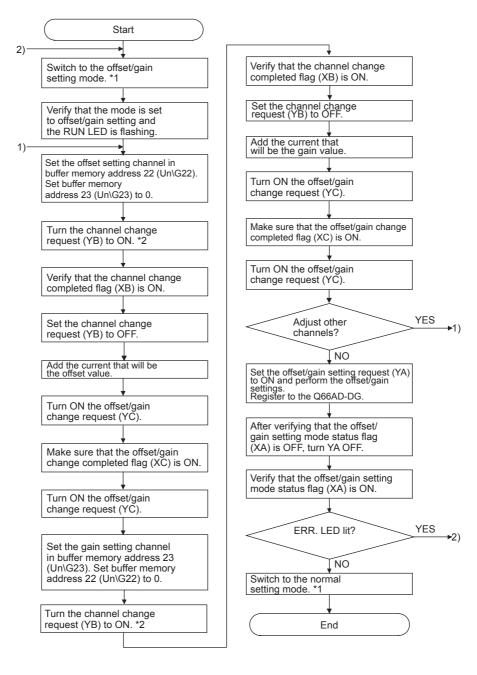


(c) When switching the mode by making intelligent function module switch setting Only the common program is necessary.

## 4.6.2 Offset/gain settings (Q66AD-DG)

SETUP AND PROCEDURES BEFORE OPERATION

### (1) Offset/gain setting procedure



# SETUP AND PROCEDURES BEFORE OPERATION



- \*1 The mode switching (normal mode to offset/gain setting mode to normal mode) method is given below.
  - Dedicated instruction (G(P).OFFGAN) ...... Refer to Section 4.6.2 (2) (a)
  - Setting made to mode switching setting (Un\G158, Un\G159) and turning the operation condition setting request (Y9) from OFF to ON ....... Refer to Section 4.6.2 (2) (b)
- \*2 Turning ON the channel change request (YB) starts power supply from the corresponding channel to the 2-wire transmitter. After fully checking the wiring, settings, etc., turn ON the channel change request (YB).

# SETUP AND PROCEDURES BEFORE OPERATION



## **⊠**Point

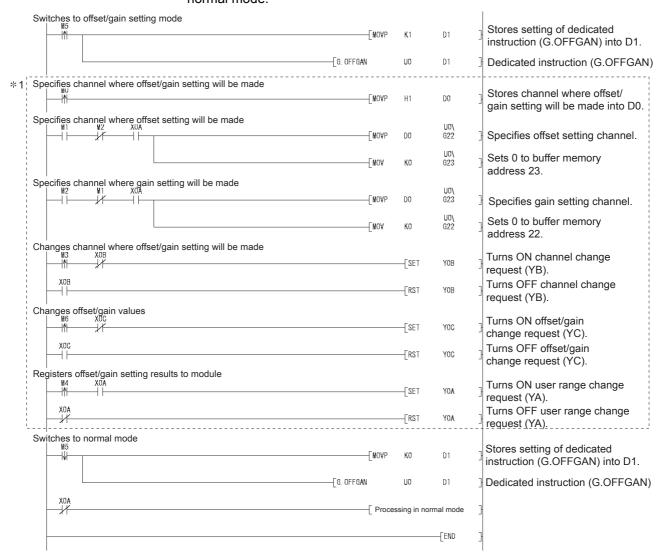
- (1) Perform the offset/gain settings in the range that satisfies the conditions specified in POINT of Section 3.1.2 (2).
  - When the setting exceeds this range, the maximum resolution or total accuracy may not be within the range indicated in the performance specification.
- (2) Though the offset/gain settings can be performed on multiple channels at the same time, set the offset and gain separately (0 at either of the buffer memory addresses 22, 23).
  - If channels are set at Un\G22 and Un\G23 at the same time, an error will occur and the ERR. LED will be lit.
- (3) After the offset/gain settings are completed, verify that the offset and gain values have been set correctly under actual usage conditions.
- (4) The offset and gain values are stored into the Flash memory and are not erased at power-off.
- (5) At the time of offset/gain setting, turn ON the user range write request (YA) to write the values to the Flash memory.
  Data can be written to the Flash memory up to 100 thousand times.
  - To prevent accidental write to the Flash memory, an error will occur and the error code (Un\G19) will be stored if write is performed 26 consecutive times.
- (6) If an error (error code: 40□ \*1) occurs during offset/gain setting, re-set the correct offset/gain value.
  The offset/gain value of the channel where the error has occurred is not
  - written to the A/D converter module. ( $^{\star 1}$ :  $\square$  indicates the corresponding channel number.)
- (7) When the offset/gain setting mode is switched to the normal mode, the module ready (X0) turns from OFF to ON. Note that the initial setting processing will be executed at this time if there is a sequence program that performs initial settings when the module ready (X0) turns ON.
- (8) When one mode is switched to the other (the normal mode is switched to the offset/gain setting mode or the offset/gain setting mode is switched to the normal mode), A/D conversion is suspended and power supply to the 2-wire transmitter is turned OFF.
  - To resume A/D conversion and power supply to the 2-wire transmitter, turn ON the operating condition setting request (Y9) after the mode is switched to the normal mode.
- (9) Un\G202 to Un\G225 are the areas used to restore the User-set offset/gain values when online module change is made. Refer to Chapter 7 for details of online module change.

### (2) Program examples

The program in the dotted area of (a) is common to (a), (b) and (c). In this example, the I/O signals for the Q66AD-DG are X/Y0 to X/YF.

• Channel selection
• Offset setting
• Gain setting
• Channel change command
Offset/gain setting value write command to the module M4
• Mode switching
Offset/gain change command
Normal mode checking signal
Channel designation storage device
• Dedicated instruction (G(P).OFFGAN) setting storage device D1

(a) When switching the mode using the dedicated instruction (G(P).OFFGAN) The following program switches to the offset/gain setting mode with the dedicated instruction (G(P).OFFGAN), changes the channel where offset/gain setting will be made, writes the offset/gain values to the Q66AD-DG, and then switches to the normal mode.

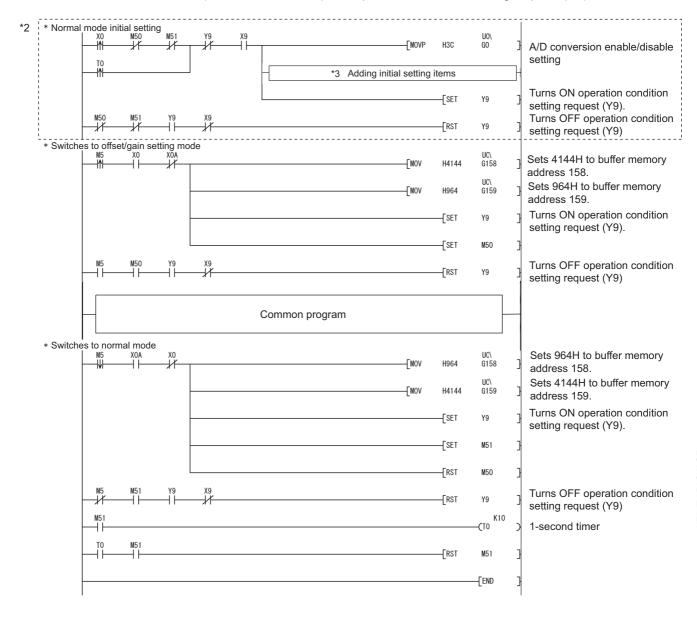


4

# SETUP AND PROCEDURES BEFORE OPERATION



(b) When switching the mode using the setting of the mode switching setting (Un\G158, Un\G159) and operation condition setting request (Y9)



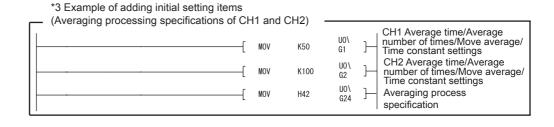


## **⊠** Point

When running this program together with the normal-mode A/D conversion program (shown in Section 6.4.3), use \*2 of this program as the initial setting program. Before using this program, check the device numbers.

Note) When adding this program to the normal-mode A/D conversion program (shown in Section 6.4.3), replace the existing initial setting program with the program marked \*2. Before using this program, check the device numbers.

In the part marked \*3, insert a setting program of the initial setting items for the function used.



(c) When switching the mode by making intelligent function module switch setting Only the common program is necessary.

#### 4.6.3 A/D conversion value storage during offset/gain setting

If during the offset/gain setting, the A/D conversion values are stored into Un\G11 to Un\G18 as in the normal mode.

#### (1) Q68AD-G

The A/D conversion values of all channels are stored into the buffer memory.

#### (2) Q66AD-DG

The A/D conversion values of the channels specified in the offset/gain setting mode (Un\G22, Un\G23) are stored into the buffer memory.

**UTILITY PACKAGE (GX Configurator-AD)** 

# MELSEG Q series

# **Utility Package Functions**

5.1

# Table 5.1 shows an overview of the utility package functions.

Table5.1 Utility package (GX Configurator-AD) function list

Item	Description	Reference section	
	(1) Sets the following items that require initial setting.		
	A/D conversion enable/disable setting		
	Averaging process specification		
	Average time/Average number of times/Move average/		
	Time constant settings		
	Conversion starting time setting (For 2-wire transmitter) (Q66AD-DG)		
	Warning output settings (Process alarm setting)		
	Process alarm upper upper limit value/upper lower limit value/		
	lower upper limit value/lower lower limit value		
	Warning output settings (Rate alarm setting)		
	Rate alarm upper limit value/lower limit value		
1-141-1441*1	Rate alarm warning detection period	Section 5.4	
Initial setting*1	Input signal error detection extended/Input signal error detection set-	Section 5.4	
	ting		
	Input signal error detection setting value/Input signal error detection		
	lower limit setting value		
	Input signal error detection upper limit setting value		
	Scaling enable/disable setting		
	Scaling upper limit value/lower limit value		
	(2) The data for which initial setting has been completed is registered in the		
	parameters for the programmable controller CPU, and automatically writ-		
	ten to the A/D		
	converter module when the programmable controller CPU changes to the		
	RUN status.		
	(1) Sets auto refresh for the A/D converter module buffer memory.		
Auto refresh	(2) The buffer memory that was set for auto refresh is automatically read and	Section 5.5	
setting*1	written to the specified device when the END command for the program-		
	mable controller CPU is executed.  (1) Monitor/Test		
	The buffer memory and I/O signals for the A/D converter modules are		
	monitored and tested.		
	(2) Operating condition setting		
	Changes the initial setting during operation.		
NA - with w/T t	(3) Offset/gain setting	Section 5.6	
Monitor/Test	When setting the offset/gain to a value selected by the user (when the		
	analog output range setting is user range setting), the offset and gain can		
	be easily set while viewing the screen.		
	(4) Pass data		
	The pass data (industrial shipment settings offset/gain values, user range		
	settings offset/gain values) can be monitored and set.		
FB conversion	Generates FB automatically from the intelligent function module parameter	Section 5.7	
	(initial setting/auto refresh setting).		



# 5.2 Installing and Uninstalling the Utility Package

For how to install or uninstall the utility package, refer to "Method of installing the MEL-SOFT Series" included in the utility package.

#### 5.2.1 Handling precautions

The following explains the precautions on using the GX Configurator-AD.

#### (1) For safety

Since GX Configurator-AD is add-in software for GX Developer, read "Safety Precautions" and the basic operating procedures in the GX Developer Operating Manual.

#### (2) About installation

GX Configurator-AD is add-in software for GX Developer Version 4 or later. Therefore, GX Configurator-AD must be installed on the personal computer that has already GX Developer Version 4 or later installed.

#### (3) Screen error of Intelligent function module utility

Insufficient system resource may cause the screen to be displayed inappropriately while using the Intelligent function module utility.

If this occurs, close the Intelligent function module utility, GX Developer (program, comments, etc.), and other applications, and then start GX Developer and Intelligent function module utility again.

#### (4) To start the Intelligent function module utility

- (a) In GX Developer, select "QCPU (Q mode)" for PLC series and specify a project. If any PLC series other than "QCPU (Q mode)" is selected, or if no project is specified, the Intelligent function module utility will not start.
- (b) Multiple Intelligent function module utilities can be started. However, [Open parameters] and [Save parameters] operations under [Intelligent function module parameter] are allowed for one Intelligent function module utility only. Only the [Monitor/test] operation is allowed for the other utilities.

#### (5) Switching between two or more Intelligent function module utilities

When two or more Intelligent function module utility screens cannot be displayed side by side, select a screen to be displayed on the top of others using the task bar.



#### (6) Number of parameters that can be set in GX Configurator-AD

When multiple intelligent function modules are mounted, the number of parameter settings must not exceed the following limit.

When intelligent function modules are installed to:	Maximum number of parameter settings	
When intelligent function modules are installed to.	Initial setting	Auto refresh setting
Q00J/Q00/Q01CPU	512	256
Q02/Q02H/Q06H/Q12H/Q25HCPU	512	256
Q02PH/Q06PH/Q12PH/Q25PHCPU	512	256
Q12PRH/Q25PRHCPU	512	256
Q02UCPU	2048	1024
Q03UD/Q04UDH/Q06UDH/Q13UDH/Q26UDH/		
Q03UDE/Q04UDEH/Q06UDEH/Q13UDEH/	4096	2048
Q26UDEHCPU		
MELSECNET/H remote I/O station	512	256

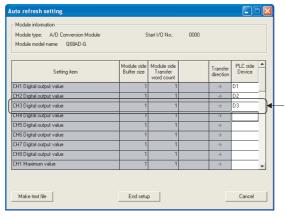
For example, if multiple intelligent function modules are installed to the MELSECNET/H remote I/O station, configure the settings in GX Configurator so that the number of parameter settings for all the intelligent function modules does not exceed the limit of the MELSECNET/H remote I/O station.

Calculate the total number of parameter settings separately for the initial setting and for the auto refresh setting.

The number of parameters that can be set for one module in GX Configurator-AD is as shown below.

Target module	Initial setting	Auto refresh setting
Q68AD-G	6 (Fixed)	36 (Max.)
Q66AD-DG	10 (Fixed)	28 (Max.)

Example) Counting the number of parameter settings in Auto refresh setting



This one row is counted as one setting. Blank rows are not counted. Count up all the setting items on this screen, and add the total to the number of settings for other intelligent function modules to get a grand total.



#### 5.2.2 Operating environment

This section explains the operating environment of the personal computer that runs GX Configurator-AD.

	Item	Description	
Installation (Add-in) target*1		Add-in to GX Developer Version 4 (English version) or later*2	
CPU		Windows <sup>®</sup> -based personal computer	
		Refer to the next page "Operating system and performance required for personal computer".	
	Required memory		
Hard disk	For installation	65 MB or more	
space*3	For operation	20 MB or more	
Display		800 × 600 dots or more resolution*4	
Operating system		Microsoft <sup>®</sup> Windows <sup>®</sup> 95 Operating System (English version)	
		Microsoft <sup>®</sup> Windows <sup>®</sup> 98 Operating System (English version)	
		Microsoft® Windows® Millennium Edition Operating System (English version)	
		Microsoft <sup>®</sup> Windows NT <sup>®</sup> Workstation Operating System Version 4.0 (English version)	
		Microsoft <sup>®</sup> Windows <sup>®</sup> 2000 Professional Operating System (English version)	
		Microsoft <sup>®</sup> Windows <sup>®</sup> XP Professional Operating System (English version)	
		Microsoft® Windows® XP Home Edition Operating System (English version)	
		Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Home Basic Operating System (English version)	
		Microsoft® Windows Vista® Home Premium Operating System (English version)	
		Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Business Operating System (English version)	
		Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Ultimate Operating System (English version)	
		Microsoft <sup>®</sup> Windows Vista <sup>®</sup> Enterprise Operating System (English version)	

- \*1: Install the GX Configurator-AD in GX Developer Version 4 or higher in the same language.

  GX Developer (English version) and GX Configurator-AD (Japanese version) cannot be used in combination, and GX Developer (Japanese version) and GX Configurator-AD (English version) cannot be used in combination.
- \*2: GX Configurator-AD cannot be used as an add-in with GX Developer Version 3 or earlier versions.
  - In addition, GX Developer Version 8 or later is necessary to use the FB conversion function.
- \*3: At least 15GB is required for Windows Vista<sup>®</sup>.
- \*4: Resolution of 1024×768 dots or more is recommended for Windows Vista<sup>®</sup>.

#### Operating system and performance required for personal computer

Operating system	Performance required for personal computer	
Operating system	CPU	Memory
Windows <sup>®</sup> 95	Pentium <sup>®</sup> 133MHz or more	32MB or more
Windows <sup>®</sup> 98	Pentium <sup>®</sup> 133MHz or more	32MB or more
Windows <sup>®</sup> Me	Pentium <sup>®</sup> 150MHz or more	32MB or more
Windows NT <sup>®</sup> Workstation 4.0	Pentium <sup>®</sup> 133MHz or more	32MB or more
Windows <sup>®</sup> 2000 Professional	Pentium <sup>®</sup> 133MHz or more	64MB or more
Windows® XP Professional (Service Pack 1 or more)	Pentium® 300MHz or more	128MB or more
Windows® XP Home Edition (Service Pack 1 or more)	Pentium® 300MHz or more	128MB or more
Windows Vista® Home Basic	Pentium <sup>®</sup> 1GHz or more	1GB or more
Windows Vista® Home Premium	Pentium <sup>®</sup> 1GHz or more	1GB or more
Windows Vista® Business	Pentium <sup>®</sup> 1GHz or more	1GB or more
Windows Vista <sup>®</sup> Ultimate	Pentium <sup>®</sup> 1GHz or more	1GB or more
Windows Vista® Enterprise	Pentium <sup>®</sup> 1GHz or more	1GB or more

# **⊠**Point

(1) The functions shown below are not available for Windows XP and Windows Vista®.

If any of the following functions is attempted, this product may not operate normally.

- Start of application in Windows® compatible mode
- · Fast user switching
- · Remote desktop
- Large fonts (Details setting of Display Properties)

Also, 64-bit version Windows® XP and Windows Vista® are not supported.

(2) Use a USER authorization or higher in Windows Vista®.



# 5.3 Utility Package Operation

# 5.3.1 Common utility package operations

#### (1) Control keys

Special keys that can be used for operation of the utility package and their applications are shown in the table below.

Key	Application
Esc	Cancels the current entry in a cell.
	Closes the window.
Tab	Moves between controls in the window.
Ctrl	Used in combination with the mouse operation to select multiple
Ctr	cells for test execution.
Poloto	Deletes the character where the cursor is positioned.
Delete	When a cell is selected, clears all of the setting contents in the cell.
Back Space	Deletes the character where the cursor is positioned.
$\uparrow \qquad \downarrow \qquad \leftarrow \qquad \rightarrow$	Moves the cursor.
Page Up	Moves the cursor one page up.
Page Down	Moves the cursor one page down.
Enter	Completes the entry in the cell.

MELSEG Q series

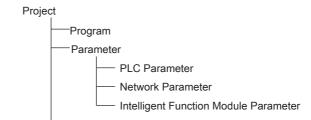
# **UTILITY PACKAGE (GX Configurator-AD)**

#### (2) Data created with the utility package

The following data or files that are created with the utility package can be also handled in GX Developer. Figure 5.1 shows respective data or files are handled in which operation.

<Intelligent function module parameter>

(a) This represents the data created in Auto refresh setting, and they are stored in an intelligent function module parameter file in a project created by GX Developer.



- (b) Steps 1) to 3) shown in Figure 5.1 are performed as follows:
  - From GX Developer, select:
     [Project] → [Open project] / [Save] / [Save as]
  - 2) On the intelligent function module selection screen of the utility, select: [Intelligent function module parameter] → [Open parameters] / [Save parameters]
  - 3) From GX Developer, select:

[Online]  $\rightarrow$  [Read from PLC] / [Write to PLC]  $\rightarrow$  "Intelligent function module parameters"

Alternatively, from the intelligent function module selection screen of the utility, select:

 $[Online] \rightarrow [Read from PLC] / [Write to PLC]$ 



#### <Text files>

(c) A text file can be created by clicking the Make text file button on the initial setting, Auto refresh setting, or Monitor/Test screen. The text files can be utilized to create user documents.

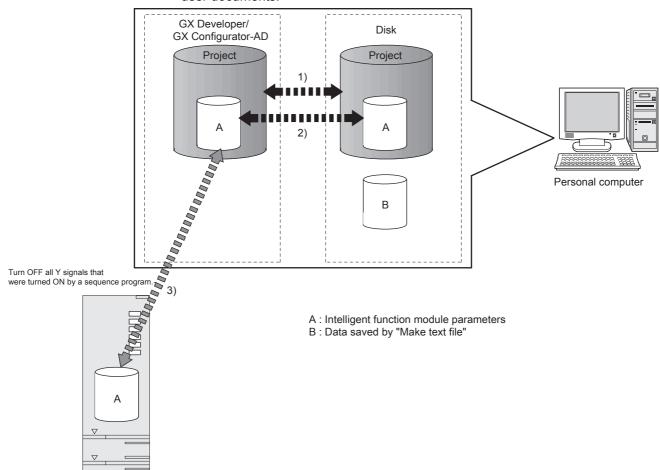


Fig5.1 Correlation chart for data created with the utility package

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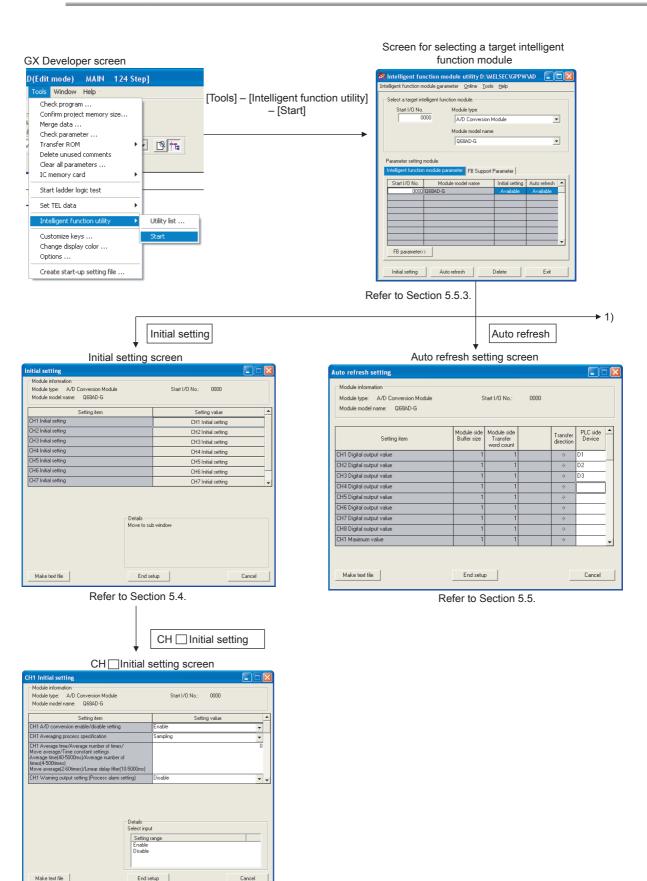
SETUP AND PROCEDURES BEFORE OPERATION

**PROGRAMMING** 

ONLINE MODULE CHANGE

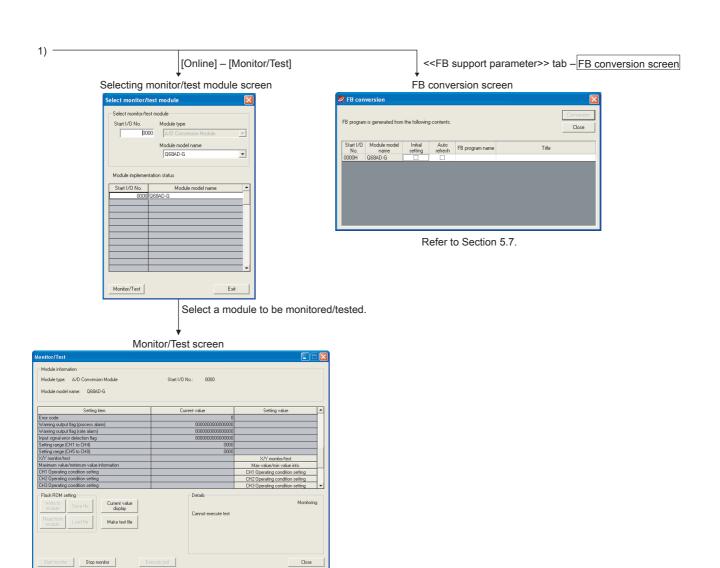
TROUBLESHOOTING

## 5.3.2 Operation overview



5.3 Utility Package Operation 5.3.2 Operation overview

# 5 UTILITY PACKAGE (GX Configurator-AD)



Refer to Section 5.6.

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SPECIFICATIONS

**PROGRAMMING** 

ONLINE MODULE CHANGE

TROUBLESHOOTING

#### 5.3.3 Starting the intelligent function module utility

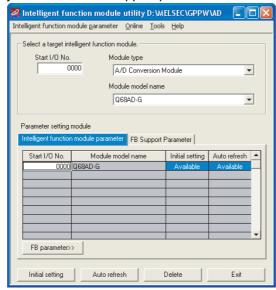
[Operating procedure]

Intelligent function module utility is started from GX Developer.

[Tools] → [Intelligent function utility] → [Start]

[Setting screen]

Display when the <<FB support parameter>> tab is selected



[Explanation of items]

#### (1) Activation of other screens

Following screens can be displayed from the intelligent function module utility screen. Common operations to the <<Intelligent function module parameter>> tab and <<FB support parameter>> tab

(a) Initial setting screen

"Start I/O No.\*1" → "Module type" → "Module model name" → |Initial setting

(b) Auto refresh setting screen

"Start I/O No.\*1" → "Module type" → "Module model name" → Auto refresh

(c) Select monitor/test module screen

[Online] → [Monitor/Test]

\*1 Enter the start I/O No. in hexadecimal.

On the <<FB support parameter>> tab

(a) Start-up of the FB conversion screen

<<FB support parameter>> tab → | FB conversion

For details, refer to Section 5.7.



## **⊠** Point

The <<FB support parameter>> tab is displayed when the project which is being edited is a label project.

#### (2) Command buttons

Common operations to the <<Intelligent function module parameter>> tab and <<FB support parameter>> tab

Delete

Deletes the initial setting and auto refresh setting of the selected module.

However, if initial setting and auto refresh setting have been prepared and the cell of initial setting or auto refresh setting is selected and executed, only the setting of the selected cell is

deleted.

Exit

Close this screen.

When the <<FB support parameter>> tab is selected

<< Parameter

Moves the setting of the selected line to the <<Intelligent function module parameter>> tab.

When the <<Intelligent Function Module Parameter>> tab is selected

FB Parameter>>

Moves the setting of the selected line to the

<<FB support parameter>> tab.

#### (3) Menu bar

(a) File menu

Intelligent function module parameters of the project opened by GX Developer are handled.

.....

[Open parameters] : Reads a parameter file.

[Close parameters] : Closes the parameter file. If any data are modi-

fied, a dialog asking for file saving will appear.

[Save parameters] : Saves the parameter file.
[Delete parameters] : Deletes the parameter file.

[Open FB support parameters] : Opens the FB support parameter file. [Save as FB support parame- : Saves the FB support parameter file.

ters]

[Exit] : Close this screen.

(b) Online menu

[Monitor/Test] : Activates the Select monitor/test module screen.

[Read from PLC] : Reads intelligent function module parameters from the

CPU module.

[Write to PLC] : Writes intelligent function module parameters to the CPU

module.



e utility C:\MELSEC\GPF

Monitor/Test.. Read from PLC

Write to PLC

# MELSEG Q series

# **⊠**Point

- (1) Saving intelligent function module parameters in a file Since intelligent function module parameters cannot be saved in a file by the project saving operation of GX Developer, save them on the shown module selection screen.
- (2) Reading/writing intelligent function module parameters from/to a programmable controller CPU using GX Developer
  - (a) Intelligent function module parameters can be read from and written into a programmable controller after having been saved in a file.
  - (b) Set a target programmable controller CPU in GX Developer: [Online] → [Transfer setup].
- (3) Checking the required utility

While the start I/O is displayed on the Intelligent function module utility setting screen, " \* " may be displayed for the model name.

This means that the required utility has not been installed or the utility cannot be started from GX Developer.

Check the required utility, selecting [Tools] - [Intelligent function utility] - [Utility list...] in GX Developer.



# 5.4 Initial Setting

#### [Purpose]

The following A/D initial setting parameters are set:

- A/D conversion enable/disable setting
- Averaging process specification
- Average time/Average number of times/Move average/Time constant settings
- A/D conversion starting time setting (Q66AD-DG)
- Warning output settings (Process alarm setting)
- Process alarm upper upper limit value/upper lower limit value/lower upper limit value/lower lower limit value
- Warning output settings (Rate alarm setting)
- Rate alarm upper limit value/lower limit value
- Rate alarm warning detection period
- Input signal error detection extended/input signal error detection setting
- Input signal error detection setting value/Input signal error detection lower limit setting value
- Input signal error detection upper limit setting value
- Scaling enable/disable setting
- Scaling upper limit value/lower limit value

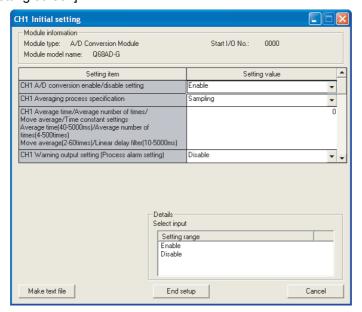
By performing these initial settings, the sequence program settings are not required.

#### [Operating procedure]

"Start I/O No. \* " →"Module type" → "Module model name" → Initial setting → CH□ Initial setting

\* Enter the start I/O No. in hexadecimal.

#### [Setting screen]



#### (1) Setting contents

Set A/D conversion enable/disable, averaging process specification and others for each channel.

#### (2) Command buttons

Make text file Creates a file containing the screen data in text file format.

End setup Saves the set data and ends the operation.

Cancels the setting and ends the operation.

# **⊠**Point

Initial settings are stored in the intelligent function module parameters. After being written to the CPU module, the initial setting is made effective by either (1) or (2).

- (1) Cycle the RUN/STOP switch of the CPU module: STOP  $\rightarrow$  RUN  $\rightarrow$  STOP  $\rightarrow$  RUN.
- (2) With the RUN/STOP switch set to RUN, turn off and then on the power or reset the CPU module.

When using a sequence program to write the initial settings, when the CPU is switched from STOP to RUN the initial settings will be written, so ensures that programming is carried out to re-execute the initial settings.

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# 5.5 Auto Refresh Setting

#### [Purpose]

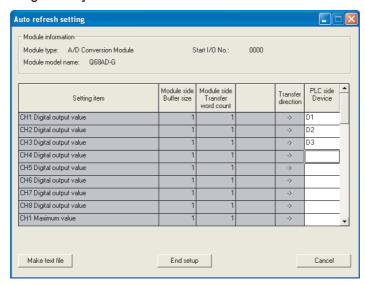
Configure the A/D converter module's buffer memory for auto refresh.

#### [Operating procedure]

"Start I/O No. \* " → "Module type" → "Module model name" → Auto refresh

\* Enter the start I/O No. in hexadecimal.

#### [Setting screen]



#### [Explanation of items]

#### (1) Items

Module side Buffer size : Displays the size of the buffer memory for

the setting item that can be transferred

(fixed at one word).

Module side Transfer word

count

: Displays the number of words to transfer the CPU device from the head device

(fixed at one word).

Transfer direction : "←" indicates that data is written from the

device to the buffer memory.

" $\rightarrow$ " indicates that data is read from the

buffer memory to the device.

PLC side Device : Enter a CPU module side device that is to

be automatically refreshed.

Applicable devices are X, Y, M, L, B, T, C,

ST, D, W, R, and ZR.

When using bit devices X, Y, M, L or B, set a number that can be divided by 16 points

(examples: X10, Y120, M16, etc.)

Also, buffer memory data are stored in a 16-point area, starting from the specified device number. For example, if X10 is entered, data are stored in X10 to X1F.

End setup

Saves the set data and ends the operation.

Cancel

Cancels the setting and ends the operation.

# **⊠**Point

The auto refresh settings are stored in an intelligent function module parameter file. The auto refresh settings become effective by performing STOP  $\rightarrow$  RUN  $\rightarrow$  STOP  $\rightarrow$  RUN operations for the CPU module, turning the power OFF and then ON or resetting the CPU module after writing the intelligent function module parameters to the CPU module.

The auto refresh settings cannot be changed from sequence programs. However, processing equivalent to auto refresh can be added using the FROM/TO instruction in the sequence program.

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# 5.6 Monitoring/Test

#### 5.6.1 Monitor/test screen

#### [Purpose]

Buffer memory monitoring/testing, I/O signal monitoring/testing, operating condition setting, offset/gain settings (Refer to Section 5.6.2) and pass data (Refer to Section 5.6.3, 5.6.4) are started from this screen.

#### [Operating procedure]

"Select monitor/test module" screen  $\rightarrow$  "Start I/O No. \* "  $\rightarrow$  "Module type"  $\rightarrow$ 

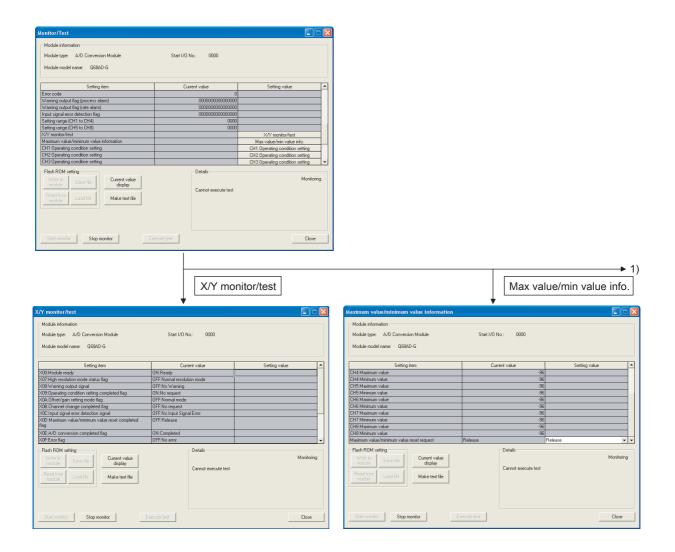
"Module model name" → | Monitor/test

\* Enter the start I/O No. in hexadecimal.

The screen can also be started from System monitor of GX Developer Version 6 or later.

Refer to the GX Developer Operating Manual for details.

[Setting screen]

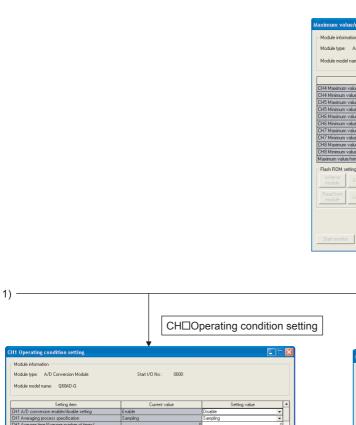


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SETUP AND PROCEDURES BEFORE OPERATION



ONLINE MODULE
CHANGE

Close



[Explanation of items]

#### (1) Items

Setting item : Displays I/O signals and buffer memory names.

Current : Monitors the I/O signal states and present buffer memory

value values.

Setting value : Select or enter the data to be written during test operation.

#### (2) Command buttons

Current value display

Displays the current value of the item selected. (This is used to check the text that cannot be displayed in the current value field. However, in this utility package, all items can be displayed in the displayed fields)

play fields).

format.

Start monitor / Selects whether or not to monitor current values.

Stop monitor

Execute test Performs a test on the selected items. To select

more than one item, select them while holding down

the Ctrl key.

Closes the screen that is currently open and returns

to the previous screen.

Remark

The following describes an example where sampling process for the selection test operation is changed to a 10-time averaging process specification.

- (2) Click and select the Setting value field of CH ☐ Average time/Average number of times/Move average/Time constant settings.
- (3) After entering the average number of times, press the Enter key.

  At this point, the value is not yet written to the A/D converter module.
- (4) Hold down the Ctrl key and select the Setting value field where the value has been entered by the operation performed in (1) to (3). Multiple items can also be selected by performing mouse drag operation.
- (5) Click Execute test to execute write operation.

  Once writing has been completed, the value that was written will be displayed in the current value field.

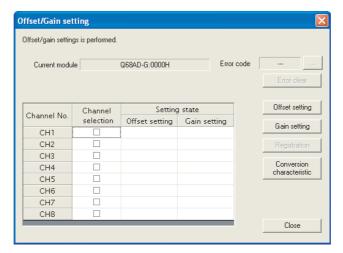


## 5.6.2 Offset/gain setting operation

Perform the offset/gain setting operation in the following sequence.

#### (1) Switch to the offset/gain setting screen

Perform the operation in Section 5.6.1 to display the offset/gain setting screen. At this point, a dialog box to confirm the transition of module's operation mode (normal mode -> offset/gain setting mode) is displayed. Click the Yes button to transit to the offset/gain setting mode.



#### (2) Specify channels

Place check marks in "Channel selection" column to specify the channels for each of which the offset or gain setting is to be made.

#### (3) Apply current/voltage

Apply current or voltage to the module. Note that "Current" only is allowed for the Q66AD-DG.

#### (4) Execute offset/gain setting

For each of the channels specified in (2), click the Offset setting or Gain setting button to execute respective setting.

#### (5) Write settings into module

Write the content set up by operations (2) to (4) into module by clicking the Registration button.

#### (6) Switch to the normal mode

When the offset/gain setting screen is closed by clicking the Close button after the setting operation has finished, module's operation mode transits to the normal mode.

# 5

# UTILITY PACKAGE (GX Configurator-AD)



⊠Point <del></del>	
△ Point	
If an error code is displa	ayed while performing the setting operation, the details and
	n be confirmed by clicking the button to the right of area. In addition, the error code can be cleared by clicking
the Error clear butto	on.

OVERVIEW

SYSTEM CONFIGURATION

SPECIFICATIONS

AD AD

**PROGRAMMING** 

ONLINE MODULE CHANGE

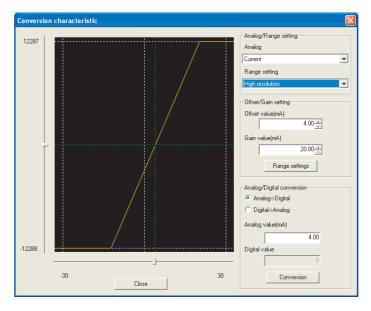
TROUBLESHOOTING

### [Purpose]

The converted value of digital-analog conversion can be confirmed according to the tilt of the graph, based on the offset/gain setting.

#### [Operating procedure]

[Setting screen]



[Explanation of items]

#### (1) Items displayed on the screen

I/O characteristic diagram: Displays the I/O conversion characteristic to the prepared offset/gain setting.

#### (2) Setting details

Analog/Range setting

Analog : Select the type of the analog signal input (voltage or current).

When the target module is the Q66AD-DG, "Current" only can be

selected.

Range setting : Make selection from "Normal resolution" or "High resolution"

Offset/Gain setting

Offset value : Enter an offset value to display the I/O characteristic diagram.

Gain value : Enter a gain value to display the I/O characteristic diagram.

Analog/Digital conversion : Select a conversion type shown below for confirming

the correspondence between an analog value and a digital value caused by the conversion characteristic.

Digital → Analog

Analog → Digital



Analog value : < When converted to a digital value>

Enter an analog value to be converted to a digital value

<When converted to an analog value>

The analog value converted from a digital value is displayed.

Digital value : < When converted to a digital value>

The digital value corresponding to an entered analog value is

displayed.

<When converted to an analog value>

Enter a digital value to be converted to an analog value.

## **⊠**Point

• The offset value is an analog input value (voltage or current) that will produce a digital output value of 0.

 The gain value is the analog value (voltage or current) output when the digital input value set from the programmable controller CPU is as follows:

4000 (Normal resolution mode)

16000/12000 (High resolution mode)

#### (3) Explanation of screen command buttons

Range setting

The entered offset/gain value is determined, and the I/O characteristic diagram is updated.

Conversion

Conversion for the entered value is performed.

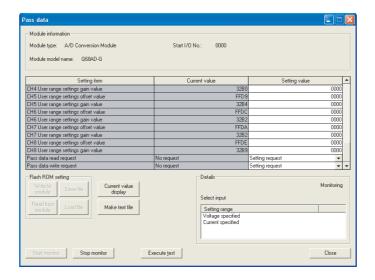
#### 5.6.4 Pass data (Q68AD-G)

Perform operation in the following sequence to save/restore the user range.

#### (1) Switch to the pass data screen

**UTILITY PACKAGE (GX Configurator-AD)** 

Perform the operation in Section 5.6.1 to display the Pass data screen.



#### (2) User range saving

- (a) Set "Voltage specified" or "Current specified" in the Setting value field of Pass data classification setting, and click the Execute test | button. When the setting is completed, the set data is displayed in the Current value field of CH 

  Pass data classification setting.
- (b) Change the Setting value field of Pass data read request to "Request", and click the Execute test button. When read is completed, the values are displayed in the Current value fields of CH ☐ Industrial shipment settings offset/gain values/CH ☐ User range settings offset/gain values.
- (c) Compare the values with those in the range reference table, and record them if they are correct.
  - Refer to Section 7.4 for the range reference table.

# 5

# UTILITY PACKAGE (GX Configurator-AD)



## (3) User range restoration

(a	) Set "Voltage specified" or "Current specified" in the Setting value field of Pass
	data classification setting, and click the Execute test button.  When the setting is completed, the set data is displayed in the Current value field of CH   Pass data classification setting.
(b	) Set the recorded values in the Setting value fields of CH ☐ Industrial shipment settings offset/gain values/user range settings offset/gain values.
(c	) Select all the Setting value fields of CH ☐ Industrial shipment settings offset/gain
	values/user range settings offset/gain values, and click the Execute test button.  When write is completed, the set values are displayed in the Current value fields of CH □ Industrial shipment settings offset/gain values/CH □ User range settings offset/gain values.
(d	) Change the Setting value field of Pass data write request to "Request", and click the Execute test button.  Make sure that the indication in the Current value field of Pass data write request changes from "Request" to "OFF" on completion of write.

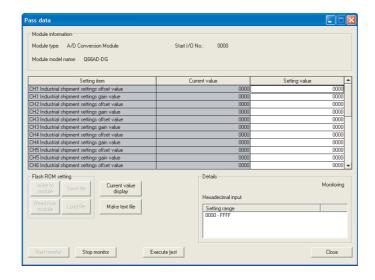
## 5.6.5 Pass data (Q66AD-DG)

Perform operation in the following sequence to save/restore the user range.

#### (1) Switch to the Pass data screen

**UTILITY PACKAGE (GX Configurator-AD)** 

Perform the operation in Section 5.6.1 to display the pass data screen.



#### (2) User range saving

- (a) Change the Setting value field of pass data read request to "Request", and click the Execute test | button.
  - When read is completed, the values are displayed in the Current value fields of CH ☐ industrial shipment settings offset/gain values/CH ☐ user range settings offset/gain values.
- (b) Compare the values with those in the range reference table, and record them if they are correct.

Refer to Section 7.4 for the range reference table.

#### (3) User range restoration

- (a) Set the recorded values in the Setting value fields of CH ☐ industrial shipment settings offset/gain values/user range settings offset/gain values.
- (b) Select all the Setting value fields of CH ☐ industrial shipment settings offset/gain values/user range settings offset/gain values, and click the Execute test button. When write is completed, the set values are displayed in the Current value fields of CH ☐ industrial shipment settings offset/gain values/ CH ☐ user range settings offset/gain values.
- (c) Change the Setting value field of pass data write request to "Request", and click the Execute test button.

Make sure that the indication in the Current value field of pass data write request changes from "Request" to "OFF" on completion of write.



# 5.7 FB Conversion of Initial Setting/Auto Refresh Setting

#### [Purpose]

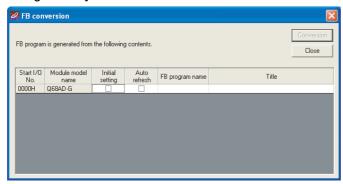
FB is generated automatically from the intelligent function module parameter (initial setting/auto refresh setting).

#### [Operating procedure]

Intelligent Function Module Parameter Setting Module Selection Screen →

<<FB Support Parameter>> → FB conversion

#### [Setting screen]



[Explanation of items]

#### (1) Items displayed on the screen

Start I/O No. : The start I/O No. of the information which is set up on the

currently open intelligent function module parameter is

displayed.

Module model name : The module model name of the information which is set up on

the currently open intelligent function module parameter is

displayed.

Initial setting : Set up whether to apply FB conversion to the parameter or not.

Check if you apply FB conversion to the parameter.

Auto refresh : Set up whether to apply FB conversion to the parameter or not.

Check if you apply FB conversion to the parameter.

FB program name : Set up the name of the converted FB program.

Up to six single-byte characters can be set up as an FB

program name.

However, the characters and terms shown below cannot be set

up as FB program name.

Character:\, /, :, ;, \*, ?, ", <, >, |, ,

Term : COM1 to COM9, LPT1 to LPT9, AUX, PRN, CON, NUL,

CLOCK\$

In addition, I- is added for initial setting and A- is added for auto refresh setting respectively to the top of the FB name setting to be registered in GX Developer after FB conversion is

performed.

Ex.: If the FB program name is "ABCDE, " the initial setting is

"I-ABCDE" and the auto refresh setting is "A-ABCDE".

Title : Set up a title on a converted FB program. Up to 32 single-byte

characters can be set up as a title.

Conversion

FB conversion is performed for the checked columns of initial setting and auto refresh setting.



# 5.8 Usage of FB

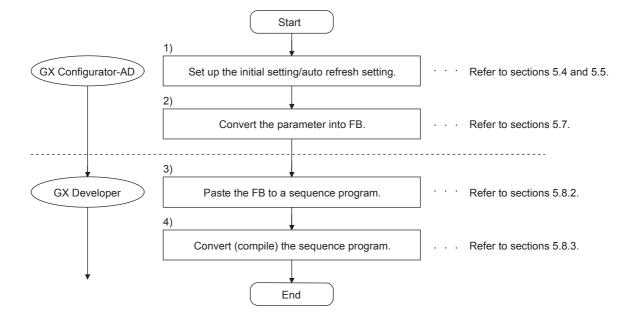
This section describes the procedure for using FB with GX Developer. For details, refer to "GX Developer Version 8 Operating Manual (Function Block)."

#### 5.8.1 Outline

The procedure for creating FB is shown below.

- (1) Set up the intelligent function module parameter (initial setting/auto refresh setting).
- (2) Convert the intelligent function module parameter into FB.
- (3) Paste the FB to a sequence program.
- (4) Convert (compile) the sequence program.

Next, a flowchart of procedures 1) to 4) is shown below.



# **⊠**Point

**UTILITY PACKAGE (GX Configurator-AD)** 

The initial setting/auto refresh setting of the intelligent function module can be performed by each of the following methods.

- (1) Set intelligent function parameters (Initial setting/Auto refresh setting) and write them to the programmable controller CPU.
- (2)Create an FB of the intelligent function module parameter (initial setting/auto refresh setting) and paste it to the sequence program.
- In accordance with the specification of the system, perform the initial setting/auto refresh setting of the intelligent function module by one of the methods above.\*1
  - \* 1:The following explains the case in which both of (1) and (2) are performed.
    - (a) Initial settingFB setting given in (2) is valid.
    - (b) Auto refresh setting
      - Both (1) and (2) are valid.
      - At the time of FB execution and in the END processing of the sequence program, auto refresh is performed.



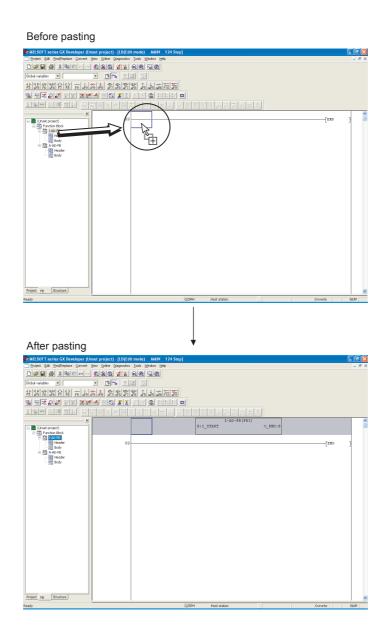
#### 5.8.2 Paste an FB to a sequence program

[Purpose of operation]

Paste an FB in order to use it with a sequence program.

[Operation procedure]

Switch the <<Pre>roject>> tab into the <<FB>> tab on GX Developer, and drag & drop the FB to be used onto the sequence program.



# n be OVERVIEW

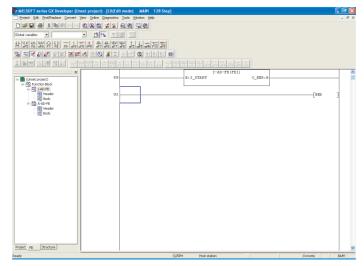
# [Purpose of operation]

5.8.3

**UTILITY PACKAGE (GX Configurator-AD)** 

Convert (compile) a sequence program

Convert (compile) the sequence program to which an FB was pasted so that it can be executed.



[Operation procedure]

Click the [Convert] menu  $\rightarrow$  [Convert/Compile] menu of GX Developer.

SYSTEM
CONFIGURATION



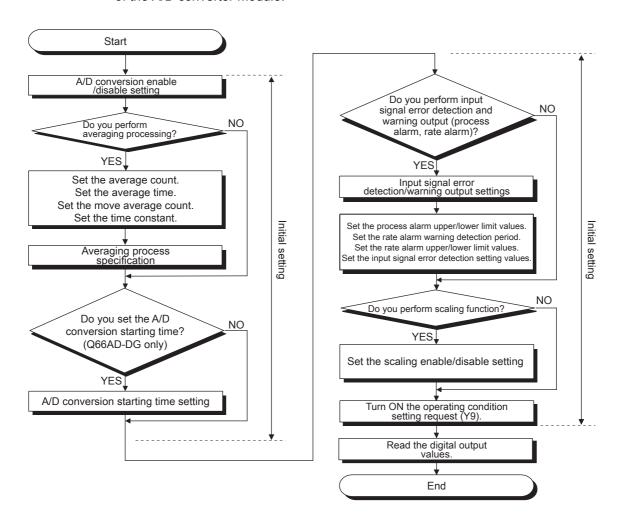
# 6 PROGRAMMING

This chapter describes the programs of the A/D converter modules.

When applying any of the program examples introduced in this chapter to the actual system, verify the applicability and confirm that no problems will occur in the system control.

# 6.1 Programming Procedure

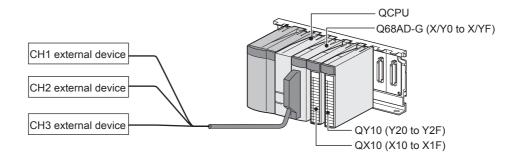
In the following procedure, create a program that will execute the analog/digital conversion of the A/D converter module.



#### For Use in Normal System Configuration (Q68AD-G) 6.2

#### (1) System configuration

**PROGRAMMING** 



#### (2) Conditions for the intelligent function module switch setting

	Input range setting	Normal resolution mode/ High resolution mode
CH1		
CH2	4 to 20mA	High resolution mode
CH3		
CH4		
to	not used	-
CH8		

### (3) Program conditions

- (a) The following averaging processing specification is used for each channel.
  - CH1: Sampling processing
  - CH2: Time averaging (50 times)
  - CH3: Primary delay filtering (100ms)
- (b) CH1 uses the input signal error detection function (Refer to Section 3.2.3.)
  - Input signal error detection: 10%
- (c) CH2 uses the warning output setting (process alarm) (Refer to Section 3.4.1 (1).)
  - Process alarm lower lower limit value: 1000
  - Process alarm lower upper limit value: 1500
  - Process alarm upper lower limit value: 6000
  - Process alarm upper upper limit value: 7000
- (d) CH3 uses the warning output setting (rate alarm) (Refer to Section 3.2.4 (2).)
  - Rate alarm warning detection period : 50ms
  - Rate alarm upper limit value: 0.3%
  - Rate alarm upper limit value: 0.1%
- (e) In the event of a write error, an error code shall be displayed in BCD format. The error code shall be reset after removal of the cause.



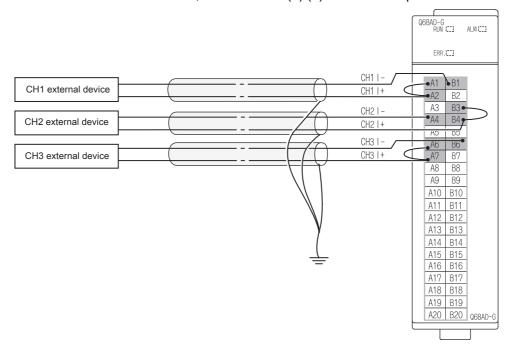
### 6.2.1 Before creating a program

Perform the following steps before creating a program.

#### (1) Wiring of external devices

Mount the Q68AD-G on the base unit and connect the external devices.

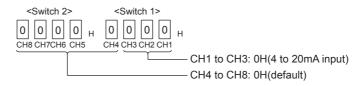
• For all of CH1 to CH3, run the cables for current input. For details, refer to "4.4.2 (2) (b) For current input".



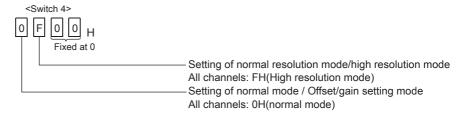
MELSEG Q series

Based on the setting conditions given in Section 6.2 (2), make the intelligent function module switch settings.

- (a) Each switch setting
  - 1) Switch1, Switch2: Input range setting



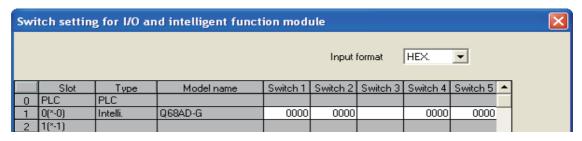
- 2) Switch3: Empty (No setting required)
- 3) Switch4: Mode setting



4) Switch5: Use prohibited (0:fixed)



- \* If any other than 0 is set to Switch 5, an error occurs.
- (b) Write the settings in (a) to the Q68AD-G. On GX Developer's "Parameter setting" screen, select the "I/O assignment" tab, click "Switch setting", and make settings of Switch 1 to 5 on the screen shown below.





### 6.2.2 Programming example using the utility package

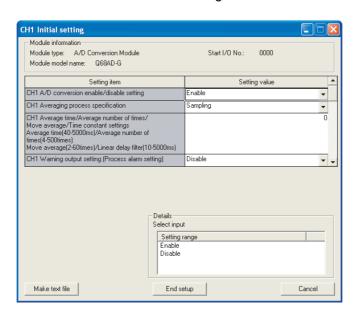
#### (1) List of devices

Device	Function	
D1	CH1 Digital output value	
D2	CH2 Digital output value	
D3	CH3 Digital output value	
D6,D7 <sup>*1</sup>	Warning output flag	
D8 <sup>*1</sup>	Input signal error detection flag	
D9 <sup>*1</sup>	Error code	
M0 to M2	A/D conversion completed flag	
M12,M13	CH2 Warning output flag (Process alarm)	
M34,M35	CH3 Warning output flag (Rate alarm)	
M50	CH1 Input signal error detection flag	
XC	Input signal error detection signal	
XE	A/D conversion completed flag	
XF	Error flag	Q68AD-G (X/Y0 to X/YF)
Y9	Operating condition setting request	
YF	Error clear request	
X10	Digital output value read command input	
X10	signal	
X11	Input signal error detection reset signal	QX10 (X10 to X1F)
X12	Error reset signal	
X14	Warning reset signal	
Y20 to Y2B	Error code display (BCD 3 digits)	QY10 (Y20 to Y2F)

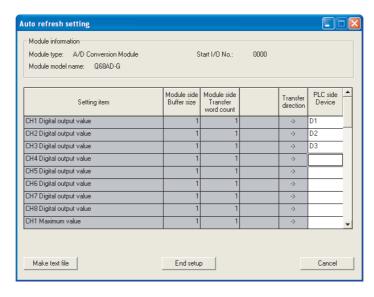
<sup>\*1:</sup>Devices used for the auto refresh function of GX Configurator-AD.

#### (2) Operating the utility package

(a) Initial setting (Refer to Section 5.4)Set the initial settings of CH1 to 3.Refer to Section 6.2 for the settings.



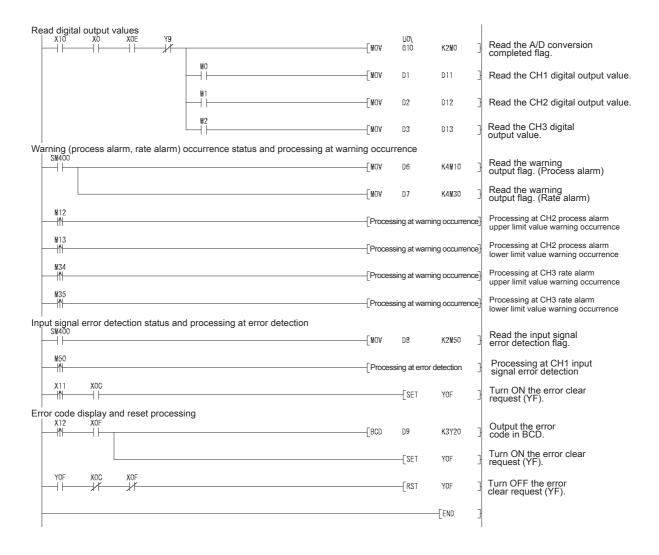
MELSEG Q series



(c) Writing the intelligent function module parameters (Refer to Section 5.3.3) Write the intelligent function module parameters to the CPU module. This operation is performed using the parameter setting module selection screen.



#### (3) Programming example



6.2.3

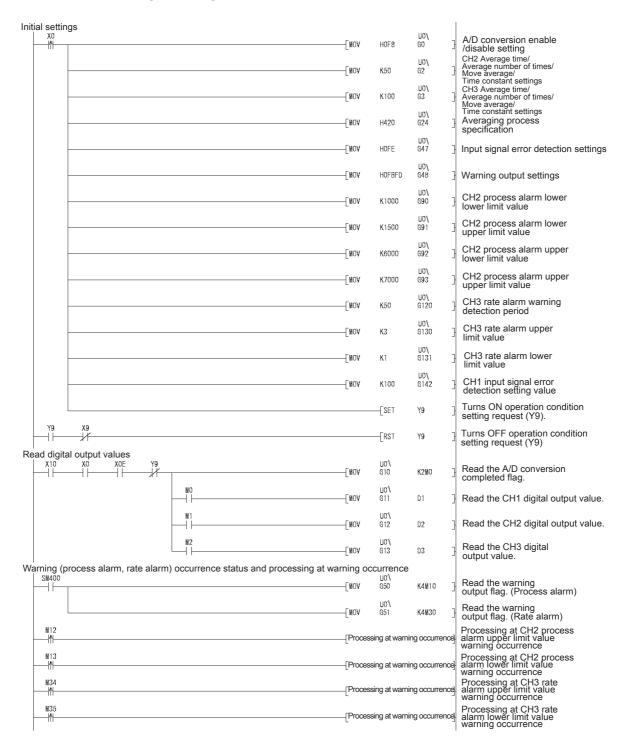
### (1) List of devices

Device	Function	
D1	CH1 Digital output value	
D2	CH2 Digital output value	
D3	CH3 Digital output value	
D6,D7	Warning output flag	
D8	Input signal error detection flag	
D9	Error code	
M0 to M2	A/D conversion completed flag	
M12,M13	CH2 Warning output flag (Process alarm)	
M34,M35	CH3 Warning output flag (Rate alarm)	
M50	CH1 Input signal error detection flag	
X0	Module ready	
XC	Input signal error detection signal	
XE	A/D conversion completed flag	Q68AD-G (X/Y0 to X/YF)
XF	Error flag	QUUAD-G (X TO LO X/TT)
Y9	Operating condition setting request	
YF	Error clear request	
X10	Digital output value read command input	
X10	signal	
X11	Input signal error detection reset signal	QX10 (X10 to X1F)
X12	Error reset signal	
X14	Warning reset signal	
Y20 to Y2B	Error code display (BCD 3 digits)	QY10 (Y20 to Y2F)

Programming example without using the utility package



#### (2) Programming example

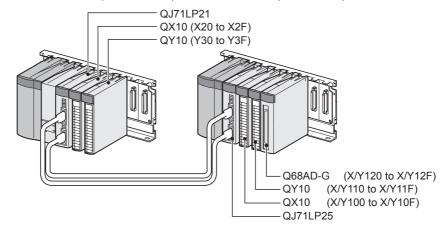




# 6.3 For Use in Remote I/O Network (Q68AD-G)

#### (1) System configuration

Remote master station (Network No. 1) Remote I/O station (Station No. 1)



#### (2) Conditions for the intelligent function module switch setting

	Input range setting	Normal resolution mode/ High resolution mode
CH1		
CH2	4 to 20mA	High resolution mode
CH3		
CH4		
to	not used	-
CH8		

Based on the setting conditions given in the above, make the intelligent function module switch settings.

Select the "I/O assignment" tab on the "Intelligent function module switch settings" screen, and click "Switch setting" to set the following values.

Switch No		Setting value
Switch 1	0000н	(CH1 to CH3: 4 to 20mA
Switch 2	0000н	CH4 to CH8: Default)
Switch 3	-	
Switch 4	0F00н (High resol	lution mode)
Switch 5	0000н (0: Fixed)	

- (a) The following averaging processing specification is used for each channel.
  - CH1: Sampling processing
  - CH2: Time averaging (50 times)
  - CH3: Primary delay filtering (100ms)
- (b) CH1 uses the input signal error detection function (Refer to Section 3.2.3.)
  - Input signal error detection: 10%
- (c) CH2 uses the warning output setting (process alarm) (Refer to Section 3.2.4 (1).)
  - Process alarm lower lower limit value: 1000
  - Process alarm lower upper limit value: 1500
  - Process alarm upper lower limit value: 6000
  - Process alarm upper upper limit value: 7000
- (d) CH3 uses the warning output setting (rate alarm) (Refer to Section 3.2.4 (2).)
  - · Rate alarm warning detection period : 50ms
  - Rate alarm upper limit value: 0.3%
  - Rate alarm upper limit value: 0.1%
- (e) In the event of a write error, an error code shall be displayed in BCD format. The error code shall be reset after removal of the cause.

#### (4) List of devices

Device	Function	
D1	CH1 Digital output value	
D2	CH2 Digital output value	
D3	CH3 Digital output value	
D6,D7 <sup>*1</sup>	Warning output flag	
D8 <sup>*1</sup>	Input signal error detection flag	
D9 <sup>*1</sup>	Error code	
M0 to M2	A/D conversion completed flag	
M12,M13	CH2 Warning output flag (Process alarm)	
M34,M35	CH3 Warning output flag (Rate alarm)	
M50	CH1 Input signal error detection flag	
X20	Digital output value read command input sinal	
X21	Input signal error detection reset signal	QX10 (X20 to X2F)
X22	Error reset signal	QX 10 (X20 to X21 )
X24	Warning reset signal	
Y30 to Y3B	Error code display (BCD 3 digits)	QY10 (Y30 to Y3F)
X120	Module ready	
X129	Operating condition setting completed flag	
X12C	Input signal error detection signal	Q66AD-DG
X12E	A/D conversion completed flag	
X12F	Error flag	(X/Y120 to X/Y12F)
Y129	Operating condition setting request	
Y12F	Error clear request	

<sup>\*1:</sup>Devices used for the auto refresh function of GX Configurator-AD.

OVERVIEW



# **⊠**Point

For details on the MELSECNET/H remote I/O network, refer to the Q Corresponding MELSECNET/H Network System Reference Manual (Remote I/O Network).

# 6.3.1 Programming example using the utility package

### (1) Operating GX Developer

**PROGRAMMING** 

(a) CPU parameter setting

Network type : MNET/H (Remote master)

Starting I/O No. : 0000H
Network No. : 1
Total stations : 1
Mode : Online

Network range assignment:

			M station	-> R statio	n		M station <- R station						
StationNo.	StationNo. Y			Υ			×			×			
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	Ш
1	256	0100	01FF	256	0000	00FF	256	0100	01FF	256	0000	00FF	▼

	M stati	ion -> R sta	ation	M station <- R station			M station -> R station			M station <- R station		
StationNo.	StationNo. B			В			W			W		
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End
. 1							256	0000	00FF	256	0100	01FF

#### Refresh parameters

				Link side						PLC side	_
	Dev.	name	Points	Start	End		Dev. nar	ne	Points	Start	End -
Transfer SB	SB		512	0000	01FF	#	SB		512	0000	01FF
Transfer SW	SW		512	0000	01FF	#	SW	Т	512	0000	01FF
Random cyclic	LB					#		₹			
Random cyclic	LW					#		-			
Transfer1	LB	-	8192	0000	1FFF	#	В	-	8192	0000	1FFF
Transfer2	LW	-	8192	0000	1FFF	#	W	-	8192	0000	1FFF
Transfer3	LX	_	512	0000	01FF	#	X ·	₹	512	0000	01FF
Transfer4	LY	₩.	512	0000	01FF	#	Υ .	₹	512	0000	01FF
Transfer5		_				#		-			
Transfer6		~				#	•	-			-



#### (2) Operating the utility package

Operate the utility package on the remote I/O station side.

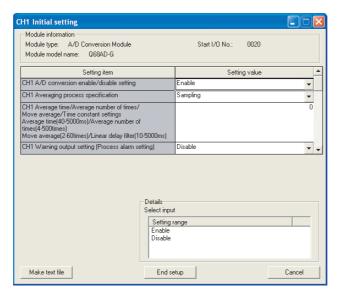
Set the following in the Intelligent function module parameter setting module select area.

• Start I/O No. : 20

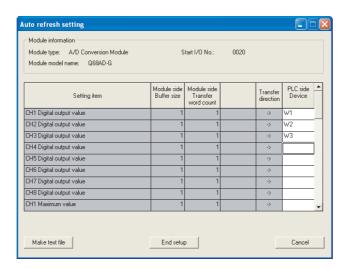
Module type : A/D Conversion Module

• Module model name: Q68AD-G

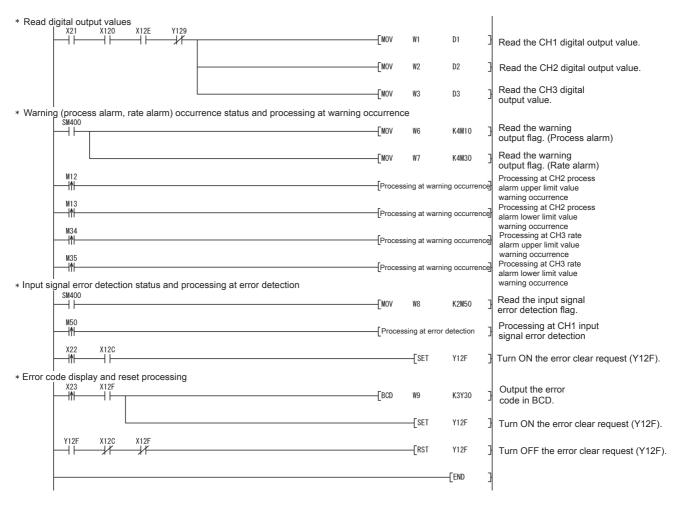
(a) Initial setting (Refer to Section 5.4) Set the initial settings of CH1 to 3. Refer to Section 6.3 for the settings.



(b) Auto refresh setting (Refer to Section 5.5) Set the digital output values, warning output flags, input signal error detection flags and error codes of CH1 to 3.



(c) Writing the intelligent function module parameters (Refer to Section 5.3.3) The intelligent function module parameters are written to the remote I/O station. This operation is performed using the intelligent function module parameter setting module select screen.



# **⊠**Point

To write the intelligent function module parameters, set the target remote I/O station from [Online] - [Transfer setup] on GX Developer.

They can be written by:

- Directly connecting GX Developer to the remote I/O station.
- Connecting GX Developer to another device such as a CPU module and passing through the network.



### 6.3.2 Programming example without using the utility package

## **⊠**Point

The dedicated instructions used for reading/writing the buffer memory of the intelligent function module on a remote I/O station (REMTO and REMFR) are the execution type for which several scans are needed. Therefore, transmissions of the execution results are not synchronized with the I/O signal operations. When reading a digital output value on an A/D converter module after changing the operating condition during operation, be sure to read the A/D conversion completed flag (buffer memory address 10) at the same time.

Also, for the case of changing the operating condition, insert an interlock to prevent the execution of the REMFR instruction.

#### (1) Operating GX Developer (CPU parameter setting)

Network type : MNET/H (Remote master)

- Starting I/O No. : 0000H

Network No. : 1Total stations : 1

Mode : Online

Network range assignment :

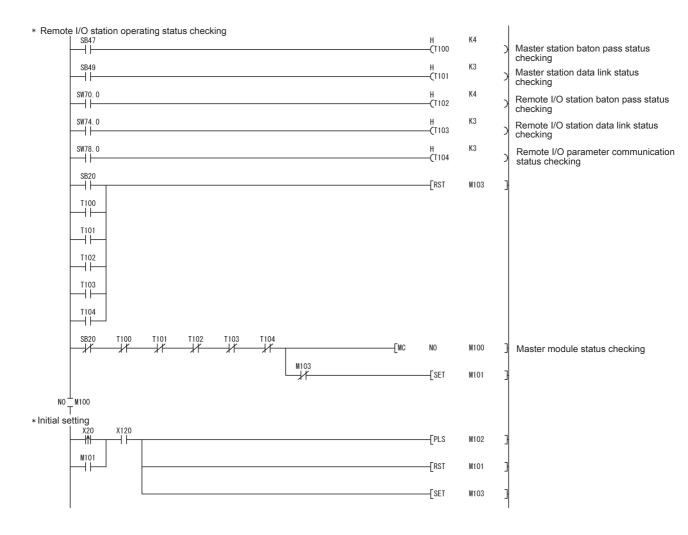
			M station	-> R station			M station <- R station						_
StationNo.	Y			Y			×			×			
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	Ш
1	256	0100	01FF	256	0000	00FF	256	0100	01FF	256	0000	00FF	7
	M stat	ion -> R st	ation	M stat	ion <-R st	ation	M stat	ion -> R st	ation	M stat	ion <-R st	ation	-

	M stati	on -> H sta	ation	M station <- H station			M stati	on -> Hista	ation	M station <- H station			
StationNo.		В		В			W			W			
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	
. 1							256	0000	00FF	256	0100	01FF	▼

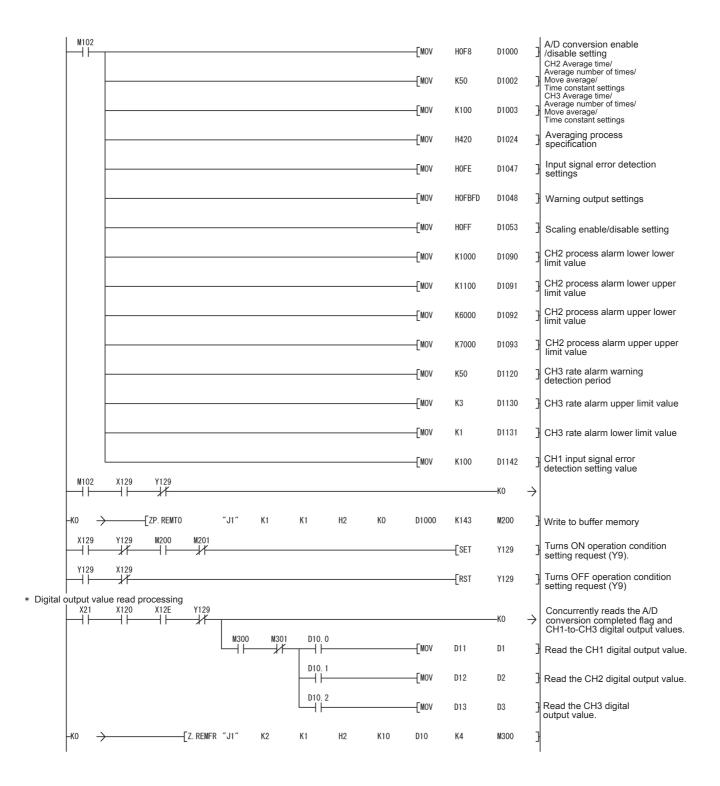
#### Refresh parameters

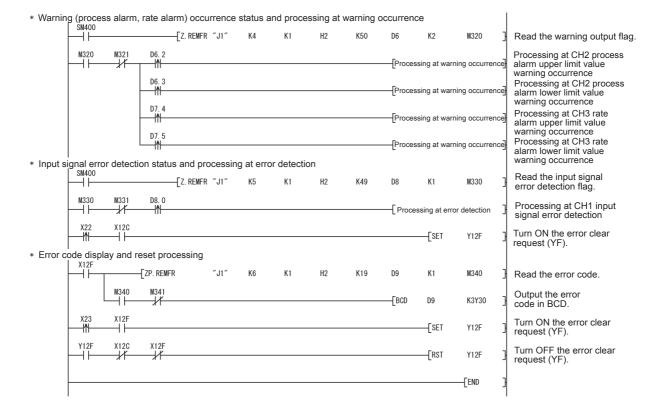
				Link side					PLC side	_
	Dev.	name	Points	Start	End		Dev. name	Points	Start	End —
Transfer SB	SB		512	0000	01FF	#	SB	512	0000	01FF
Transfer SW	SW		512	0000	01FF	#	SW	512	0000	01FF
Random cyclic	LB					#	•			
Random cyclic	LW					#	▼			
Transfer1	LB	~	8192	0000	1FFF	#	В ▼	8192	0000	1FFF
Transfer2	LW	-	8192	0000	1FFF	#	W 🔻	8192	0000	1FFF
Transfer3	LX	~	512	0000	01FF	#	X 🔻	512	0000	01FF
Transfer4	LY	~	512	0000	01FF	#	Υ 🔻	512	0000	01FF
Transfer5		▼				#	▼			
Transfer6		-				#	▼			▼

#### (2) Programming example





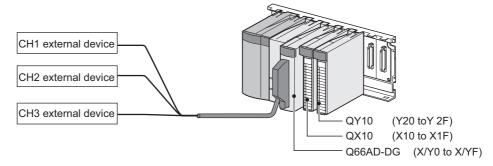






# 6.4 For Use in Normal System Configuration (Q66AD-DG)

#### (1) System configuration



#### (2) Conditions for the intelligent function module switch setting

	Input range setting	Normal resolution mode/ High resolution mode
CH1		
CH2	4 to 20mA	High resolution mode
CH3		
CH4		
to	not used	-
CH6		

#### (3) Program conditions

- (a) The following averaging processing specification is used for each channel.
  - · CH1: Sampling processing
  - CH2: Time averaging (50 times)
  - CH3: Primary delay filtering (100ms)
- (b) CH1 uses the input signal error detection function (Refer to Section 3.2.3.)
  - Input signal error detection: 10%
- (c) CH2 uses the warning output setting (process alarm) (Refer to Section 3.2.4 (1).)
  - Process alarm lower lower limit value: 1000
  - Process alarm lower upper limit value: 1500
  - Process alarm upper lower limit value: 6000
  - Process alarm upper upper limit value: 7000
- (d) CH3 uses the warning output setting (rate alarm) (Refer to Section 3.2.4 (2).)
  - Rate alarm warning detection period : 50ms
  - Rate alarm upper limit value: 0.3%
  - Rate alarm upper limit value: 0.1%
- (e) In the event of a write error, an error code shall be displayed in BCD format. The error code shall be reset after removal of the cause.



### 6.4.1 Before creating a program

**PROGRAMMING** 

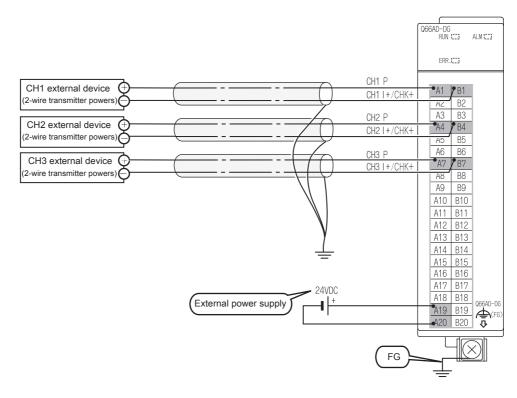
Perform the following steps before creating a program.

#### (1) Wiring of external devices

Mount the Q68AD-G on the base unit and connect the external devices.

• For all of CH1 to CH3, run the cables for current input.

For details, refer to "4.4.2 (2) (a) For 2-wire transmitter input".

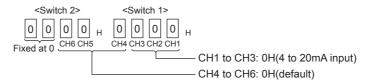




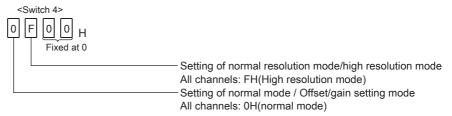
#### (2) Intelligent function module switch setting

Based on the setting conditions given in Section 6.2 (2), make the intelligent function module switch settings.

- (a) Each switch setting
  - 1) Switch1, Switch2: Input range setting



- 2) Switch3: Empty (No setting required)
- 3) Switch4: Mode setting

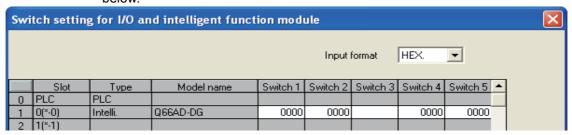


4) Switch5: Use prohibited (0:fixed)



If any other than 0 is set to Switch 5, an error occurs.

(b) Write the settings in (a) to the Q68AD-G On GX Developer's "Parameter setting" screen, select the "I/O assignment" tab, click "Switch setting", and make settings of Switch 1 to 5 on the screen shown below.



## 6.4.2 Programming example using the utility package

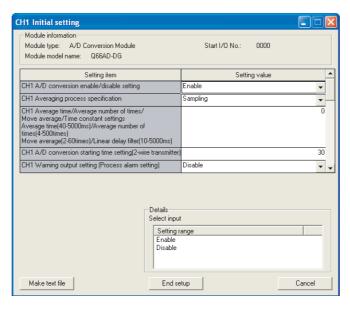
#### (1) List of devices

Device	Function		
D1	CH1 Digital output value		
D2	CH2 Digital output value		
D3	CH3 Digital output value		
D6,D7*1	Warning output flag		
D8 <sup>*1</sup>	Input signal error detection flag		
D9*1	Error code		
M0 to M2	A/D conversion completed flag		
M12,M13	CH2 Warning output flag (Process alarm)		
M34,M35	CH3 Warning output flag (Rate alarm)		
M50	CH1 Input signal error detection flag		
XC	Input signal error detection signal		
XE	A/D conversion completed flag		
XF	Error flag	Q66AD-DG (X/Y0 to X/YF)	
Y9	Operating condition setting request		
YF	Error clear request		
X10	Digital output value read command input signal		
X11	Input signal error detection reset signal	QX10 (X10 to X1F)	
X12	Error reset signal		
X14	Warning reset signal	1	
Y20 to Y2B	Error code display (BCD 3 digits)	QY10 (Y20 to Y2F)	

<sup>\*1:</sup>Devices used for the auto refresh function of GX Configurator-AD.

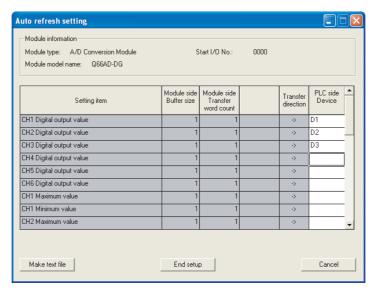
#### (2) Operating the utility package

(a) Initial setting (Refer to Section 5.4)Set the initial settings of CH1 and 2.Refer to Section 6.4 for the settings.





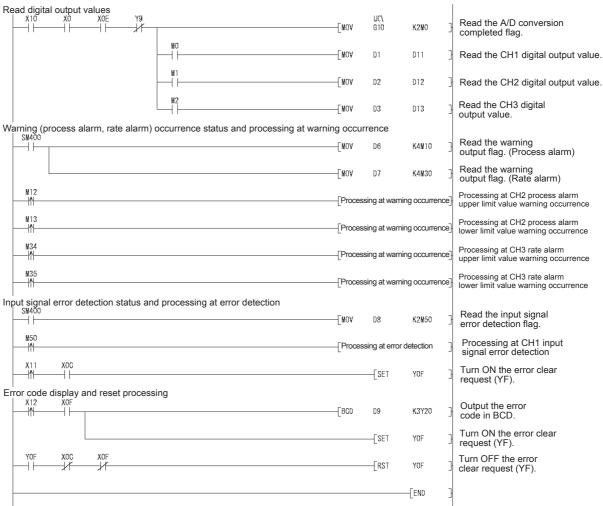
(b) Auto refresh setting (Refer to Section 5.5)
Set the digital output values, warning output flags, input signal error detection flags and error codes of CH1 and 2.



(c) Writing the intelligent function module parameters (Refer to Section 5.3.3) Write the intelligent function module parameters to the CPU module. This operation is performed using the parameter setting module selection screen.

#### (3) Programming example

**PROGRAMMING** 





# 6.4.3 Programming example without using the utility package

# (1) List of devices

Device	Function	
D1	CH1 Digital output value	
D2	CH2 Digital output value	
D3	CH3 Digital output value	
D6,D7	Warning output flag	
D8	Input signal error detection flag	
D9	Error code	
M0 to M2	A/D conversion completed flag	
M12,M13	CH2 Warning output flag (Process alarm)	
M34,M35	CH3 Warning output flag (Rate alarm)	
M50	CH1 Input signal error detection flag	
X0	Module ready	
XC	Input signal error detection signal	
XE	A/D conversion completed flag	Q66AD-DG (X/Y0 to X/YF)
XF	Error flag	QUUAD-DG (X/10 to X/11)
Y9	Operating condition setting request	
YF	Error clear request	
X10	Digital output value read command input signal	
X11	Input signal error detection reset signal	QX10 (X10 to X1F)
X12	Error reset signal	
X14	Warning reset signal	
Y20 to Y2B	Error code display (BCD 3 digits)	QY10 (Y20 to Y2F)

D3 Warning (process alarm, rate alarm) occurrence status and processing at warning occurrence | SW400 U0\ G50 -FMOV K4H10 \_⊏ผก∨ 651 KAM30 M12 177 M13 180 M34 1 M35

Read the CH2 digital output value.

Read the CH3 digital output value.

D2

Read the warning output flag. (Process alarm)

Read the warning output flag. (Rate alarm)

Processing at CH2 process alarm upper limit value warning occurrence Processing at CH2 process alarm lower limit value warning occurrence Processing at CH3 rate alarm upper limit value warning occurrence

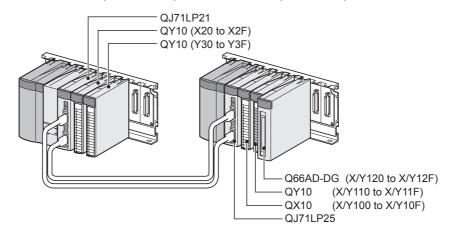
Processing at CH3 rate alarm lower limit value warning occurrence



# 6.5 For Use in Remote I/O Network (Q66AD-DG)

#### (1) System configuration

Remote master station (Network No. 1) Remote I/O station (Station No. 1)



#### (2) Conditions for the intelligent function module switch setting

	Input range setting	Normal resolution mode/ High resolution mode
CH1		
CH2	4 to 20mA	High resolution mode
CH3		
CH4		
to	not used	-
CH6		

Based on the setting conditions given in the above, make the intelligent function module switch settings.

Select the "I/O assignment" tab on the "Intelligent function module switch settings" screen, and click "Switch setting" to set the following values.

Switch No.	Setting value								
Switch 1	0000н	(CH1 to CH3: 4 to 20mA							
Switch 2	0000н	CH4 to CH6: Default)							
Switch 3	-								
Switch 4	0F00н (High resol	ution mode)							
Switch 5	0000н (0: Fixed)								

#### (3) Program conditions

- (a) The following averaging processing specification is used for each channel.
  - · CH1: Sampling processing
  - CH2: Time averaging (50 times)
  - CH3: Primary delay filtering (100ms)
- (b) CH1 uses the input signal error detection function (Refer to Section 3.2.3.)
  - Input signal error detection: 10%

- (c) CH2 uses the warning output setting (process alarm) (Refer to Section 3.2.4 (1).)
  - Process alarm lower lower limit value: 1000
  - Process alarm lower upper limit value: 1500
  - Process alarm upper lower limit value: 6000
  - Process alarm upper upper limit value: 7000
- (d) CH3 uses the warning output setting (rate alarm) (Refer to Section 3.2.4 (2).)
  - Rate alarm warning detection period : 50ms
  - Rate alarm upper limit value: 0.3%
  - Rate alarm upper limit value: 0.1%
- (e) In the event of a write error, an error code shall be displayed in BCD format. The error code shall be reset after removal of the cause.

#### (4) List of devices

Device	Function				
D1	CH1 Digital output value				
D2	CH2 Digital output value				
D3	CH3 Digital output value				
D6,D7 <sup>*1</sup>	Warning output flag				
D8 <sup>*1</sup>	Input signal error detection flag				
D9 <sup>*1</sup>	Error code				
M0 to M2	A/D conversion completed flag				
M12,M13	CH2 Warning output flag (Process alarm)				
M34,M35	CH3 Warning output flag (Rate alarm)				
M50	CH1 Input signal error detection flag				
X20	Digital output value read command input sig-				
<b>A20</b>	nal	QX10 (X20 to X2F)			
X21	Input signal error detection reset signal				
X22	Error reset signal				
X24	Warning reset signal				
Y30 to Y3B	Error code display (BCD 3 digits)	QY10 (Y30 to Y3F)			
X120	Module ready				
X129	Operating condition setting completed flag				
X12C	Input signal error detection signal	Q68AD-G			
X12E	A/D conversion completed flag	(X/Y120 to X/Y12F)			
X12F	Error flag	(1120 (0 11121)			
Y129	Operating condition setting request				
Y12F	Error clear request				

<sup>\*1:</sup>Devices used for the auto refresh function of GX Configurator-AD.

# ⊠Point

For details on the MELSECNET/H remote I/O network, refer to the Q Corresponding MELSECNET/H Network System Reference Manual (Remote I/O Network).



# 6.5.1 Programming example using the utility package

#### (1) Operating GX Developer

(a) CPU parameter setting

Network type : MNET/H (Remote master)

Starting I/O No. : 0000H
Network No. : 1
Total stations : 1
Mode : Online

• Network range assignment:

			M station	-> R statio	n		M station <- R station						•
StationNo.		Y			Y			×			×		
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	
1	256	0100	01FF	256	0000	00FF	256	0100	01FF	256	0000	00FF	<b>]</b>
	M stat	ion -> R st	ation	M stat	ion <-R st	ation	M stati	ion -> R st	ation	M stat	ion <-R st	ation	-
StationNo.		В		В			W			W			
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	
1							256	0000	OOFF	256	0100	01FF	7

#### Refresh parameters

				Link side			PLC side ▲						
	Dev.	name	Points	Start	End		Dev. name	Points	Start	End -			
Transfer SB	SB		512	0000	01FF	#	SB	512	0000	01FF			
Transfer SW	SW		512	0000	01FF	#	SW	512	0000	01FF			
Random cyclic	LB					#	~						
Random cyclic	LW					#	~						
Transfer1	LB	+	8192	0000	1FFF	#	В ▼	8192	0000	1FFF			
Transfer2	LW	*	8192	0000	1FFF	#	W •	8192	0000	1FFF			
Transfer3	LX	~	512	0000	01FF	#	X 🔻	512	0000	01FF			
Transfer4	LY	▼	512	0000	01FF	#	Y <b>▼</b>	512	0000	01FF			
Transfer5		-				#	•						
Transfer6		-				₩.	-						

Operate the utility package on the remote I/O station side.

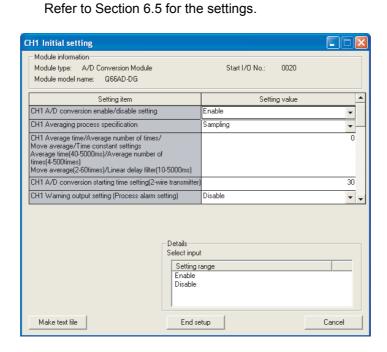
Set the following in the Intelligent function module parameter setting module select area.

• Start I/O No. : 20

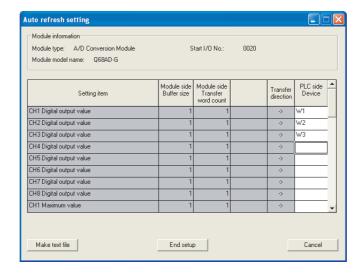
• Module type : A/D Conversion Module

• Module model name: Q66AD-DG

(a) Initial setting (Refer to Section 5.4)
Set the initial settings of CH1 and 2.



(b) Auto refresh setting (Refer to Section 5.5) Set the digital output values, warning output flags, input signal error detection flags and error codes of CH1 and 2.

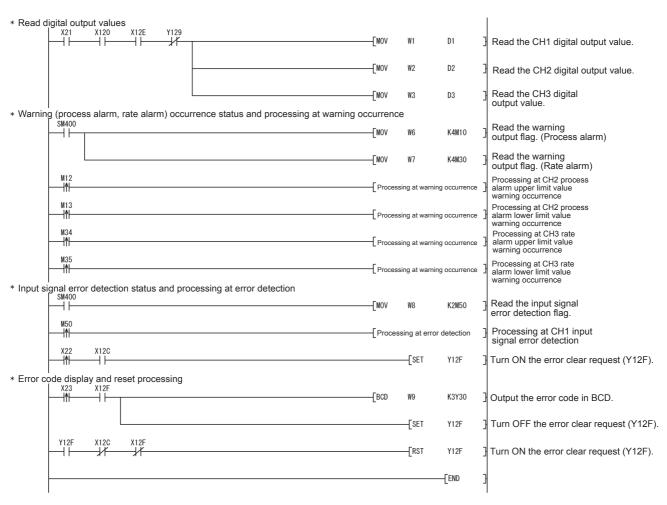


6



(c) Writing the intelligent function module parameters (Refer to Section 5.3.3) The intelligent function module parameters are written to the remote I/O station. This operation is performed using the intelligent function module parameter setting module select screen.

#### (3) Programming example



# **⊠**Point

To write the intelligent function module parameters, set the target remote I/O station from [Online] - [Transfer setup] on GX Developer.

They can be written by:

- Directly connecting GX Developer to the remote I/O station.
- Connecting GX Developer to another device such as a CPU module and passing through the network.

MELSEG Q series

### 6.5.2 Programming example without using the utility package

# **⊠**Point

The dedicated instructions used for reading/writing the buffer memory of the intelligent function module on a remote I/O station (REMTO and REMFR) are the execution type for which several scans are needed. Therefore, transmissions of the execution results are not synchronized with the I/O signal operations. When reading a digital output value on an A/D converter module after changing the operating condition during operation, be sure to read the A/D conversion completed flag (buffer memory address 10) at the same time.

Also, for the case of changing the operating condition, insert an interlock to prevent the execution of the REMFR instruction.

#### (1) Operating GX Developer (CPU parameter setting)

Network type : MNET/H (Remote master)

• Starting I/O No. : 0000н

Network No. : 1Total stations : 1

- Mode : Online

Network range assignment :

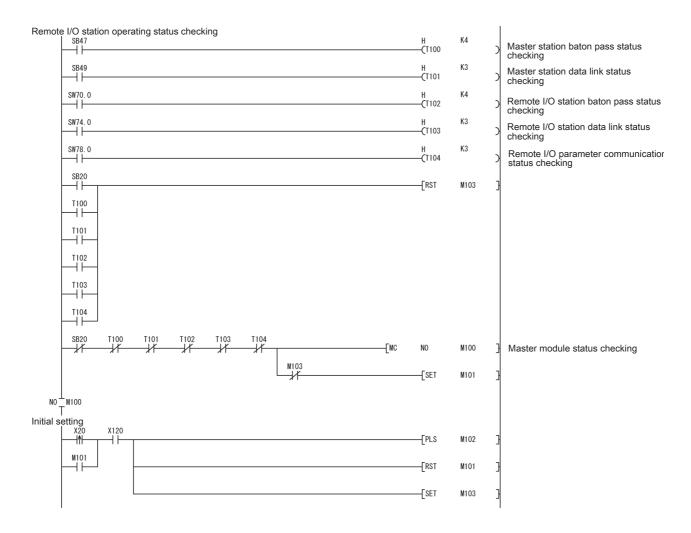
			M station	-> R statio	n		M station <- R station						•
StationNo.		Y			Y			×			×		
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	
1	256	0100	01FF	256	0000	00FF	256	0100	01FF	256	0000	00FF	7
	M stati	ion -> R sta	ation	M stati	on <- Rista	ation	M stati	on -> R sta	ation	M stati	on <- Rista	ation	
StationNo.	В			В			W			W			
	Points	Start	End	Points	Start	End	Points	Start	End	Points	Start	End	
1							256	0000	00FF	256	0100	01FF	1-1

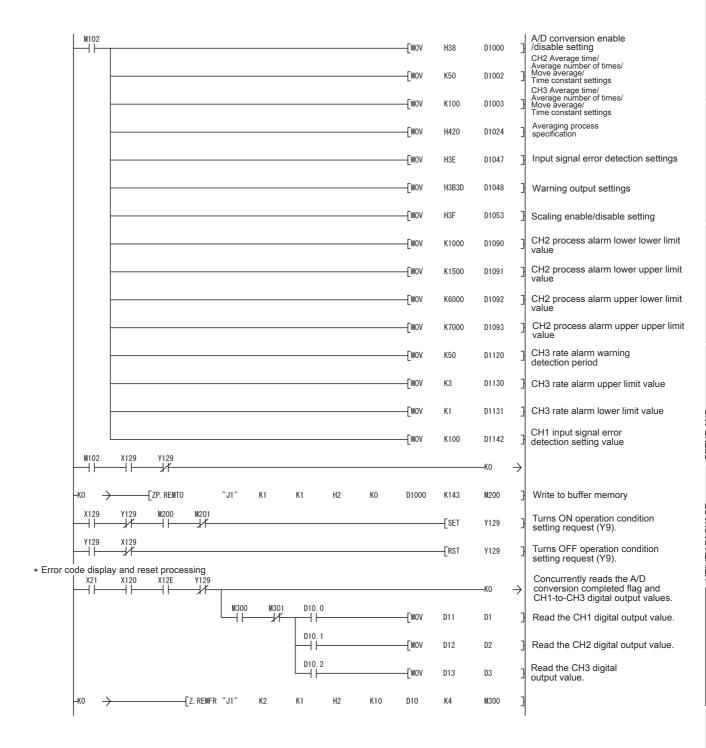
#### Refresh parameters

				Link side					PLC side		
	Dev.	name	Points	Start	End		Dev. name	Points	Start	End -	
Transfer SB	SB		512	0000	01FF	#	SB	512	0000	01FF	
Transfer SW	SW		512	0000	01FF	#	SW	512	0000	01FF	
Random cyclic	LB					#	~				
Random cyclic	LW					#	~				
Transfer1	LB	-	8192	0000	1FFF	#	В ▼	8192	0000	1FFF	
Transfer2	LW	-	8192	0000	1FFF	#	W 🔻	8192	0000	1FFF	
Transfer3	LX	-	512	0000	01FF	#	X 🔻	512	0000	01FF	
Transfer4	LY	~	512	0000	01FF	#	Υ 🔻	512	0000	01FF	
Transfer5		-				#	~				
Transfer6		-				<b>+</b>	•			•	

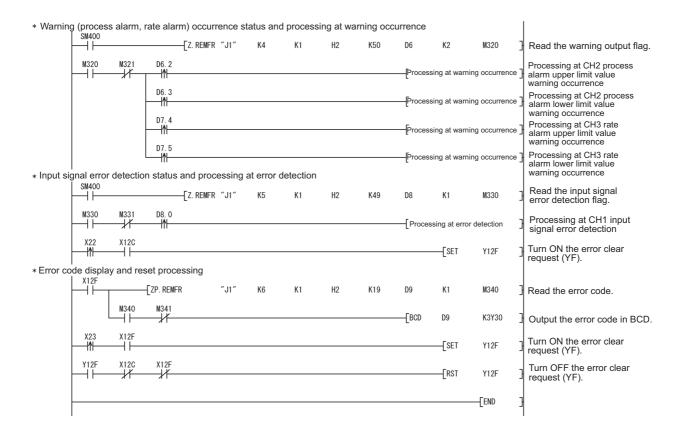


#### (2) Programming example









When changing a module online, carefully read the QCPU User's Manual (Hardware Design, Maintenance and Inspection), section 12.4.1 "Online module change". This chapter describes the specifications of an online module change.

- (1) Perform an online module change by operating GX Developer.
- (2) To ensure ease of offset/gain re-setting, there is a user range save/restoration function that is performed by executing the dedicated instruction or read/write from/to buffer memory.

## **⊠**Point

- (1) Perform an online module change after making sure that the system outside the programmable controller will not malfunction.
- (2) To prevent an electric shock and malfunction of operating modules, provide means such as switches for powering off each of the external power supply and external devices connected to the module to be replaced online.
- (3) After the module has failed, data may not be saved properly. Referring to Section 3.4.23, therefore, prerecord the data to be saved (offset/gain values of the industrial shipment settings and user range settings in the buffer memory).
- (4) It is recommended to perform an online module change in the actual system in advance to ensure that it would not affect the other modules by checking the following:
  - Means of cutting off the connection to external devices and its configuration are correct.
  - Switching ON/OFF does not bring any undesirable effect.
- (5) Do not install/remove the module to/from the base unit more than 50 times after the first use of the product. (IEC 61131-2 compliant) Failure to do so may cause malfunction.

### (Note)

The dedicated instruction cannot be executed during an online module change. When using the dedicated instruction to execute save/restoration, therefore, execute save/restoration in the other system.

If the other system is unavailable, execute restoration by performing write to the buffer memory.

\*: If the module is mounted on the remote I/O station, execute save/restoration in the other system mounted on the main base unit. (Save/restoration cannot be executed in the other system mounted on the remote I/O station.)



# 7.1 Online Module Change Conditions

The CPU, MELSECNET/H remote I/O module, A/D converter module, GX Developer and base unit given below are needed to perform an online module change.

### (1) CPU

The Process CPU is required.

For precautions for multiple CPU system configuration, refer to the QCPU User's Manual (Multiple CPU System).

### (2) MELSECNET/H remote I/O module

The module of function version D or later is necessary.

### (3) A/D converter module

The module of function version C or later is necessary.

### (4) GX Developer

GX Developer of Version 7.10L or later is necessary.

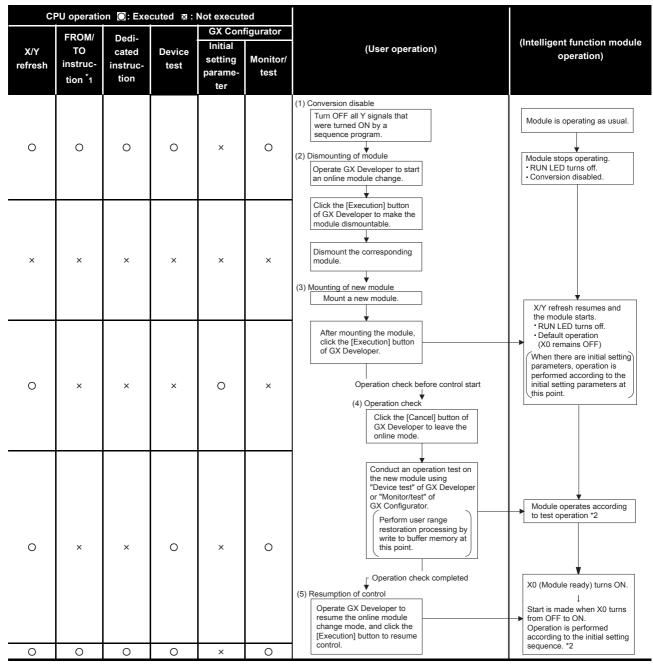
GX Developer of Version 8.18U or later is required to perform an online module change on the remote I/O station.

### (5) Base unit

- 1) When the slim type main base unit (Q3□SB) is used, an online module change cannot be performed.
- 2) When the power supply module unnecessary type extension base unit (Q5□B) is used, online module change cannot be performed for the modules on all the base units connected.

# 7.2 Online Module Change Operations

The following gives the operations performed for an online module change.



<sup>\*1:</sup> Access to the intelligent function module device (U □ \G □ ) is included.

<sup>\*2:</sup> In the absence of the operation marked \*2, the operation of the intelligent function module is the operation performed prior to that.



# 7.3 Online Module Change Procedure

There are the following online module change procedures depending on whether the user range setting has been made or not, whether the initial setting of GX Configurator-AD has been made or not, and whether the other system exists or not.

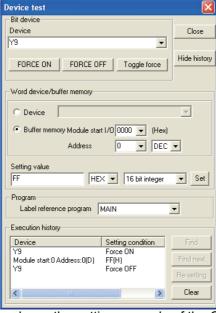
Range setting	Initial setting	Other system	Reference section
Industrial shipment setting	GX Configurator-AD	-	Section 7.3.1
Industrial shipment setting	Sequence program	-	Section 7.3.2
User range setting	GX Configurator-AD	Present	Section 7.3.3
User range setting	GX Configurator-AD	Absent	Section 7.3.4
User range setting	Sequence program	Present	Section 7.3.5
User range setting	Sequence program	Absent	Section 7.3.6

# 7.3.1 When industrial shipment setting is used and initial setting was made with GX Configurator-AD

### (1) Conversion disable

(a) Set the A/D conversion enable/disable setting (Un\G0) for all channel conversion disable and turn operating condition setting request (Y9) from OFF to ON to stop conversion.

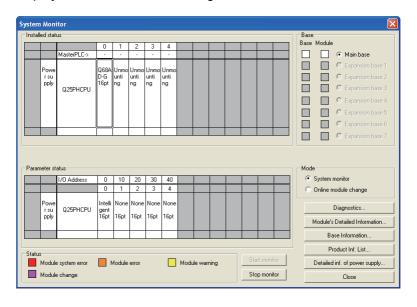
After confirming that conversion has stopped with the A/D conversion completion flag (Un\G10), turn off Operation Condition Setting Request (Y9).



(The screen shows the setting example of the Q68AD-G.)

## (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen



(b) Click the "Execution" button to enable a module change.



If the following error screen appears, click the [OK] button, dismount the module as-is, and mount a new module.



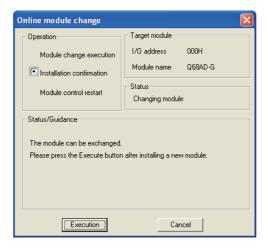
(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

# **⊠**Point

Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.

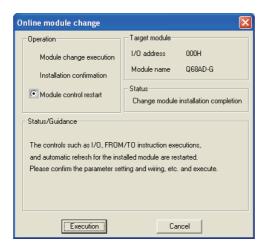
### (3) Mounting of new module

- (a) Mount a new module to the same slot and install the connector.
- (b) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.

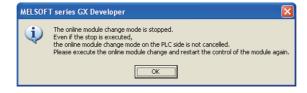


## (4) Operation check

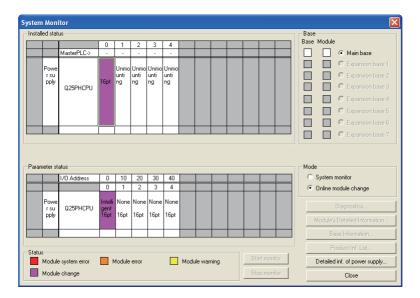
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



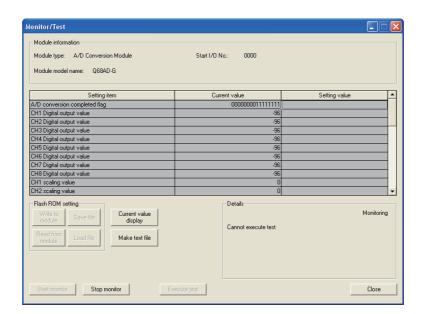
(b) Click the [OK] button to leave the "Online module change" mode.



(c) Click the [Close] button to close the System monitor screen.



(d) Monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.



### (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.





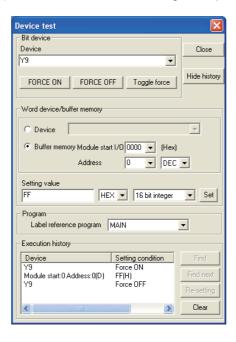
# 7.3.2 When industrial shipment setting is used and initial setting was made with sequence program

### (1) Conversion disable

(a) Set the A/D conversion enable/disable setting (Un\G0) for all channel conversion disable and turn operating condition setting request (Y9) from OFF to ON to stop conversion.

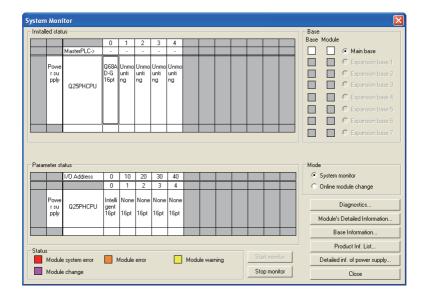
After confirming that conversion has stopped with the A/D conversion completion flag (Un\G10), turn off Operation Condition Setting Request (Y9).

(The screen shows the setting example of the Q68AD-G.)



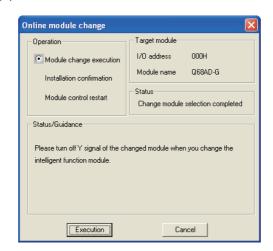
## (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen.



**PROGRAMMING** 

(b) Click the "Execution" button to enable a module change.



If the following error screen appears, click the [OK] button, dismount the module as-is, and mount a new module.



(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

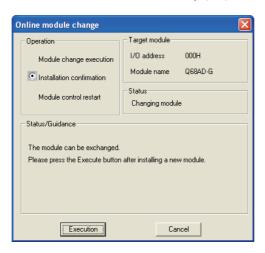
# **⊠**Point

Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.



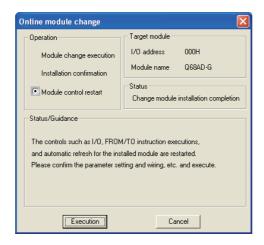
### (3) Mounting of new module

- (a) Mount a new module to the same slot and install the connector.
- (b) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.



## (4) Operation check

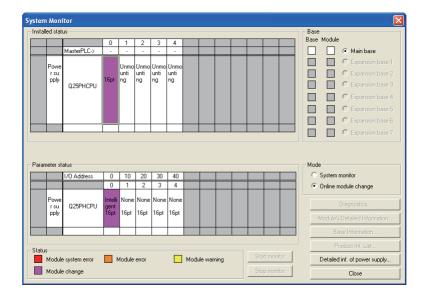
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



(b) Click the [OK] button to leave the "Online module change" mode.



(c) Click the [Close] button to close the System monitor screen.



- (d) Referring to (1), set the used channel to conversion enable, and monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.
- (e) Since the new module is in a default status, it must be initialized by a sequence program after control resumption.

Before performing initialization, check whether the contents of the initialization program are correct or not.

### 1) Normal system configuration

The sequence program should perform initialization on the leading edge of Module READY (X9) of the A/D converter module.

When control resumption is executed, Module READY (X0) turns ON and initialization is performed. (If the sequence program performs initialization only one scan after RUN, initialization is not performed.)

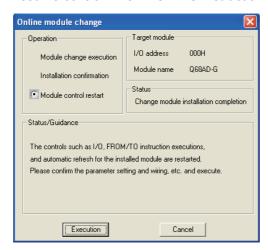
#### 2) When used on remote I/O network

Insert a user device that will execute initialization at any timing (initialization request signal) into the sequence program. After control resumption, turn ON the initialization request signal to perform initialization. (If the sequence program performs initialization only one scan after a data link start of the remote I/O network, initialization is not performed.)



### (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.

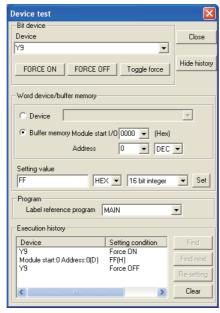


# 7.3.3 When user range setting is used and initial setting was made with GX Configurator-AD (other system is available)

### (1) Conversion disable

(a) Set the A/D conversion enable/disable setting (Un\G0) for all channel conversion disable and turn operating condition setting request (Y9) from OFF to ON to stop conversion.

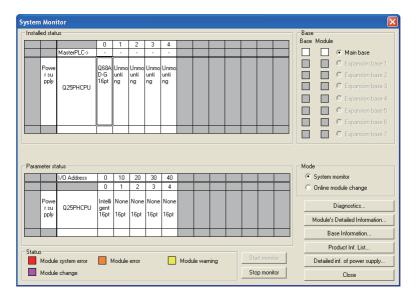
After confirming that conversion has stopped with the A/D conversion completion flag (Un\G10), turn off Operation Condition Setting Request (Y9).



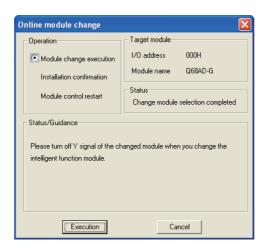
(The screen shows the setting example of the Q68AD-G.)

## (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen.



(b) Click the "Execution" button to enable a module change.



If the following error screen appears, the user range cannot be saved. Click the [OK] button, dismount the module as-is, and perform the operation in Section 7.3.4 (2) (c) and later.

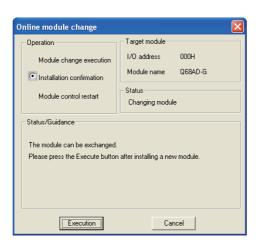


(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

# **⊠**Point

Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.

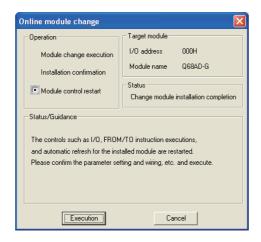
- (a) Mount the dismounted module and new module to the other system.
- (b) Using the G(P).OGLOAD instruction, save the user set values to the CPU device. Refer to Appendix 1.2 for the G(P).OGLOAD instruction.
- (c) Using the G(P).OGSTOR instruction, restore the user set values to the module. Refer to Appendix 1.3 for the G(P).OGSTOR instruction.
- (d) Dismount the new module from the other system, mount it to the slot from where the old module was dismounted in the original system, and install the connector.
- (e) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.





### (4) Operation check

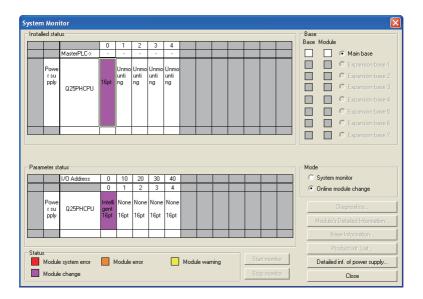
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



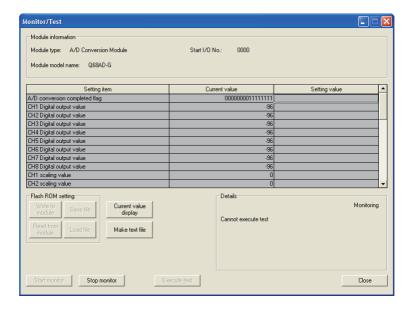
(b) Click the [OK] button to leave the "Online module change" mode.



(c) Click the [Close] button to close the System monitor screen.

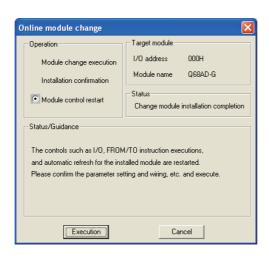


(d) Monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.



## (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.

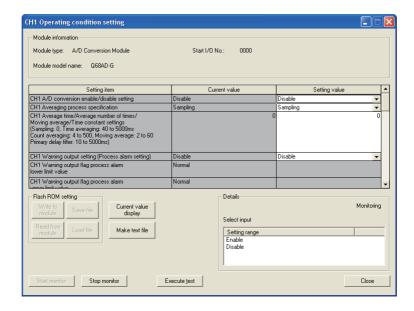




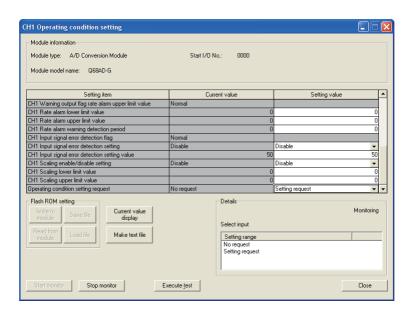
# 7.3.4 When user range setting is used and initial setting was made with GX Configurator-AD (other system is unavailable)

### (1) Conversion disable

(a) On the Operating condition setting screen of GX Configurator-AD, set "Disable" in the Setting value field of CH ☐ A/D conversion enable/disable setting, and click the [Execute test] button.



Monitor the A/D conversion completion flag (Un\G10) and confirm that conversion has stopped.



- (c) If the saved buffer memory contents are not yet prerecorded, record them in the following procedure.
  - 1) Display the pass data screen of GX Configurator-AD.
  - 2) Set the pass data classification setting \* and make a pass data read request. (Refer to Section 5.6.3, 5.6.4)
  - 3) Compare the current values of the industrial shipment settings and user range settings offset/gain values with those of the range reference table. Refer to Section 7.4 for the range reference table.
  - 4) If the values are proper, record the offset/gain values of the pass data classification setting, industrial shipment settings and user range settings.
  - \*: The Q66AD-DG does not require the setting and recording of the pass data classification setting.

## **⊠**Point

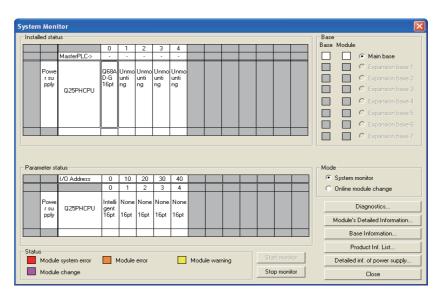
If the buffer memory values compared with the reference table are not proper, save and restoration of the user range cannot be executed.

Before executing module control resumption, make offset/gain setting in the GX Configurator-AD. (Refer to Section 5.6.2.)

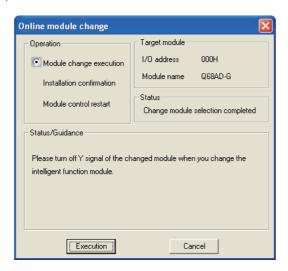
Note that if module control is resumed without offset/gain setting being made, operation will be performed with the default values.

## (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen.



(b) Click the "Execution" button to enable a module change.



If the following error screen appears, the user range cannot be saved. Click the [OK] button, dismount the module as-is, and perform the operation in Section (2) (c) and later.



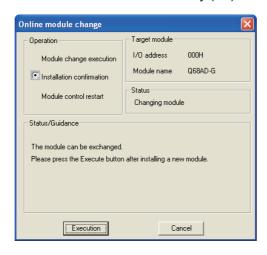
(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

# **⊠** Point

Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.

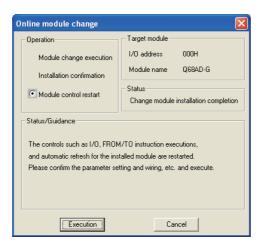
### (3) Mounting of new module

- (a) Mount a new module to the same slot and install the connector.
- (b) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.

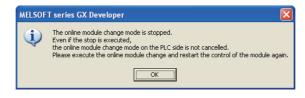


## (4) Oeration check

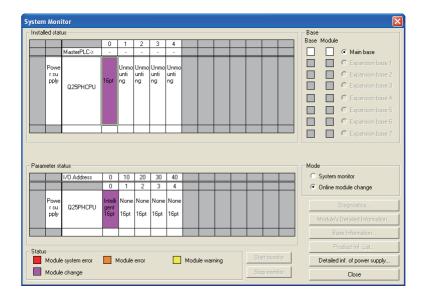
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



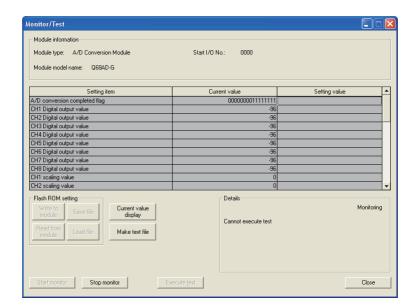
(b) Click the [OK] button to leave the "Online module change" mode.





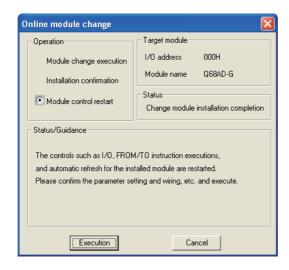


- (d) On the pass data screen of GX Configurator-AD, set the prerecorded values and make a pass data write request. (Refer to Section 5.6.3, 5.6.4.)
- (e) Monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.



### (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.



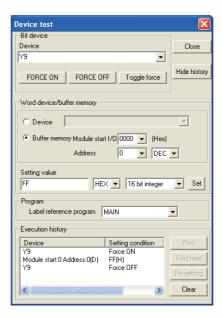


# 7.3.5 When user range setting is used and initial setting was made with sequence program (other system is available)

### (1) Conversion disable

(a) Set the A/D conversion enable/disable setting (Un\G0) for all channel conversion disable and turn operating condition setting request (Y9) from OFF to ON to stop conversion.

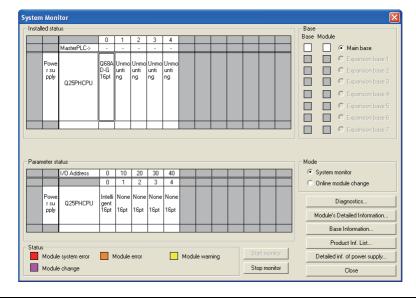
After confirming that conversion has stopped with the A/D conversion completion flag (Un\G10), turn off Operation Condition Setting Request (Y9).



(The screen shows the setting example of the Q68AD-G.)

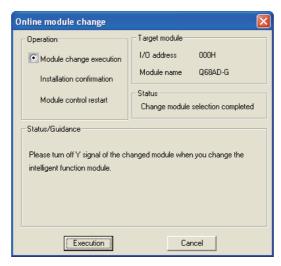
### (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen.



**ONLINE MODULE CHANGE** 

(b) Click the "Execution" button to enable a module change.



If the following error screen appears, the user range cannot be saved. Click the [OK] button, dismount the module as-is, and perform the operation in Section 7.3.6 (2) (c) and later.



(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

# **⊠**Point

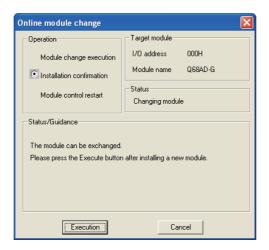
Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.

### (3) Mounting of new module

- (a) (Mount the dismounted module and new module to the other system.
- (b) Using the G(P).OGLOAD instruction, save the user set values to the CPU device. Refer to Appendix 1.2 for the G(P).OGLOAD instruction.
- (c) Using the G(P).OGSTOR instruction, restore the user set values to the module. Refer to Appendix 1.3 for the G(P).OGSTOR instruction.
- (d) Dismount the new module from the other system, mount it to the slot from where the old module was dismounted in the original system, and install the connector.

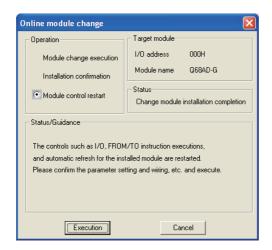


(e) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.

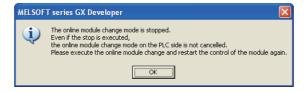


## (4) Operation check

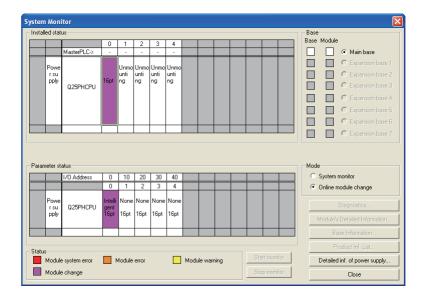
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



(b) Click the [OK] button to leave the "Online module change" mode.



(c) Click the [Close] button to close the System monitor screen.



- (d) Referring to (1), set the used channel to conversion enable, and monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.
- (e) Since the new module is in a default status, it must be initialized by a sequence program after control resumption.Before performing initialization, check whether the contents of the initialization

program are correct or not.

## 1) Normal system configuration

The sequence program should perform initialization on the leading edge of Module READY (X9) of the A/D converter module.

When control resumption is executed, Module READY (X0) turns ON and initialization is performed. (If the sequence program performs initialization only one scan after RUN, initialization is not performed.)

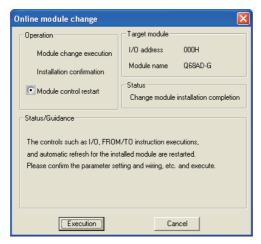
#### 2) When used on remote I/O networ

Insert a user device that will execute initialization at any timing (initialization request signal) into the sequence program. After control resumption, turn ON the initialization request signal to perform initialization. (If the sequence program performs initialization only one scan after a data link start of the remote I/O network, initialization is not performed.)



### (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.

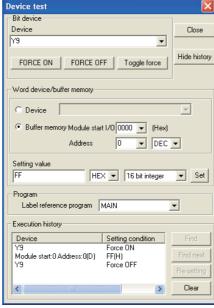


# 7.3.6 When user range setting is used and initial setting was made with sequence program (other system is unavailable)

### (1) Conversion disable

(a) Set the A/D conversion enable/disable setting (Un\G0) for all channel conversion disable and turn operating condition setting request (Y9) from OFF to ON to stop conversion.

After confirming that conversion has stopped with the A/D conversion completion flag (Un\G10), turn off Operation Condition Setting Request (Y9).



(The screen shows the setting example of the Q68AD-G.)



- (b) If the saved buffer memory contents are not yet prerecorded, record them in the following procedure.
  - 1) Make the pass data classification setting (Un\G200).
  - 2) Turn operating condition setting request (Y9) from OFF to ON.
  - 3) Compare the offset/gain values of the industrial shipment settings and user range settings (Un\G202 to Un\G233) with the range reference table. Refer to Section 7.4 for the range reference table.
  - 4) If the values are proper, record the offset/gain values of the pass data classification setting\*, industrial shipment settings and user range settings.
  - \*: The Q66AD-DG does not require the setting and recording of the pass data classification setting.

## **⊠** Point

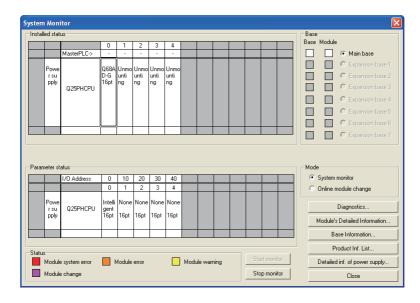
If the buffer memory values compared with the reference table are not proper, save and restoration of the user range cannot be executed. Before resuming module control, follow the flowchart in Section 4.6.1 for the Q68AD-G, or follow the flowchart in Section 4.6.2 for the Q66AD-DG, and make offset/gain setting in the device test of GX Developer.

Perform mode switching by making the setting of the mode switching setting (Un\G158, Un\G159) and turning operating condition setting request (Y9) from OFF to ON.

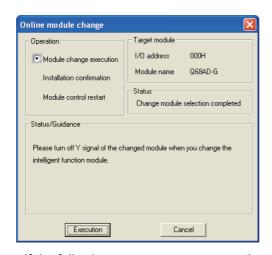
Note that if module control is resumed without offset/gain setting being made, operation will be performed with the default values.

### (2) Dismounting of module

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to enter the "Online module change" mode, double-click the module to be changed online to display the "Online module change" screen.



(b) Click the "Execution" button to enable a module change.



If the following error screen appears, the user range cannot be saved. Click the [OK] button, dismount the module as-is, and perform the operation in Section (2) (c) and later.



(c) After confirming that the "RUN" LED of the module has turned off, remove the connector and dismount the module.

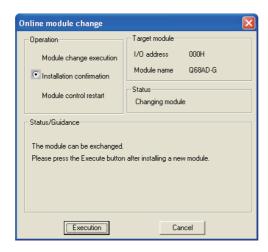
# **⊠**Point

Always dismount the module. If mounting confirmation is made without the module being dismounted, the module will not start properly and the "RUN" LED will not be lit.



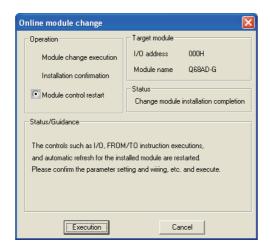
### (3) Mounting of new module

- (a) Mount a new module to the same slot and install the connector.
- (b) After mounting the module, click the [Execution] button and make sure that the "RUN" LED is lit. Module ready (X0) remains OFF.

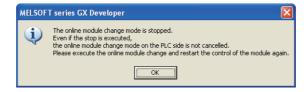


### (4) Operation check

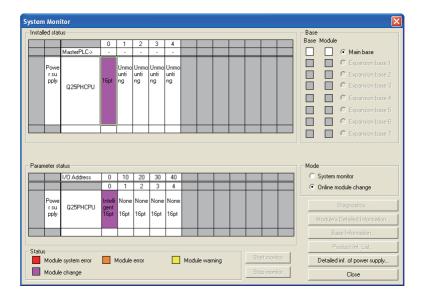
(a) To make an operation check, click the [Cancel] button to cancel control resumption.



(b) Click the [OK] button to leave the "Online module change" mode.



(c) Click the [Close] button to close the System monitor screen.



- (d) Choose [Online] [Debug] [Device test] on GX Developer and set the prerecorded values to the buffer memory.
- (e) Turn the user range writing request (YA) from OFF to ON to restore the user set values to the module.
  - After confirming that the offset/gain setting mode status flag (XA) is ON, turn OFF the user range write request (YA).
- (f) Referring to (1), set the used channel to conversion enable, and monitor the digital output values (Un\G11 to Un\G18) to check whether proper conversion has been made or not.
- (g) Since the new module is in a default status, it must be initialized by a sequence program after control resumption.
  - Before performing initialization, check whether the contents of the initialization program are correct or not.

### 1) Normal system configuration

The sequence program should perform initialization on the leading edge of Module READY (X9) of the A/D converter module.

When control resumption is executed, Module READY (X0) turns ON and initialization is performed. (If the sequence program performs initialization only one scan after RUN, initialization is not performed.)

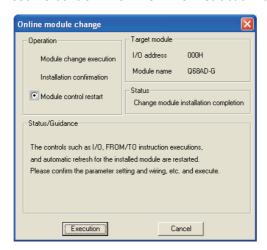
#### 2) When used on remote I/O network

Insert a user device that will execute initialization at any timing (initialization request signal) into the sequence program. After control resumption, turn ON the initialization request signal to perform initialization. (If the sequence program performs initialization only one scan after a data link start of the remote I/O network, initialization is not performed.)



### (5) Resumption of control

(a) After choosing [Diagnosis] - [Online module change] on GX Developer to redisplay the "Online module change" screen, click the [Execution] button to resume control. The FROM/TO instruction for the module resumes.



(b) The "Online module change completed" screen appears.



## 7.4 Range Reference Table

**ONLINE MODULE CHANGE** 

The range reference tables are given below.

# (1) Reference table for offset/gain values of industrial shipment settings (Un\G202 to Un\G217)

#### (a) For Q68AD-G

The reference values change depending on the setting of the pass data classification setting (Un\G200).

		Ad	dress	(Decin	nal)			Description	Pass data classification	Reference value
CH1	CH2	CH3	CH4	CH5	CH6	CH7	CH8	Description	setting (Un\G200)	(Hexadecimal)
202	204	206	208	210	212	214	216	Industrial shipment settings off-	Voltage specified (0V)	Approx. 0H
202	204	200	200	210	212	214	210	set value	Current specified (0mA)	Approx. 0H
203	205	207	209	211	213	215	217	Industrial shipment settings gain	Voltage specified (10V)	Арргох. 6666н
203	205	207	209	211	213	213	217	value	Current specified (20mA)	Арргох. 3333н

#### (b) For Q66AD-DG

		dress	`	,		Description	Reference value
CH1	CH2	CH3	CH4	CH5	CH6	Beschiption	(Hexadecimal)
202	204	206	208	210	212	Industrial shipment settings offset value	Approx. 0н
202	204	200	200	210	212	industrial shipment settings onset value	Approx. 0H
203	205	207	209	211	213	Industrial shipment settings gain value	Арргох. 6666н
203	200	201	209	Z 11	213	midustriai shipinent settings gairi value	Арргох. 3333н

# (2) Reference table for user range settings offset/gain values (Un\G218 to 233)

### (a) For Q68AD-G

Offset/ga	ain value	Reference value (Hexadecimal)	
	0V	Арргох. Он	
Voltage	1V	Approx. 0A3Dн	
voltage	5V	Арргох. 3333н	
	10V	Арргох. 6666н	
	0mA	Арргох. Он	
Current	4mA	Approx. 0A3Dн	
	20mA	Арргох. 3333н	

#### (b) For Q66AD-DG

Offset/gain value	Reference value (Hexadecimal)
0mA	Арргох. 0н
4mA	Approx. 10E5н
20mA	Approx. 547Вн

#### Example)

When the offset value of the Q68AD-G channel 1 is 1V and its gain value is 5V, the reference value of the CH1 user range settings offset value (Un\G218) is approximately 0A3DH, and the reference value of the CH1 user range settings gain value (Un\G220) is approximately 3333H.



# 7.5 Precautions for Online Module Change

The following are the precautions for online module change.

- (1) Always perform an online module change in the correct procedure. A failure to do so can cause a malfunction or failure.
- (2) If an online module change is made with the user range setting, the accuracy after that will fall to about less than three times of the accuracy before that.

  Re-set the offset/gain values as necessary.

# 8 TROUBLESHOOTING

The following section explains the types of errors that may occur when the A/D converter module is used, and how to troubleshoot such errors.

## 8.1 Error Code List

If an error occurs in the A/D converter module while writing to or reading data from the programmable controller CPU, an error code is written to buffer memory address 19 (Un\G19).

Table 8.1 Error code list (1/3)

Error code (decimal)	Error description	Action
10□	The input range is set with an illegal value in the intelligent function module switch setting in GX Developer.  □ indicates the channel number set incorrectly.	Set a correct parameter value in the parameter setting of GX Developer. (Refer to Section 4.5.)
111	Hardware error of the module.	Turn the power OFF and ON again. If the error occurs again, the module may be malfunctioning. Please consult your local Mitsubishi representative, explaining the detailed description of the problem.
112	The setting of the intelligent function module switch 5 is other than 0.	Set a correct parameter value in the parameter setting of GX Developer. (Refer to Section 4.5.)
120* <sup>1</sup>	An invalid value is set in the offset/gain setting.  The number of the error channel is not identified	Perform the offset/gain setting again for all of the chan- nels that use the user range settings. If the error occurs again, the module may be malfunctioning. Please consult your local Mitsubishi rep- resentative, explaining the detailed description of the problem.
12□* <sup>1</sup>	An invalid value is set in the offset/gain setting.  □ indicates the channel number set incorrectly.	Perform the offset/gain setting again for the error channel.  If the error occurs again, the module may be malfunctioning. Please consult your local Mitsubishi representative, explaining the detailed description of the problem.
161* <sup>2</sup>	The G(P).OGSTOR instruction was executed in the offset/gain setting mode.	Do not execute the G(P).OGSTOR instruction in the off- set/gain setting mode.
162	<ul> <li>The G(P).OGSTOR instruction was executed consecutively.</li> <li>At the time of offset/gain setting, a set value was written to the E<sup>2</sup>PROM 26 or more times.</li> </ul>	<ul> <li>Execute the G(P).OGSTOR instruction only once for one module.</li> <li>At the time of offset/gain setting, write a set value only once at one time.</li> </ul>
163	The G(P).OGSTOR instruction was executed for the model that differs from the model for which the G(P).OGLOAD instruction had been executed.	Execute the G(P).OGLOAD and G(P).OGSTOR instructions for the same model.



#### Table 8.1 Error code list (2/3)

Error code (decimal)	Error description	Processing
20□*1	The averaging time set in Un\G1 to Un\G8 is outside the range of 40 to 5000ms.  □ indicates the channel number set incorrectly.	Reset the averaging time setting to within 40 to 5000ms. Also, the set value must be "4 x 10 x No. of channels used" or greater.
30□*1	The averaging count set in Un\G1 to Un\G8 is outside the range of 4 to 500 times.  □ indicates the channel number set incorrectly.	Reset the averaging count setting to within 4 to 500 times.
31□*1	The moving average count set in Un\G1 to Un\G8 is outside the range of 2 to 60 times.  □ indicates the channel number set incorrectly.	Reset the moving average count setting to within 2 to 60 times.
32□*1	The time constant for the primary delay filter set in Un\G1 to Un\G8 is outside the range of 10 to 5000.  □ indicates the channel number set incorrectly.	Reset the time constant setting to within 10 to 5000. Also, the set value must be "10 x No. of channels used" or greater
34□*1	In the CH□ rate alarm upper/lower limit value (Un\G126 to Un\G141) setting, Lower limit ≧ Upper limit. □ indicates the channel number set incorrectly.	Reset the CH rate alarm upper/lower limit value (Un\G126 to Un\G141) so that the lower limit value is smaller than the upper limit value.
35□* <sup>1</sup> (Q66AD-DG only)	The conversion starting time setting (for 2-wire transmitter) (Un\G78 to Un\G83) is outside the range of 0 to 32767.  □ indicates the channel number set incorrectly.	Reset the conversion starting time setting (for 2-wire transmitter) to within 0 to 32767.
40□* <sup>1</sup>	The offset value is equal to or greater than the gain value at the time of user range setting or user range restoration.  ☐ indicates the error causing channel number.	Reset so that the offset value becomes smaller than the gain value.
500* <sup>1</sup>	The offset/gain channels were set at the same time during offset and gain value settings, or both were set to 0.	Reset the contents of buffer memory addresses 22 and 23 (Un\G22 and Un\G23).

## **⊠**Point

- (1) When two or more errors have occurred, the latest error found by the A/D converter module is stored.
- (2) The error described with \*1 can be cleared by turning ON the error clear request (YF).
- (3) Error code 161 marked \*2 is not stored in the Error code (Un\G19) area. It is written to the Completion status area, (S) + 1, of the G(P).OGSTOR instruction.



# 8.2 Troubleshooting

## 8.2.1 When the "RUN" LED is flashing or turned off

#### (1) When flashing

Check item	Corrective action
Is the mode set to the offset/gain setting mode?	Reset switch 4 of the intelligent function module switch setting for
is the mode set to the onsergant setting mode?	GX Developer to the normal mode (Refer to Section 4.5).

#### (2) When off

Check item	Corrective action
Is the power being supplied?	Confirm that the supply voltage for the power supply module is
is the power being supplied:	within the rated range.
	Calculate the current consumption of the CPU module, I/O module
Is the capacity of the power supply module adequate?	and intelligent function module mounted on the base unit to see if
	the power supply capacity is adequate.
	Reset the programmable controller CPU and verify that it is lit. If
Has a watchdog timer error occurred?	the RUN LED does not light even after doing this, the module may
rias a watchdog timer error occurred:	be malfunctioning. Please consult your local Mitsubishi represen-
	tative, explaining the detailed description of the problem.
Is the module correctly mounted on the base unit?	Check the mounting condition of the module.
Is a module change enabled during an online module change?	Refer to Chapter 7 and take corrective action.

## 8.2.2 When the "ERR." LED is on or flashing

#### (1) When on

Check item	Corrective action
la an arrar baing generated?	Confirm the error code and take corrective action described in
Is an error being generated?	Section 8.1.

#### (2) When flashing

Check item	Corrective action
Is intelligent function module setting switch 5 set to "other than 0"?	Using GX Developer parameter setting, set intelligent function
is intelligent function module setting switch 3 set to other than 0 :	module setting switch 5 to "0" (Refer to Section 4.5).

## 8.2.3 When the "ALM" LED is on or flashing

#### (1) When on

Check item	Corrective action
Is a warning output being generated?	Check the warning output flag (Un\G50, Un\G51).

## (2) When flashing

Check item	Corrective action
Is an input signal error being generated?	Check the input signal error detection flag (Un\G49).

#### When the digital output values cannot be read 8.2.4

Check item	Corrective action
Is 24VDC external supply power being supplied? (Q66AD-DG only)	Check that the external supply power terminals (between terminals No. A19 and A20 or B19 and B20) are supplied with a 24VDC
Is there any fault with the analog signal lines such as disconnection (for the Q66AD-DG, disconnection of the signal line with the 2-wire transmitter) or wire break?	voltage.  Check for faulty condition of the signal lines by a visual check and a continuity check.
Is the CPU module in the STOP status?	Set the CPU module to the RUN status.
Are the offset/gain settings correct?	Verify that the offset/gain settings are correct (Refer to Section 4.6 and 5.6.2).  If the user range is being used, switch to a different default input range and check if A/D conversion is correctly performed. If so redo the offset/gain settings.
Is the input range setting correct?	Check the Un\G20, Un\G21 in the monitor of GX Developer.  If the input range setting is incorrect, redo the GX Developer intelligent function module switch setting (Refer to Section 4.5).
Is the A/D conversion enable/disable setting for the channel to be used set to A/D conversion disabled?	Check the ON/OFF status with Un\G0 in GX Developer monitor and review the initial setting of the sequence program or utility package (Refer to Section 3.4).
Is a large value set as the conversion starting time setting (for the 2-wire transmitter)?	Check the Un\G78 to Un\G83 in the monitor of GX Developer.
Has the operating condition setting request (Y9) been executed?	From GX Developer, turn the operating condition setting request (Y9) from ON to OFF to check that the digital output values are stored into the Un\G11 to Un\G18.  If so, review the initial setting of the sequence program or utility package (Refer to Section 3.3).
Is the value set for the averaging processing specification correct?	<ul> <li>For the time averaging, set a value of "4 (times) x 10 (ms) x No. of channels" or greater.</li> <li>For the primary delay filter, set a value of "10 (ms) x No. of channels" or greater.</li> <li>If the above requirements are not met, 0 is stored as a digital output value.</li> </ul>
In the case of current input are terminals (V+) and ( +) connected ?	In the case of current input, connect terminals (V+) and ( +)
(For the Q68AD-G only)	referring to Section 4.4.2.

# **⊠**Point

The module may be faulty if the digital output values cannot be read after proper corrective actions have been taken according to the above check items. Please consult your local Mitsubishi representative, explaining the detailed description of the problem.



# 8.2.5 When A/D conversion completed flag does not turn ON during use in normal mode

Check item	Corrective action
Is 24VDC external supply power being supplied? (Q66AD-DG	Check that the external supply power terminals (between terminals
	No. A19 and A20 or B19 and B20) are supplied with a 24VDC
only)	voltage.
Is an input signal error being generated?	Check the input signal error detection flag (Un\G49).

# 8.2.6 Checking the A/D converter module status using GX Developer system monitor

When the A/D converter module detail information is selected in GX Developer system monitor, error code, LED ON status and status of the intelligent function module switch setting can be checked.

#### (1) Operating GX Developer

[Diagnostics] → [System monitor] → Select the A/D conversion module checking the status. → Module Detailed Information

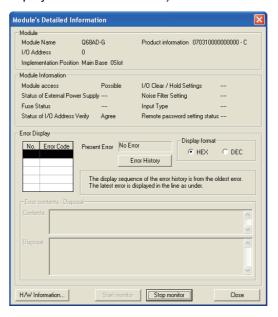
#### (2) Module Detail Information

- (a) Checking the function version and product information

  The function version and product information of the A/D converter module are displayed in the product information field.
- (b) Checking the error code

The error code stored in buffer memory address 19 (Un\G19) of the A/D converter module is displayed in the Present Error field.

(When the Error History button is pressed, the contents displayed in the Present Error field are displayed in the No. 1 field.)



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#### (3) H/W information

#### (a) H/W LED information

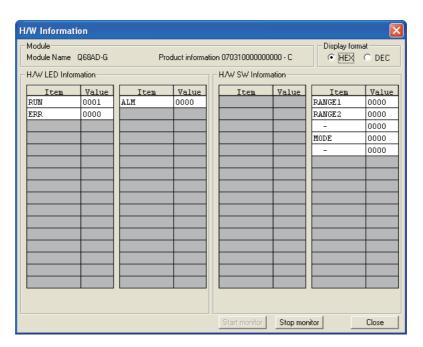
The LED ON status is displayed.

No.	LED name	Status
1)	RUN LED	0000н : Indicates that LED is unlit.
2)	ERR. LED	0001н : Indicates that LED is lit.
3)	ALM LED	Alternate display of 0000H and 0001H: Indicates that LED is flashing.

#### (b) H/W SW information

The status of the intelligent function module switch setting is displayed.

No.	Switch setting for intelligent function module
1	Switch 1
2	Switch 2
3	Switch 3
4	Switch 4
5	Switch 5



For GX Developer Version 8



## **APPENDIX**

# **Appendix 1 Dedicated Instruction List and Available Devices**

#### (1) Dedicated instruction list

The following table lists the dedicated instructions that can be used with the A/D converter modules.

Instruction	Description	Reference sec- tion	
G(P).OFFGAN	Switches to the offset/gain setting mode.	Appendix 1.1	
G(F).OIT GAIN	Switches to the normal mode.	Appendix 1.1	
G(P).OGLOAD	Reads the offset/gain values of the user range setting to the	Appendix 1.2	
O(I ).OOLOAD	CPU.		
G(P).OGSTOR	Restores the offset/gain values of the user range setting stored	Appendix 1.3	
G(1 ).5051010	in the CPU to the A/D converter module.	дрених 1.5	

### **⊠**Point

When the module is mounted to a MELSECNET/H remote station, the dedicated instructions cannot be used.

#### (2) Available devices

The following devices are available for the dedicated instructions:

Internal	devices	File register	Constant
Bit <sup>*1</sup>	Word	File register	Constant
X, Y, M, L, F, V, B	T, ST, C, D, W	R, ZR	-

\* Word device bit designation can be used as bit data.

Word device bit designation is done by designating Word device . Bit No. . (Designation of bit numbers is done in hexadecimal.)

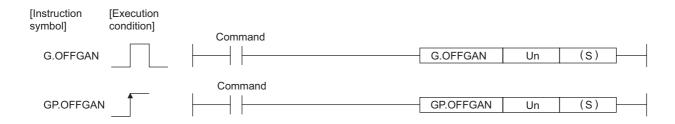
For example, bit 10 of D0 is designated as D0.A.

However, there can be no bit designation for timers (T), retentive timers (ST) and counters (C).

## Appendix 1.1 G(P).OFFGAN

Switches the mode of the A/D converter module. (Normal mode to offset/gain setting mode, offset/gain setting mode to normal mode)

					Usable (	devices				
	Internal device			Link direct device Intelligent		Constant				
Set data	(System	ı, user)	File	J□	] <b>\</b> □	function	Index reg-		Statit	
oor autu	Bit	Word	register	Bit	Word	module device U⊟\G⊟	ister Z□	K, H	\$	Other
(S)	_	(	)	_		_		_	_	_



#### Set data

Set data	Description	Setting range	Data type
Un	Start I/O number of the module	0 to FEH	Binary 16 bits
	Mode switching		
	0: Switching to normal mode		
(S)	1: Switching to offset/gain setting mode	0 ,1	Binary 16 bits
	The setting of any other value results in "switching to offset/gain setting		
	mode".		

#### (1) Function

Switches the mode of the A/D converter module.

- Normal mode to offset/gain setting mode (the offset/gain setting mode flag (XA) turns ON)
- Offset/gain setting mode to normal mode (the offset/gain setting mode flag (XA) turns OFF)



## **⊠**Point

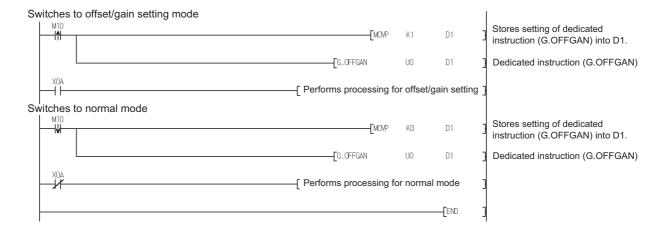
- (1) When the offset/gain setting mode is switched to the normal mode, Module ready (X0) turns from OFF to ON.
  - Note that initial setting processing will be executed if there is a sequence program that makes initial setting when Module ready (X0) turns ON.
- (2) When one mode is switched to the other (the normal mode is switched to the offset/gain setting mode or the offset/gain setting mode is switched to the normal mode), the A/D converter module suspends A/D conversion and switches OFF the power supply to the 2-wire transmitter.
  - To resume A/D conversion and power supply to the 2-wire transmitter, turn ON the operating condition setting request (Y9) after the mode is switched to the normal mode.

#### (2) Operation error

No errors.

#### (3) Program example

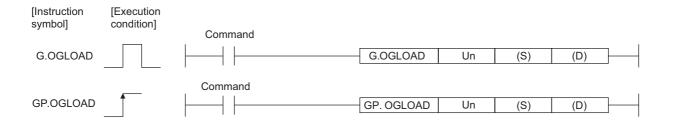
The following program is designed to switch the A/D converter module mounted in the position of I/O number X/Y0 to X/YF to the offset/gain setting mode when M10 is turned ON, and to return it to the normal mode when M10 is turned OFF.



## Appendix 1.2 G(P).OGLOAD

Reads the offset/gain values of the user range setting of the A/D converter module to the CPU.

		Usable devices								
	Internal device			Link direct device Intelligent			Constant			
Set data	(System, user)		File	J□	<b>1</b> \□	function Index reg-				
Get data	Bit	Word	register	Bit	Word	module device U⊟\G□	ister Z□	K, H	\$	Other
(S)	_	(	)					_	_	_
(D)	0				_		_	_	_	



#### Set data

Set data	Description	Setting range	Data type
Un	Start I/O number of the module	0 to FEн	Binary 16 bits
(S)	Start number of the device in which control data is stored.	Within the range of the specified device	Device name
(D)	Device that is turned ON 1 scan on completion of dedicated instruction processing.  (D) + 1 also turns ON at an abnormal completion.	Within the range of the speci- fied device	Bit



## Control data\*1 of Q68AD-G

Device	ltem	Set data	Setting range	Set by
(S)	System area	_	_	_
		Stores the status when the instruction is		
(C) ± 1	Completion status	complete.		System
(S) + 1	Completion status	0 : Normal completion	_	System
		Other than 0: Abnormal completion		
		Specify the voltage/current of the offset/gain		
		values to be read.		
		0: Voltage specified		
(S) + 2	Pass data classification setting	1: Current specified	0000н to 00FF н	User
, ,				
		b15 b8 b7 b6 b5 b4 b3 b2 b1 b0		
		0 ~ 0 CH8 CH7 CH6 CH5 CH4 CH3 CH2 CH1		
(S) + 3	System area	_	_	
(S) + 4	CH1 Industrial shipment settings offset value	_	_	System
(S) + 5	CH1 Industrial shipment settings gain value	_	_	System
(S) + 6	CH2 Industrial shipment settings offset value	_	_	System
(S) + 7	CH2 Industrial shipment settings gain value	_	_	System
(S) + 8	CH3 Industrial shipment settings offset value	_	_	System
(S) + 9	CH3 Industrial shipment settings gain value	_	_	System
(S) + 10	CH4 Industrial shipment settings offset value	_	_	System
(S) + 11	CH4 Industrial shipment settings gain value	_	_	System
(S) + 12	CH5 Industrial shipment settings offset value	_	_	System
(S) + 13	CH5 Industrial shipment settings gain value	_	_	System
(S) + 14	CH6 Industrial shipment settings offset value	_	_	System
(S) + 15	CH6 Industrial shipment settings gain value	_	_	System
(S) + 16	CH7 Industrial shipment settings offset value	_	_	System
(S) + 17	CH7 Industrial shipment settings gain value	_	_	System
(S) + 18	CH8 Industrial shipment settings offset value	_	_	System
(S) + 19	CH8 Industrial shipment settings gain value	_	_	System
(S) + 20	CH1 user range settings offset value	_	_	System
(S) + 21	CH1 user range settings gain value	_	_	System
(S) + 22	CH2 user range settings offset value	_	_	System
(S) + 23	CH2 user range settings gain value	_	_	System
(S) + 24	CH3 user range settings offset value	_	_	System
(S) + 25	CH3 user range settings gain value	_	_	System
(S) + 26	CH4 user range settings offset value	_	_	System
(S) + 27	CH4 user range settings gain value	_	_	System
(S) + 28	CH5 user range settings offset value	_	_	System
(S) + 29	CH5 user range settings gain value	_	_	System
(S) + 30	CH6 user range settings offset value	_	_	System
(S) + 31	CH6 user range settings gain value	_	_	System
(S) + 32	CH7 user range settings offset value	_	_	System
(S) + 33	CH7 user range settings gain value	_	_	System
(S) + 34	CH8 user range settings offset value	_	_	System
(S) + 35	CH8 user range settings gain value	_	_	System

<sup>\*1</sup> Set only the pass data classification setting (S)+2. If data is written to the area set by the system, the offset/gain values will not be read properly.

## Control data\*2 of Q66AD-DG

Device	ltem	Set data	Setting range	Set by
(S)	System area	_	_	_
		Stores the status when the instruction is		
(C) ± 1	Completion status	complete.		System
(S) + 1	Completion status	0 : Normal completion	_	System
		Other than 0: Abnormal completion		
(S) + 2	System area	_	_	
(S) + 3				
(S) + 4	CH1 Industrial shipment settings offset value	_	_	System
(S) + 5	CH1 Industrial shipment settings gain value	_	_	System
(S) + 6	CH2 Industrial shipment settings offset value	_	_	System
(S) + 7	CH2 Industrial shipment settings gain value	_	_	System
(S) + 8	CH3 Industrial shipment settings offset value	_	_	System
(S) + 9	CH3 Industrial shipment settings gain value	_	_	System
(S) + 10	CH4 Industrial shipment settings offset value	_	_	System
(S) + 11	CH4 Industrial shipment settings gain value	_	_	System
(S) + 12	CH5 Industrial shipment settings offset value	_	_	System
(S) + 13	CH5 Industrial shipment settings gain value	_	_	System
(S) + 14	CH6 Industrial shipment settings offset value	_	_	System
(S) + 15	CH6 Industrial shipment settings gain value	_	_	System
(S) + 16				
to	System area	_	_	_
(S) + 19				
(S) + 20	CH1 user range settings offset value	_	_	System
(S) + 21	CH1 user range settings gain value	_	_	System
(S) + 22	CH2 user range settings offset value	_	_	System
(S) + 23	CH2 user range settings gain value	_	_	System
(S) + 24	CH3 user range settings offset value	_	_	System
(S) + 25	CH3 user range settings gain value	_	_	System
(S) + 26	CH4 user range settings offset value	_	_	System
(S) + 27	CH4 user range settings gain value	_	_	System
(S) + 28	CH5 user range settings offset value	_	_	System
(S) + 29	CH5 user range settings gain value	_	_	System
(S) + 30	CH6 user range settings offset value	_	_	System
(S) + 31	CH6 user range settings gain value	_	_	System
(S) + 32				
to	System area	_	_	_
(S) + 35				

<sup>\*2</sup> Setting is not necessary. If setting is made, the offset/gain values will not be read properly.



#### (1) Functions

- (a) Reads the offset/gain values of the user range setting of the A/D converter module to the CPU.
- (b) There are two types of interlock signals for the G(P).OGLOAD instruction: the completion device (D) and the status display device at completion (D) + 1.

#### 1) Completion device

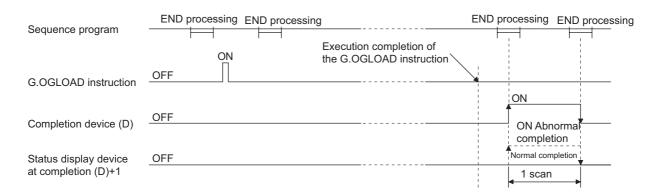
Turns ON in the END processing of the scan where the G(P).OGLOAD instruction is completed, and turns OFF in the next END processing.

#### 2) Status display device at completion

Turns ON and OFF depending on the completion status of the G(P).OGLOAD instruction.

Normal completion : Stays OFF and does not change.

Abnormal completion: Turns ON in the END processing of the scan where the G(P).OGLOAD instruction is completed, and turns OFF in the next END processing.

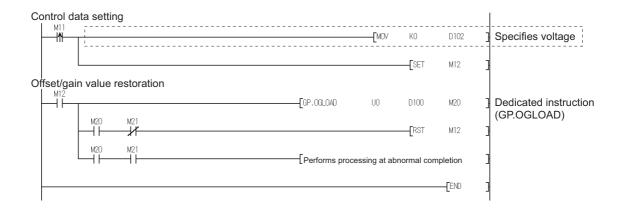


#### (2) Operation error

No errors.

#### (3) Program example

The following program is designed to read the offset/gain values of the A/D converter module mounted in the position of I/O number X/Y0 to X/YF when M11 is turned ON.

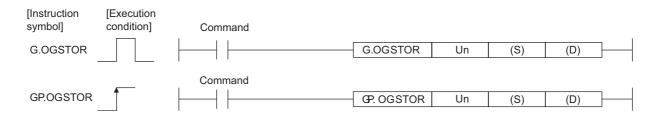


APPENDIX

## Appendix 1.3 G(P).OGSTOR

Restores the offset/gain values of the user range setting stored in the CPU to the A/D converter module.

Set data	Usable devices								
	Internal device			Link dire	ct device	Intelligent function Index	Constant		
	(System	ı, user)	File	J□\□			Index	Constant	
	Bit	Word	register	Bit	Word	module device U⊟\G□	register Z□	K, H	\$
(S)	_	(	)	_		_	_	_	
(D)	0				_	•	_	_	_



#### Set data

Set data	Description	Setting range	Data type
Un	Start I/O number of the module	0 to FEH	Binary 16 bits
(S)*1	Start number of the device in which control data is stored.	Within the range of the specified device	Device name
(D)	Device that is turned ON 1 scan on completion of dedicated instruction processing.  (D) + 1 also turns ON at an abnormal completion.	Within the range of the specified device	Bit

<sup>\*1</sup> When executing the G(P).OGLOAD instruction, specify the device designated in (S). Do not change the data read with the G(P).OGLOAD instruction. If it is changed, normal operation cannot be guaranteed.



#### Control data of Q68AD-G

Device	ltem	Set data	Setting range	Set by
(S)	System area	_	_	_
(S) + 1	Completion status	Stores the status when the instruction is complete.  O : Normal completion  Other than 0: Abnormal completion	_	System
(S) + 2	Pass data classification setting	The value set to Pass data classification setting (S)+2 using the G.OGLOAD instruction is stored.  0: Voltage specified  1: Current specified  b15 b8 b7 b6 b5 b4 b3 b2 b1 b0  0 ~ 0 CH8 CH7 CH6 CH5 CH4 CH3 CH2 CH1	0000н to 00FF н	System
(S) + 3	System area	_	_	Custom
(S) + 4 (S) + 5	CH1 Industrial shipment settings offset value CH1 Industrial shipment settings gain value	_	_	System System
(S) + 6	CH2 Industrial shipment settings offset value		<u> </u>	System
$\frac{(S) + 7}{(S) + 7}$	CH2 Industrial shipment settings onset value	_		System
(S) + 8	CH3 Industrial shipment settings offset value	_	_	System
(S) + 9	CH3 Industrial shipment settings gain value	_	_	System
(S) + 10	CH4 Industrial shipment settings offset value	_	_	System
(S) + 11	CH4 Industrial shipment settings gain value	_	_	System
(S) + 12	CH5 Industrial shipment settings offset value	_	_	System
(S) + 13	CH5 Industrial shipment settings gain value	_	_	System
(S) + 14	CH6 Industrial shipment settings offset value	_	1	System
(S) + 15	CH6 Industrial shipment settings gain value	_		System
(S) + 16	CH7 Industrial shipment settings offset value	_	_	System
(S) + 17	CH7 Industrial shipment settings gain value	_	_	System
(S) + 18	CH8 Industrial shipment settings offset value	_	_	System
(S) + 19 (S) + 20	CH8 Industrial shipment settings gain value	_	_	System
(S) + 20 (S) + 21	CH1 user range settings offset value CH1 user range settings gain value	_	_	System System
(S) + 21	CH2 user range settings offset value		<u> </u>	System
(S) + 23		_	_	System
(S) + 24		_	_	System
` '	CH3 user range settings gain value	_	_	System
(S) + 26		_	_	System
(S) + 27	CH4 user range settings gain value	_	_	System
(S) + 28	CH5 user range settings offset value	_	_	System
(S) + 29	CH5 user range settings gain value	_	_	System
(S) + 30	CH6 user range settings offset value	_	_	System
(S) + 31	CH6 user range settings gain value	_	_	System
(S) + 32	CH7 user range settings offset value	_	_	System
(S) + 33	CH7 user range settings gain value	_	_	System
(S) + 34	CH8 user range settings offset value	_	_	System
(S) + 35	CH8 user range settings gain value	_	_	System

## Control data\*2 of Q66AD-DG

Device	ltem	Set data	Setting range	Set by
(S)	System area	<u> </u>	_	_
		Stores the status when the instruction is		
(S) + 1	Completion status	complete.		Custom
(0) . 1	Completion status	0 : Normal completion	_	System
		Other than 0: Abnormal completion		
(S) + 2	System area	_	_	
(S) + 3				
(S) + 4	CH1 Industrial shipment settings offset value	<u> </u>	_	System
(S) + 5	CH1 Industrial shipment settings gain value	<del>-</del>	_	System
(S) + 6	CH2 Industrial shipment settings offset value	<del>-</del>	_	System
(S) + 7	CH2 Industrial shipment settings gain value	<del>-</del>	_	System
(S) + 8	CH3 Industrial shipment settings offset value	_		System
(S) + 9	CH3 Industrial shipment settings gain value	_		System
(S) + 10	CH4 Industrial shipment settings offset value	_		System
(S) + 11	CH4 Industrial shipment settings gain value	_	1	System
(S) + 12	CH5 Industrial shipment settings offset value	_	_	System
(S) + 13	CH5 Industrial shipment settings gain value	_	_	System
(S) + 14	CH6 Industrial shipment settings offset value	_	_	System
(S) + 15	CH6 Industrial shipment settings gain value	_	_	System
(S) + 16				
to	System area	_	_	
(S) + 19				
(S) + 20	CH1 user range settings offset value	_	_	System
(S) + 21	CH1 user range settings gain value	_	_	System
(S) + 22	CH2 user range settings offset value	_	_	System
(S) + 23	CH2 user range settings gain value	_	_	System
(S) + 24	CH3 user range settings offset value	_	_	System
(S) + 25	CH3 user range settings gain value	_	_	System
(S) + 26	CH4 user range settings offset value	_	_	System
(S) + 27	CH4 user range settings gain value	_	_	System
(S) + 28	CH5 user range settings offset value	_	_	System
(S) + 29	CH5 user range settings gain value	_	_	System
(S) + 30	CH6 user range settings offset value	_	_	System
(S) + 31	CH6 user range settings gain value	_	_	System
(S) + 32				
to	System area	_	_	_
(S) + 35				

<sup>\*2</sup> Setting is not necessary. If setting is made, the offset/gain values will not be read properly.

#### (1) Functions

- (a) Restores the offset/gain values of the user range setting stored in the CPU to the A/D converter module.
- (b) There are two types of interlock signals for the G(P).OGSTOR instruction: the completion device (D) and the status display device at completion (D) + 1.

#### 1) Completion device

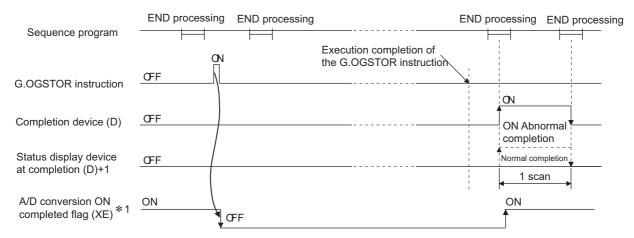
Turns ON in the END processing of the scan where the G(P).OGSTOR instruction is completed, and turns OFF in the next END processing.

#### 2) Status display device at completion

Turns ON and OFF depending on the completion status of the G(P).OGSTOR instruction.

Normal completion : Stays OFF and does not change.

Abnormal completion: Turns ON in the END processing of the scan where the G(P).OGSTOR instruction is completed, and turns OFF in the next END processing.



- \*1 When the G(P).OGSTOR instruction is executed, A/D conversion is not performed. After the completion device (D) turns ON, A/D conversion starts, the A/D conversion value is stored into the buffer memory, and the A/D conversion completed flag (XE) then turns ON.
- (c) When the offset/gain values are restored, the reference accuracy falls to about less than three times of the accuracy before that.

#### (2) Operation error

In any of the following cases, an error occurs and the corresponding error code is stored into the completion status area (S)+1.

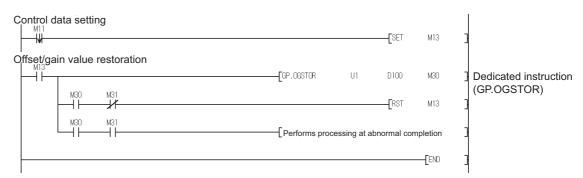
Error code Case resulting in operation error				
161	The G(P).OGSTOR instruction was executed in the offset/gain setting			
101	mode.			
162	The G(P).OGSTOR instruction was executed consecutively.			
	The G(P).OGSTOR instruction was executed for the model that differs			
163	from the model for which the G(P).OGLOAD instruction had been exe-			
	cuted.			

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**APPENDIX** 

#### (3) Program example

Program that restores the offset/gain setting to the A/D converter module mounted in the position of I/O No. X/Y10 to X/Y1F when M11 is turned OFF.





# **Appendix 2 Functions Added or Changed Due to Version Upgrade**

The A/D converter has been upgraded with new functions and specifications.

The functions available for use of the A/D converter module vary with the first five digits of product number.

Also, use the GX Configurator-AD whose version number is compatible one or later.

#### (1) Q68AD-G

App.1 Q68AD-G

		Applica			
Added or changed function	Description	First 5 digits of product number	GX Configurator- AD	Reference section	
Analog input range expanded mode	When the following input ranges are selected in the intelligent function module switch setting, the analog input ranges can be extended.  • 4 to 20mA (Expanded mode)  • 1 to 5V (Expanded mode)  The input ranges supported by the analog input range expanded mode cannot be used for products of incompatible version.	10052 or later	2.09K or later	Section3.1.1 Section4.5	
Input signal error detection extended setting	When the warning of input signal error detection is enabled, the input signal error detection can be performed by setting the same value of upper and lower limit or different value of upper and lower limit.  The input signal error detection cannot be performed by setting the different value of upper and lower limit, but can be performed by setting the same value of upper and lower limit.  Set the buffer memories as follows.  • Un\G47.b15 to b8 ··· Fixed at 0  • Un\G150 to Un\G157 ··· Reserved (system area)	10052 or later	2.09K or later	Section3.2.3 Section3.4.11 Section3.4.22	

# MELSEG Q series

## (2) Q66AD-DG

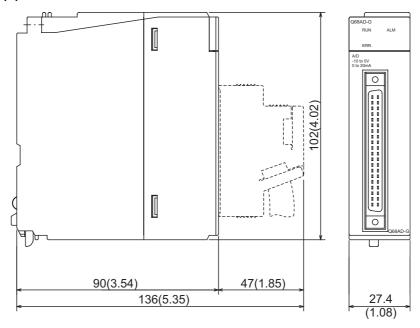
#### App.2 Q66AD-DG

		Applica			
Added or changed function	Description	First 5 digits of product number	GX Configurator- AD	Reference section	
Analog input range expanded mode	When the following input ranges are selected in the intelligent function module switch setting, the analog input ranges can be extended.  • 4 to 20mA (Expanded mode) (For 2-wire transmitter input)  • 4 to 20mA (Expanded mode) (For current input)  The input ranges supported by the analog input range expanded mode cannot be used for products of incompatible version.	10092 or later	2.09K or later	Section3.1.1 Section4.5	
Input signal error detection extended setting	When the warning of input signal error detection is enabled, the input signal error detection can be performed by setting the same value of upper and lower limit or different value of upper and lower limit.  The input signal error detection cannot be performed by setting the different value of upper and lower limit, but can be performed by setting the same value of upper and lower limit.  Set the buffer memories as follows.  • Un\G47.b15 to b6 ··· Fixed at 0  • Un\G150 to Un\G155 ··· Reserved (system area)	10092 or later	2.09K or later	Section3.2.3 Section3.4.11 Section3.4.22	



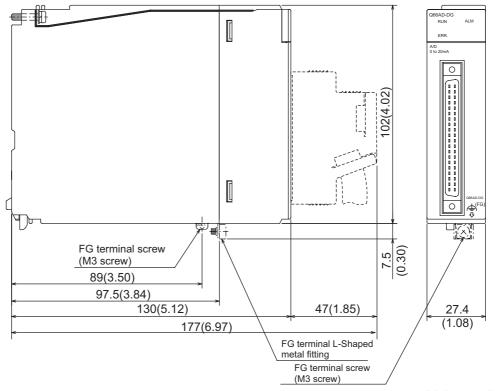
# **Appendix 3 External Dimensions**

## (1) Q68AD-G



(Unit: mm (inch))

#### (2) Q66AD-DG



(Unit: mm (inch))

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## **Warranty**

Please confirm the following product warranty details before using this product.

#### 1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company.

However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module.

[Gratis Warranty Term]

The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place.

Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.

[Gratis Warranty Range]

- (1) The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
- (2) Even within the gratis warranty term, repairs shall be charged for in the following cases.
  - 1. Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
  - 2. Failure caused by unapproved modifications, etc., to the product by the user.
  - 3. When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
  - 4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
  - 5. Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
  - 6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
  - 7. Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.

#### 2. Onerous repair term after discontinuation of production

- (1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued.
  - Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.
- (2) Product supply (including repair parts) is not available after production is discontinued.

#### 3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.

#### 4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation of damages caused by any cause found not to be the responsibility of Mitsubishi, loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products, special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products, replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

#### 5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

#### 6. Product application

- (1) In using the Mitsubishi MELSEC programmable controller, the usage conditions shall be that the application will not lead to a major accident even if any problem or fault should occur in the programmable controller device, and that backup and fail-safe functions are systematically provided outside of the device for any problem or fault.
- (2) The Mitsubishi programmable controller has been designed and manufactured for applications in general industries, etc. Thus, applications in which the public could be affected such as in nuclear power plants and other power plants operated by respective power companies, and applications in which a special quality assurance system is required, such as for Railway companies or Public service purposes shall be excluded from the programmable controller applications.
  - In addition, applications in which human life or property that could be greatly affected, such as in aircraft, medical applications, incineration and fuel devices, manned transportation, equipment for recreation and amusement, and safety devices, shall also be excluded from the programmable controller range of applications.

However, in certain cases, some applications may be possible, providing the user consults their local Mitsubishi representative outlining the special requirements of the project, and providing that all parties concerned agree to the special circumstances, solely at the users discretion.

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Channel Isolated Analog-Digital Converter Module Channel Isolated Analog-Digital Converter Module (With Signal Conditioning Function)

User's Manual

MODEL	Q-A/D-(D)G-U-SY-E
MODEL CODE	13JR96
SH(NA)-080647ENG-E(0810)MEE	



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